

SOMETHING WITH CONNECTED COMPONENTS

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ABSTRACT

We present an improved parallel implementation of a tree based connected components algorithm originally introduced by U. Vishkin[1]. To improve the algorithm's performance the graph's edges are distributed across multiple cores. Each `coreprocess` computes in a lock-free manner a local forest representing the different connected components. The resulting forests are then combined using a reduction. A comparison of our algorithm against a communication avoiding algorithm proposed by Lukas Gianinazzi et al.[2] on a set of large graphs (5×10^8 Edges with up to 2×10^7 Vertices) is presented. Results where our algorithm uses a mixture of both MPI and OMP are also shown.

1. INTRODUCTION

Motivation. Problems in computer science are often modeled as graphs. Therefore, graph algorithms are ubiquitous. One of these graph problems is finding connected components. It is a well understood problem in graph theory with a variety of applicable domains. Computer vision tasks, such as pattern recognition and image segmentation [3] can make use of connected components [4]. Other fields are medical imaging [5] and image processing [6]. The related problem of strongly connected components will not be discussed in this paper.

Related Work. The first sequential algorithm to solve the connected components problem goes back to [7]. Parallel approaches were presented in [8] and [9]. Recently [2] was published, where a communication-avoiding approach was discussed. A communication-avoiding algorithm uses asymptotically less communication. By doing so [2] sacrifices some computational efficiency as the root node does most of the work. In this paper we present an algorithm which distributes the work while still avoiding as much communication as possible. This is achieved by distributing the list of edges evenly among different MPI ranks. These then locally compute their corresponding connected components which are represented as a forest. In a next step the algorithm reduces these forests in a binary manner. Two MPI ranks compare and merge their results and then compress

them. This step is repeated until the final result is propagated to the root process.

Connected components.

For an undirected graph $G = (V, E)$, the connected components are the ensemble of connected subgraphs, where connected means that for any two vertices there exists a path along the edges connecting them. A straightforward algorithm to find the connected components is to perform either a breath or depth first search starting from a random vertex in V , and give the same label to all vertices reached. The search is then repeated, starting from an unlabeled vertex. This has a cost in terms of memory accesses of $\Theta(|E| + |V|)$, which turns out to be optimal [7].

2. PROPOSED ALGORITHM

Unfortunately this algorithm does not parallelize in a straightforward way. Instead we first implemented an algorithm proposed by Uzi Vishkin [1] and later described in a class by Pavel Tvrđik [10]. This algorithm casts the problem in terms of the generation of a forest, where the vertices of the same connected component belong to the same tree, and its root can be used as the representative.

We define a star as a tree of height one, a singleton as a tree with a single element, and use the variables $n = |V|$ and $m = |E|$. The algorithm can be summarized as:

Algorithm 1 Pavel Tvrđik's Connected components

```
1: procedure HOOK( $i, j$ )
2:    $p[p[i]] = p[j]$ 
3: end procedure
4: procedure CONNECTEDCOMPONENTS( $n, \text{edges}$ )
5:    $p[i] = i \quad \forall i \in \{1, \dots, n\}$ .  $\triangleright$  Initialize a list of
     parents.
6:   while Elements of  $p$  are changed. do
7:     for  $\langle i, j \rangle \in \text{edges}$  do  $\triangleright$  Execute in parallel.
8:       if  $i \geq j$  then HOOK( $i, j$ )
9:       if isSingleton( $i$ ) then HOOK( $i, j$ )
10:    end for
11:    for  $\langle i, j \rangle \in \text{edges}$  do  $\triangleright$  Execute in parallel.
12:      if isStar( $i$ ) and  $i \neq j$  then HOOK( $i, j$ )
13:    end for
14:     $p[i] = \text{root}(i) \quad \forall i \in \{1, \dots, n\}$   $\triangleright$  Compress
     the forest in parallel.
15:  end while
16: end procedure
```

We defer to [10] for a proof of correctness.

After implementing this algorithm we found it advantageous to remove the constraint that only singletons and stars can be hooked to another vertex. This allows the algorithm to terminate after a single pass through the edge list, so that only a single pass through the edge list is required. Extra care is then required during parallel execution: as each vertex only has one outgoing connection, we need to avoid that a process overwrites a connection that has been formed by another one. When hooking vertex v_1 to v_2 it must hold that: We therefore need to grow our forest with the following rules:

1. $v_1.id > v_2.id$ A hook must originate from a vertex id larger than the destination.
2. All edges must generate a connection between the relative vertices, or vertices at a higher level in their tree.
3. v_1 must be a root. A hook must originate from a vertex that is currently the root of a tree.

An intuitive proof of correctness follows: rule 1 means that the graph generated by the hooks is a directed graph without cycles and each vertex has at most one outgoing connection. Therefore, it must be a forest. Rules 2 and 3 enforce that after processing an edge between two nodes, they belong to the same tree. Rule 23 guarantees that this connection cannot be broken by a different edge. After the algorithm terminates all vertices in a tree belong to the same connected component. The connected component can be canonically represented by the tree's root. At the end of the algorithm, by following the connections from

each vertex to the root, we can find a representative for each connected component.

To implement rule 23 in a multi-threaded environment we use an atomic compare and swap is used. We compare the parent of the hook's origin with its id. If they match it means the vertex is still a root and we hook it to its destination.¹ For correctness it does not matter if the destination is a root, but doing so minimizes the tree height². Empirically we found We found empirically that using `std::atomic_compare_exchange` offers better performance compared to `std::atomic_compare_exchange`.

In pseudocode our algorithm is:

Algorithm 2 Single pass connected component.

```
1: procedure CONNECTEDCOMPONENTS( $n, \text{edges}$ )
2:    $p[i] = i \quad \forall i \in \{1, \dots, n\}$ .
3:   for  $\langle i, j \rangle \in \text{edges}$  do  $\triangleright$  Execute in parallel.
4:     while hook is not successful. do
5:        $\text{from} = \max(\text{root}(i), \text{root}(j))$ 
6:        $\text{to} = \min(\text{root}(i), \text{root}(j))$ 
7:        $\text{atomicHook}(\text{from}, \text{to})$ 
8:     end while
9:     if !isRoot( $i$ ) then  $p[i] = \text{root}(i)$ 
10:    if !isRoot( $j$ ) then  $p[j] = \text{root}(j)$ 
11:  end for
12:   $p[i] = \text{root}(i) \quad \forall i \in \{1, \dots, n\}$   $\triangleright$  Compress the
     forest in parallel.
13: end procedure
```

While step 9 is not necessary for correctness, we found that reusing the already computed vertex's representative leads to a smaller tree height. This and the parallel compression works and was tested to be efficient only on architectures such as x86, where writes to 32 or 64 bits, used to store a vertex's id, are atomic.³

We^R to implement tried implementing the parallel execution of loop 3 with Boost fibers [11] whose execution is scheduled with a work stealing algorithm, and OpenMP with a dynamic scheduler. OpenMP performed better by a large margin and and therefore was and thus was^R used to acquire the data presented later.

The algorithm's overall cost The overall cost of the algorithm is $\Theta((n + m)\langle H \rangle)$, where $\langle H \rangle$ is the average tree height. Therefore $\langle H \rangle = \Theta(1)$ for a sub-critical random graph, and on average (depending on relatively to the execution order of the loop) $\langle H \rangle = \Theta(\log(n))$ for a supercritical random graph [12].

Multiple compute nodes. The^R algorithm works only on a single compute node with a shared memory model. Moreover, it is efficient only when the graph is sparse enough relatively

¹this is confusing

²redundant as it is mentioned below

³what is this trying to say?

~~sparse so~~ that the ~~probability~~chance of a collision between two processors trying to update the same parent is low.

We propose to extend our algorithm by distributing the list of edges evenly among each MPI ~~rank~~process. Then each one of them computes a forest ~~using~~used only the subset of edges it received. This local computation is followed by a reduction step, ~~where pairs of MPI ranks combine their respective forests, where the list of representatives is sent to another process, which confront it with its own. If a discrepancy is detected, a hook is inserted between the two different parents.~~⁴ The resulting forest is ~~then~~ compressed ~~again~~ before the following reduction step.

Using p processes, the total execution time of this extension scales as $\Theta(\frac{(m+n)}{p} \langle H \rangle + n \log p)$.

On top of allowing to scale past a single compute node, this approach is advantageous on dense graphs: if the reduction cost is negligible, the scaling is the same as algorithm 2 executed on a single node, but we can avoid the cost of performing atomic hooks, if a single thread is used, or limit the number of failures if a few threads are used. Therefore a different mixture of MPI ranks and OpenMP threads per rank is advised depending on the density of the graph.

Distributed vertices. While the described approach works on generic graphs, it performs poorly on very sparse graphs using a large number of compute nodes. Moreover, the full set of ~~vertices~~vertex-id's^R must fit in memory, limiting the graph size to 8 billion vertices. If the connectivity of a graph the size of a human brain needs to be studied, we propose to distribute the representation of the vertices as well.

Often, real world graphs are embedded on a space with some metric, and connections ~~occur~~are present much more frequently between vertices that are close together. For example the pixel representing features of ~~an image~~a picture, or the roads connecting cities with a known geographical position, ~~posses this property~~^{R5}.

We represent this type of graph with a very simple model: a two dimensional lattice with random connections between nearest neighbors only. We split the lattice in as many square tiles as there are processes. Then each process applies algorithm 2 ~~to~~with the subset of edges connecting two vertices in ~~its~~their-own tile. Finally we process the boundary edges(~~edges connecting tiles~~), ~~connecting vertices of different tiles~~, ~~using~~with MPI one sided communication. The list ~~local vertex id~~s of ~~ids of the local vertices~~^{R6} is stored in an MPI window, so that the representative of a remote vertex can be obtained with MPI_Get, while a hook can be created with MPI_Compare_and_swap. Therefore only two global synchronization points are necessary: after all edges have been processed, and after the final compression

of the forest.

Unfortunately, due to time constraints ~~in developing, in our implementation~~ each MPI operation is synchronized locally ~~in our implementation~~. This leads to good scaling results only on extremely sparse graphs. ~~Future work should consider batching several MPI requests before synchronization is required.~~⁷

3. EXPERIMENTAL RESULTS

To evaluate our algorithm's performance a number of experiments were run on both the Euler and the Piz Daint cluster. In the following paragraphs we will first describe both the Euler and the Piz Daint setups. We will then go on to discussing each experiment ~~seperately~~^R.

Euler setup. Each node in the Euler V cluster contains two 12-core Intel Xeon Gold 5118 processors and 96 GB of DDR4 memory clocked at 2400 MHz [13]. We were allowed to use up to two nodes, giving us a maximum of 48 cores. The algorithm was compiled and run using gcc 6.2.3 and Open MPI 3.0.0.

Piz Daint setup. Each of the utilized XC40 nodes on Piz Daint contains two Intel Xeon E5-2695, each with 18 hardware threads, and ~~still missing, just so we dont forgett~~^R

Graph Generation. Our algorithm was evaluated on undirected, unweighted graphs. Multiple edges connecting the same two vertices and self loops were not allowed. All graphs were generated using [14] ~~to generate the same type of graphs as used in [2].~~^R.

MPI vs OMP vs Communication Avoiding.

⁴this should either be explained in more detail or left out

⁵R: I would not delete this part as it now is not a correct sentence anymore

⁶R: I would not change this one

⁷belongs in future work section

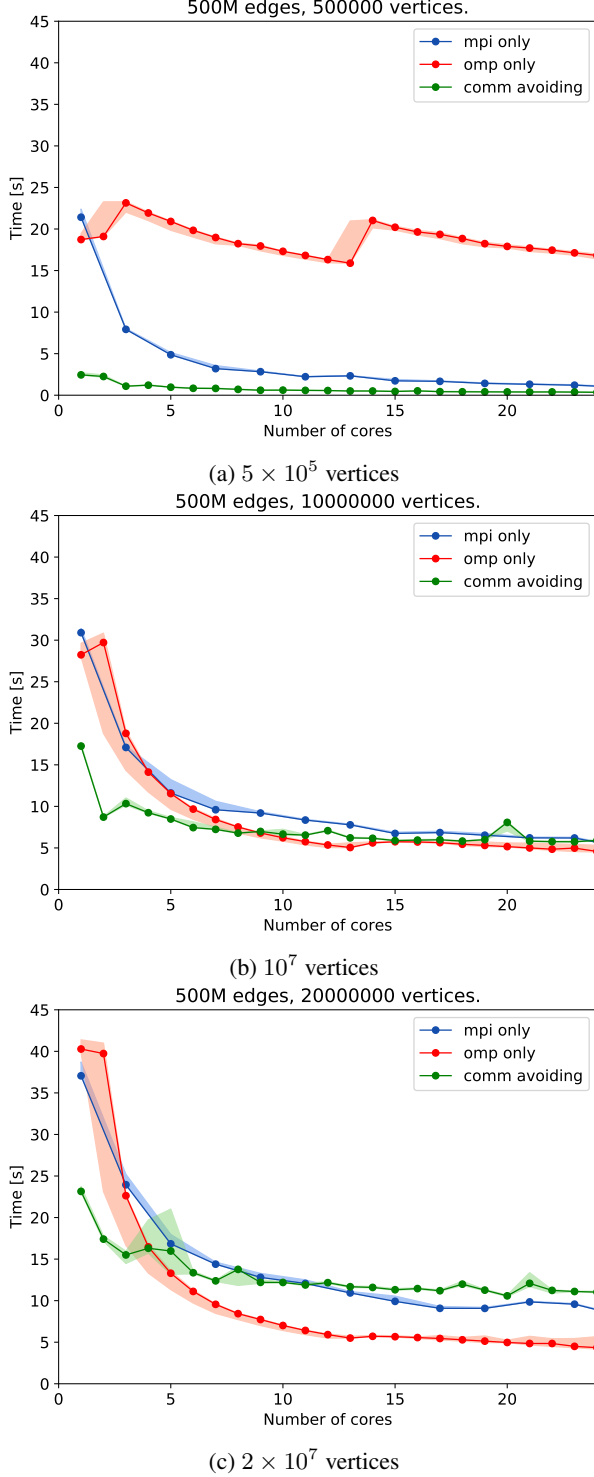


Fig. 1: Comparison of the total runtime of our algorithm with the communication avoiding algorithm [2] over three different graphs each with 5×10^8 edges and $5 \times 10^5, 1 \times 10^7, 2 \times 10^7$ vertices. The experiment was run on the Euler cluster.

The results in Figure 1 show our algorithm compared with the communication avoiding algorithm [2] on three different graphs with the same number of edges but different densities. Our algorithm was run in MPI and OMP only mode.

Figure 1a shows the MPI ⁸ version outperforming the OMP version on the densest graph. This can be explained by the combination of two effects. ⁹: first ^R that a dense graph results in more contention between the OMP threads during the edge contractions, and the second ^R is being ^R that the reduction afterwards after the contractions ^R scales linearly with the number of vertices. Since the number of vertices is comparatively low in a dense graph the reduction is fast. For the OMP ^R version we further observe a significant increase in total compute time from one to two cores and from 12 to 13 cores. The first jump can be explained by the initial overhead of doing the computation in parallel. Since a single CPU on Euler has 12 cores the second jump is a result of the cache coherency protocol being slow across multiple CPUs.

The results in Figure 1b and Figure 1c were obtained using sparser graphs compared to Figure 1a. Here, for a large number of cores, the OMP ^R version is clearly faster than the MPI ^R version. Figure 1 shows a trend of the OMP ^R version speeding up as the graph becomes sparser, while the MPI ^R version slows down. The OMP ^R version's speed up can be explained by ^R less the reduced ^R contention between the OMP threads due to the sparser graph. The MPI ^R version's slow down is a result of the ^R longer increased ^R reduction time due to the ^R larger increasing ^R number of vertices.

The results in Figure 1 show the communication avoiding algorithm scaling badly with the number of cores. This is expected since the edge contractions are computed on a single node ^R in this algorithm ^R. Since our algorithm does to scale with the number of cores up to some point we manage to outperform the communication avoiding algorithm on each graph.

Results on Piz Daint.

⁸R: I deleted all the onlys. I think it hurts the flow.

⁹R: long sentence split in two

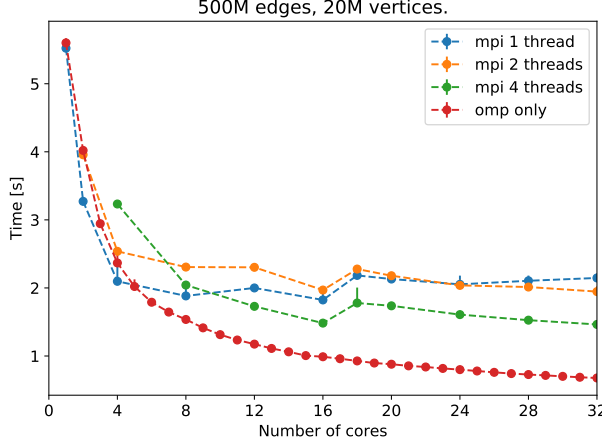


Fig. 3: Piz Daint results on graph with 5×10^8 edges and 2×10^7 vertices.

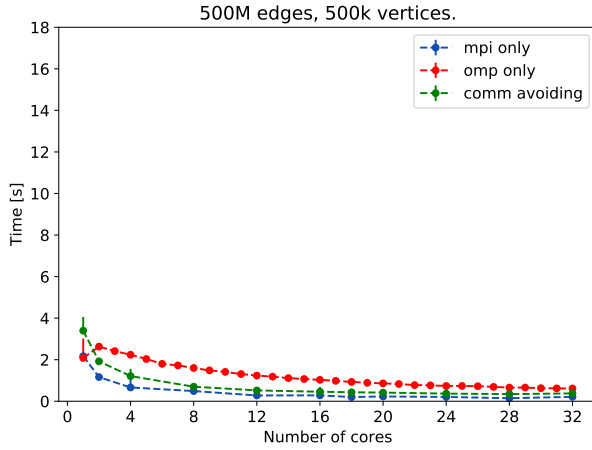


Fig. 2: Piz Daint results on graph with 5×10^8 edges and 5×10^5 vertices.

Additionally we tested our algorithm on the Piz Daint cluster. In addition to testing our algorithm on the Euler cluster it was also tested on the Piz Daint cluster^R. The graphs used were the same as in the experiments run on Euler. We will now discuss interesting differences in the results. Figure 2 shows the results for the densest graph on Piz Daint. The OMP *only*^R version behaves differently compared to Euler. We still observe a decrease in performance when going from one to two cores. The decrease is not as severe as on Euler. The performance decrease as we go from one to multiple CPUs is no longer present. This can be explained by Daint's cache coherency protocol being more efficient, especially across multiple CPUs. Figure 3 shows the Piz Daint results for different mixtures of MPI and OMP on the sparsest graph. As on Euler, we do see the OMP *only*^R version performing best for a large number of cores. For all versions, except the OMP *only*^R version, we can clearly see the

effect the number of reduction steps has on the runtime. It is most noticeable as we go from 16 to 18 cores. Here each version has to perform an additional reduction step which results in the increased runtime.

Speedups.

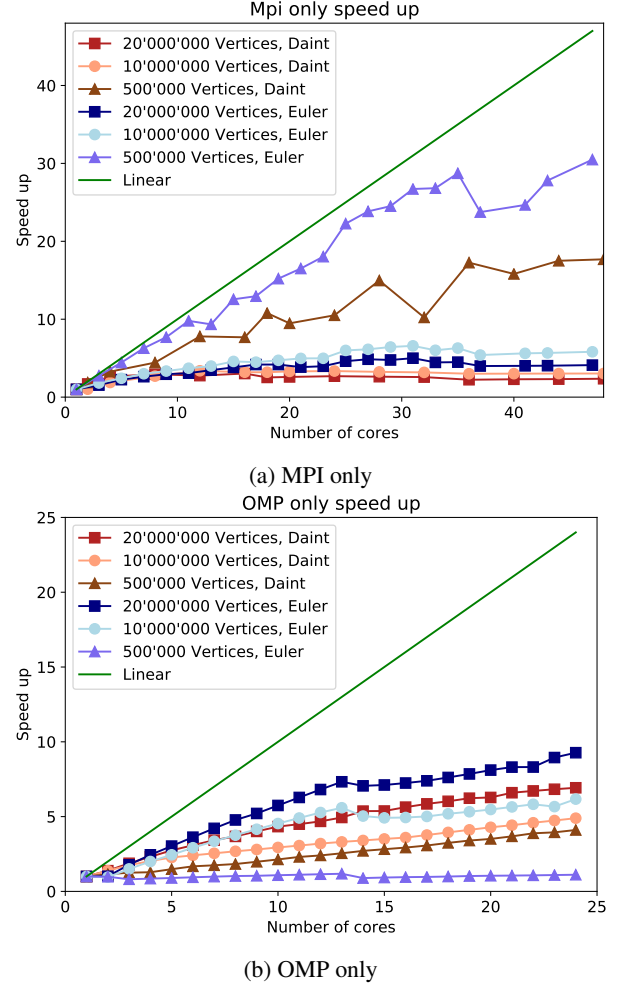


Fig. 4: Speedup of MPI only and OMP only version on three different graphs each with 5×10^8 edges.

Figure 4 shows the measured speed up of the MPI *only*^R and OMP *only*^R version. As one would expect from the results discussed previously the MPI *only*^R version achieves better scaling on dense graphs while the OMP *only*^R version scales better on sparse graphs. We can also see that our algorithm achieves better scaling on Euler for the MPI *only*^R version. The reason our algorithm scales better on Euler is that the single core performance is lower on Euler compared to Daint. This means that the edge contractions dominate the runtime for longer before the reduction step starts to be an issue. For the OMP *only*^R version the *multipliedifferences*^R in speed ups can be explained by the differences in the shared mem-

ory architecture.

Results.

4. CONCLUSIONS

The proposed algorithm manages to take advantage of the parallelism available within shared memory units while avoiding excessive communication between them. By choosing the right number of OMP threads per MPI rank the algorithm achieves good scaling across a variety of graphs. While on dense graphs the cE^R ommunication aA^R voiding algorithm [2] yields better results, it is outperformed by our algorithm on sparser graphs.

5. FUTURE WORK

The main drawback of our algorithm is the reduction step's runtime of $O(n \cdot \log(p))$, where p is the number of MPI ranks and n is the number of vertices. For a large number of MPI ranks the $\log(p)$ factor becomes an issue.

$DecreasingReduceing^R$ the reductions $critical\ path\ height^R$ addresses this problem. In our algorithm this is done by using a mixture of OMP and MPI which reduces the number of MPI ranks. Since OMP does not scale well once the number of OMP threads exceeds the number of cores per CPU this approach is only viable up to a limited number of cores. Another approach would be reducing the work n^R done in each reduction step. Due to time constraints we were unable to investigate this approach. One could imagine a more efficient hook tree representation solving this problem.

Another drawback of our algorithm is that in order to achieve satisfying performance one needs to find the right mixture of MPI and OMP. While we analysed the behaviour of different mixtures on graphs with varying density we did not come up with an a priori scheme to determine the right mixture. A good heuristic or even a scheme to find the optimal mixture would be worth exploring.

6. REFERENCES

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