

Figure 1 Convert VHDL to a graphic symbol

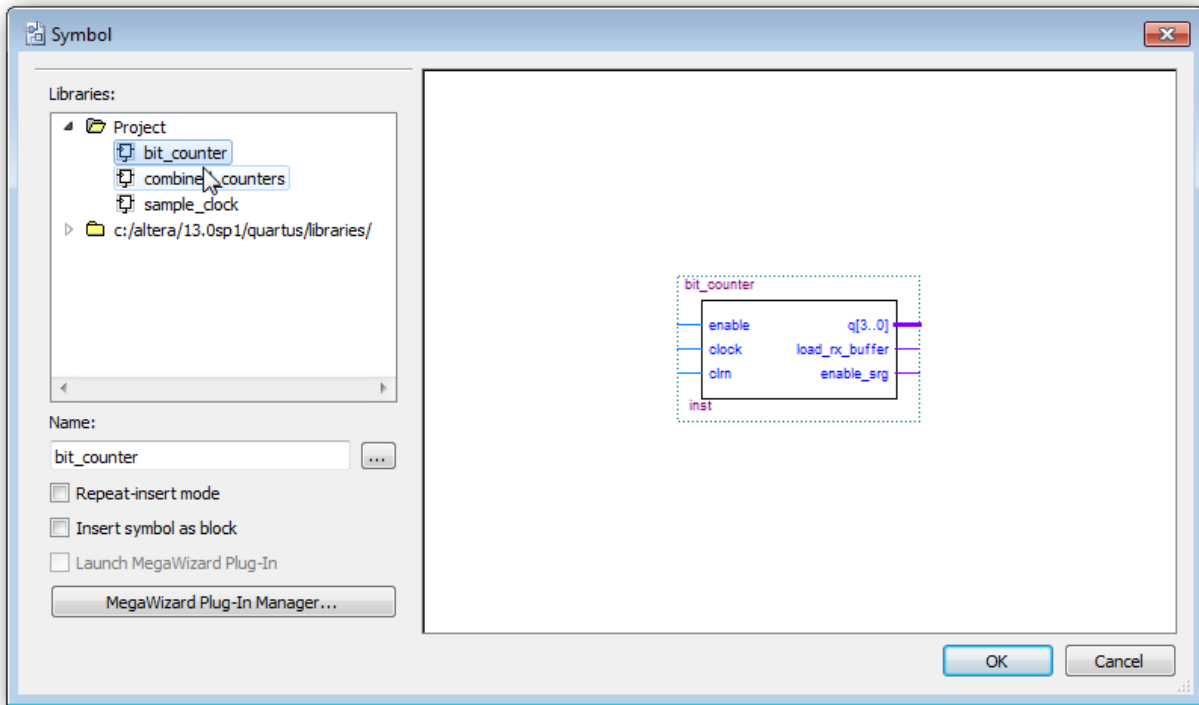


Figure 2 Selecting a graphic symbol

Place symbol in a blank Block Diagram File

Set the BDF to top level of hierarchy.

