Cianal Name	EDCA Din No	Description
Signal Name	FPGA Pin No.	Description  DEST Contact (2)
SW[0]	PIN_N25	DPDT Switch[0]
SW[1]	PIN_N26	DPDT Switch[1]
SW[2]	PIN_P25	DPDT Switch[2]
SW[3]	PIN_AE14	DPDT Switch[3]
SW[4]	PIN_AF14	DPDT Switch[4]
SW[5]	PIN_AD13	DPDT Switch[5]
SW[6]	PIN_AC13	DPDT Switch[6]
SW[7]	PIN_C13	DPDT Switch[7]
SW[8]	PIN_B13	DPDT Switch[8]
SW[9]	PIN_A13	DPDT Switch[9]
SW[10]	PIN_N1	DPDT Switch[10]
SW[11]	PIN_P1	DPDT Switch[11]
SW[12]	PIN_P2	DPDT Switch[12]
SW[13]	PIN_T7	DPDT Switch[13]
SW[14]	PIN_U3	DPDT Switch[14]
SW[15]	PIN_U4	DPDT Switch[15]
SW[16]	PIN_V1	DPDT Switch[16]
SW[17]	PIN_V2	DPDT Switch[17]
Signal Name	FPGA Pin No.	Description CDDAM Address [6]
DRAM_ADDR[0]	PIN_T6	SDRAM Address[0]
DRAM_ADDR[1]	PIN_V4	SDRAM Address[1]
DRAM_ADDR[2]	PIN_V3	SDRAM Address[2]
DRAM_ADDR[3]	PIN_W2	SDRAM Address[3]
DRAM_ADDR[4]	PIN_W1	SDRAM Address[4]
DRAM_ADDR[5]	PIN_U6	SDRAM Address[5]
DRAM_ADDR[6]	PIN_U7	SDRAM Address[6]
DRAM_ADDR[7]	PIN_U5	SDRAM Address[7]
DRAM_ADDR[8]	PIN_W4	SDRAM Address[8]
DRAM_ADDR[9]	PIN_W3	SDRAM Address[9]
DRAM_ADDR[10]	PIN_Y1	SDRAM Address[10]
DRAM_ADDR[11]	PIN_V5	SDRAM Address[11]
DRAM_DQ[0]	PIN_V6	SDRAM Data[0]
DRAM_DQ[1]	PIN_AA2	SDRAM Data[1]
DRAM_DQ[2]	PIN_AA1	SDRAM Data[2]
DRAM_DQ[3]	PIN_Y3	SDRAM Data[3]
DRAM_DQ[4]	PIN_Y4	SDRAM Data[4]
DRAM_DQ[5]	PIN_R8	SDRAM Data[5]
DRAM_DQ[6]	PIN_T8	SDRAM Data[6]
DRAM_DQ[7]	PIN_V7	SDRAM Data[7]
DRAM_DQ[8]	PIN_W6	SDRAM Data[8] SDRAM Data[9]
DRAM_DQ[9]	PIN_AB2 PIN_AB1	SDRAM Data[9] SDRAM Data[10]
DRAM_DQ[10] DRAM_DQ[11]	PIN_AA4	SDRAM Data[10] SDRAM Data[11]
DRAM_DQ[12]	PIN AA3	SDRAM Data[11]
DRAM_DQ[12]	PIN_AC2	SDRAM Data[12]
DRAM_DQ[14]	PIN_AC2 PIN_AC1	SDRAM Data[13]
DRAM_DQ[14]	PIN_AA5	SDRAM Data[14]
DRAM_BA_0	PIN_AE2	SDRAM Bank Address[0]
DRAM_BA_1	PIN_AE3	SDRAM Bank Address[1]
DRAM_LDQM	PIN_AD2	SDRAM Low-byte Data Mask
DRAM_UDQM	PIN_Y5	SDRAM High-byte Data Mask
DRAM_RAS_N	PIN_AB4	SDRAM Row Address Strobe
DRAM_CAS_N	PIN_AB3	SDRAM Column Address Strobe
DRAM_CKE	PIN_AA6	SDRAM Clock Enable
DRAM_CLK	PIN_AA7	SDRAM Clock
DRAM_WE_N	PIN_AD3	SDRAM Write Enable
DRAM_CS_N	PIN_AC3	SDRAM Chip Select
Signal Name	FPGA Pin No.	Description Description
FL_ADDR[0]	PIN_AC18	FLASH Address[0]
FL_ADDR[1]	PIN_AB18	FLASH Address[1]
FL_ADDR[2]	PIN_AE19	FLASH Address[2]
FL_ADDR[3]	PIN_AF19	FLASH Address[3]
FL_ADDR[4]	PIN_AE18	FLASH Address[4]
		. L/OII/(ddi000[T]

FL_ADDR[5]	PIN_AF18	FLASH Address[5]
FL_ADDR[6]	PIN_Y16	FLASH Address[6]
FL_ADDR[7]	PIN_AA16	FLASH Address[7]
FL_ADDR[8]	PIN_AD17	FLASH Address[8]
FL_ADDR[9]	PIN_AC17	FLASH Address[9]
FL_ADDR[10]	PIN AE17	FLASH Address[10]
	_	
FL_ADDR[11]	PIN_AF17	FLASH Address[11]
FL_ADDR[12]	PIN_W16	FLASH Address[12]
FL_ADDR[13]	PIN_W15	FLASH Address[13]
FL_ADDR[14]	PIN_AC16	FLASH Address[14]
FL_ADDR[15]	PIN_AD16	FLASH Address[15]
FL_ADDR[16]	PIN_AE16	FLASH Address[16]
FL_ADDR[17]	PIN_AC15	FLASH Address[17]
FL_ADDR[18]	PIN_AB15	FLASH Address[18]
FL_ADDR[19]	PIN_AA15	FLASH Address[19]
FL_DQ[0]	PIN_AD19	FLASH Data[0]
FL_DQ[1]	PIN_AC19	FLASH Data[1]
FL_DQ[2]	PIN_AF20	FLASH Data[1]
	_	
FL_DQ[3]	PIN_AE20	FLASH Data[3]
FL_DQ[4]	PIN_AB20	FLASH Data[4]
FL_DQ[5]	PIN_AC20	FLASH Data[5]
FL_DQ[6]	PIN_AF21	FLASH Data[6]
FL_DQ[7]	PIN_AE21	FLASH Data[7]
FL_CE_N	PIN_V17	FLASH Chip Enable
FL_OE_N	PIN_W17	FLASH Output Enable
FL_RST_N	PIN_AA18	FLASH Reset
FL_WE_N	PIN_AA17	FLASH Write Enable
Signal Name	FPGA Pin No.	Description
SRAM_ADDR[0]	PIN_AE4	SRAM Address[0]
	PIN_AF4	SRAM Address[1]
SRAM_ADDR[1]	_	
SRAM_ADDR[2]	PIN_AC5	SRAM Address[2]
SRAM_ADDR[3]	PIN_AC6	
	=	SRAM Address[3]
SRAM_ADDR[4]	PIN_AD4	SRAM Address[3] SRAM Address[4]
SRAM_ADDR[4] SRAM_ADDR[5]	=	
SRAM_ADDR[4]	PIN_AD4	SRAM Address[4]
SRAM_ADDR[4] SRAM_ADDR[5]	PIN_AD4 PIN_AD5	SRAM Address[4] SRAM Address[5]
SRAM_ADDR[4] SRAM_ADDR[5] SRAM_ADDR[6]	PIN_AD4 PIN_AD5 PIN_AE5	SRAM Address[4] SRAM Address[5] SRAM Address[6]
SRAM_ADDR[4] SRAM_ADDR[5] SRAM_ADDR[6] SRAM_ADDR[7] SRAM_ADDR[8]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5	SRAM Address[4] SRAM Address[5] SRAM Address[6] SRAM Address[7]
SRAM_ADDR[4] SRAM_ADDR[5] SRAM_ADDR[6] SRAM_ADDR[7] SRAM_ADDR[8] SRAM_ADDR[9]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7	\$RAM Address[4]  \$RAM Address[5]  \$RAM Address[6]  \$RAM Address[7]  \$RAM Address[8]  \$RAM Address[9]
SRAM_ADDR[4] SRAM_ADDR[5] SRAM_ADDR[6] SRAM_ADDR[7] SRAM_ADDR[8] SRAM_ADDR[9] SRAM_ADDR[10]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10	SRAM Address[4] SRAM Address[5] SRAM Address[6] SRAM Address[7] SRAM Address[8] SRAM Address[9] SRAM Address[10]
SRAM_ADDR[4] SRAM_ADDR[5] SRAM_ADDR[6] SRAM_ADDR[7] SRAM_ADDR[8] SRAM_ADDR[9] SRAM_ADDR[10] SRAM_ADDR[11]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9	SRAM Address[4]  SRAM Address[5]  SRAM Address[6]  SRAM Address[7]  SRAM Address[8]  SRAM Address[9]  SRAM Address[10]  SRAM Address[11]
SRAM_ADDR[4] SRAM_ADDR[5] SRAM_ADDR[6] SRAM_ADDR[7] SRAM_ADDR[8] SRAM_ADDR[9] SRAM_ADDR[10] SRAM_ADDR[11] SRAM_ADDR[12]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7	SRAM Address[4]  SRAM Address[5]  SRAM Address[6]  SRAM Address[7]  SRAM Address[8]  SRAM Address[9]  SRAM Address[10]  SRAM Address[11]  SRAM Address[12]
SRAM_ADDR[4] SRAM_ADDR[5] SRAM_ADDR[6] SRAM_ADDR[7] SRAM_ADDR[8] SRAM_ADDR[9] SRAM_ADDR[10] SRAM_ADDR[11] SRAM_ADDR[12] SRAM_ADDR[13]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8	SRAM Address[4]  SRAM Address[5]  SRAM Address[6]  SRAM Address[7]  SRAM Address[8]  SRAM Address[9]  SRAM Address[10]  SRAM Address[11]  SRAM Address[12]  SRAM Address[13]
SRAM_ADDR[4] SRAM_ADDR[5] SRAM_ADDR[6] SRAM_ADDR[7] SRAM_ADDR[8] SRAM_ADDR[9] SRAM_ADDR[10] SRAM_ADDR[11] SRAM_ADDR[12] SRAM_ADDR[13] SRAM_ADDR[14]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10	SRAM Address[4]  SRAM Address[5]  SRAM Address[6]  SRAM Address[7]  SRAM Address[8]  SRAM Address[9]  SRAM Address[10]  SRAM Address[11]  SRAM Address[12]  SRAM Address[13]  SRAM Address[14]
SRAM_ADDR[4] SRAM_ADDR[5] SRAM_ADDR[6] SRAM_ADDR[7] SRAM_ADDR[8] SRAM_ADDR[9] SRAM_ADDR[10] SRAM_ADDR[11] SRAM_ADDR[12] SRAM_ADDR[13] SRAM_ADDR[14] SRAM_ADDR[14]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_W10	SRAM Address[4]  SRAM Address[5]  SRAM Address[6]  SRAM Address[7]  SRAM Address[8]  SRAM Address[9]  SRAM Address[10]  SRAM Address[11]  SRAM Address[12]  SRAM Address[13]  SRAM Address[14]  SRAM Address[15]
SRAM_ADDR[4] SRAM_ADDR[5] SRAM_ADDR[6] SRAM_ADDR[7] SRAM_ADDR[8] SRAM_ADDR[9] SRAM_ADDR[10] SRAM_ADDR[11] SRAM_ADDR[12] SRAM_ADDR[12] SRAM_ADDR[14] SRAM_ADDR[14] SRAM_ADDR[15] SRAM_ADDR[16]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_W10 PIN_W10 PIN_W10 PIN_W10 PIN_AB8	SRAM Address[4]  SRAM Address[5]  SRAM Address[6]  SRAM Address[7]  SRAM Address[8]  SRAM Address[9]  SRAM Address[10]  SRAM Address[11]  SRAM Address[12]  SRAM Address[13]  SRAM Address[14]  SRAM Address[15]  SRAM Address[16]
SRAM_ADDR[4]  SRAM_ADDR[5]  SRAM_ADDR[6]  SRAM_ADDR[7]  SRAM_ADDR[8]  SRAM_ADDR[9]  SRAM_ADDR[10]  SRAM_ADDR[11]  SRAM_ADDR[12]  SRAM_ADDR[13]  SRAM_ADDR[14]  SRAM_ADDR[15]  SRAM_ADDR[15]  SRAM_ADDR[16]  SRAM_ADDR[17]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_W10 PIN_W10 PIN_Y10 PIN_AB8 PIN_AC8	SRAM Address[4]  SRAM Address[5]  SRAM Address[6]  SRAM Address[7]  SRAM Address[8]  SRAM Address[9]  SRAM Address[10]  SRAM Address[11]  SRAM Address[12]  SRAM Address[13]  SRAM Address[14]  SRAM Address[15]  SRAM Address[15]  SRAM Address[16]  SRAM Address[17]
SRAM_ADDR[4]  SRAM_ADDR[5]  SRAM_ADDR[6]  SRAM_ADDR[7]  SRAM_ADDR[8]  SRAM_ADDR[10]  SRAM_ADDR[11]  SRAM_ADDR[12]  SRAM_ADDR[13]  SRAM_ADDR[14]  SRAM_ADDR[14]  SRAM_ADDR[15]  SRAM_ADDR[15]  SRAM_ADDR[16]  SRAM_ADDR[17]  SRAM_DQ[0]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_W10 PIN_Y10 PIN_AB8 PIN_AC8 PIN_AD8	\$RAM Address[4]  \$RAM Address[5]  \$RAM Address[6]  \$RAM Address[7]  \$RAM Address[8]  \$RAM Address[9]  \$RAM Address[10]  \$RAM Address[11]  \$RAM Address[12]  \$RAM Address[13]  \$RAM Address[14]  \$RAM Address[15]  \$RAM Address[15]  \$RAM Address[16]  \$RAM Address[17]  \$RAM Address[17]
SRAM_ADDR[4]  SRAM_ADDR[5]  SRAM_ADDR[6]  SRAM_ADDR[7]  SRAM_ADDR[8]  SRAM_ADDR[10]  SRAM_ADDR[11]  SRAM_ADDR[11]  SRAM_ADDR[12]  SRAM_ADDR[13]  SRAM_ADDR[14]  SRAM_ADDR[15]  SRAM_ADDR[16]  SRAM_ADDR[17]  SRAM_DQ[0]  SRAM_DQ[1]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_Y10 PIN_Y10 PIN_AB8 PIN_AC8 PIN_AD8 PIN_AE6	SRAM Address[4]  SRAM Address[5]  SRAM Address[6]  SRAM Address[7]  SRAM Address[8]  SRAM Address[9]  SRAM Address[10]  SRAM Address[11]  SRAM Address[12]  SRAM Address[13]  SRAM Address[14]  SRAM Address[15]  SRAM Address[15]  SRAM Address[16]  SRAM Address[17]  SRAM Address[17]  SRAM Data[0]  SRAM Data[1]
SRAM_ADDR[4]  SRAM_ADDR[5]  SRAM_ADDR[6]  SRAM_ADDR[7]  SRAM_ADDR[8]  SRAM_ADDR[10]  SRAM_ADDR[11]  SRAM_ADDR[12]  SRAM_ADDR[13]  SRAM_ADDR[14]  SRAM_ADDR[14]  SRAM_ADDR[15]  SRAM_ADDR[15]  SRAM_ADDR[16]  SRAM_ADDR[17]  SRAM_DQ[0]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_W10 PIN_Y10 PIN_AB8 PIN_AC8 PIN_AD8	\$RAM Address[4]  \$RAM Address[5]  \$RAM Address[6]  \$RAM Address[7]  \$RAM Address[8]  \$RAM Address[9]  \$RAM Address[10]  \$RAM Address[11]  \$RAM Address[12]  \$RAM Address[13]  \$RAM Address[14]  \$RAM Address[15]  \$RAM Address[15]  \$RAM Address[16]  \$RAM Address[17]  \$RAM Address[17]
SRAM_ADDR[4]  SRAM_ADDR[5]  SRAM_ADDR[6]  SRAM_ADDR[7]  SRAM_ADDR[8]  SRAM_ADDR[10]  SRAM_ADDR[11]  SRAM_ADDR[11]  SRAM_ADDR[12]  SRAM_ADDR[13]  SRAM_ADDR[14]  SRAM_ADDR[15]  SRAM_ADDR[16]  SRAM_ADDR[17]  SRAM_DQ[0]  SRAM_DQ[1]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_Y10 PIN_Y10 PIN_AB8 PIN_AC8 PIN_AD8 PIN_AE6	SRAM Address[4]  SRAM Address[5]  SRAM Address[6]  SRAM Address[7]  SRAM Address[8]  SRAM Address[9]  SRAM Address[10]  SRAM Address[11]  SRAM Address[12]  SRAM Address[13]  SRAM Address[14]  SRAM Address[15]  SRAM Address[15]  SRAM Address[16]  SRAM Address[17]  SRAM Address[17]  SRAM Data[0]  SRAM Data[1]
SRAM_ADDR[4]  SRAM_ADDR[5]  SRAM_ADDR[6]  SRAM_ADDR[6]  SRAM_ADDR[7]  SRAM_ADDR[8]  SRAM_ADDR[10]  SRAM_ADDR[10]  SRAM_ADDR[11]  SRAM_ADDR[12]  SRAM_ADDR[13]  SRAM_ADDR[14]  SRAM_ADDR[15]  SRAM_ADDR[16]  SRAM_ADDR[17]  SRAM_DQ[0]  SRAM_DQ[1]  SRAM_DQ[2]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_Y10 PIN_AB8 PIN_AC8 PIN_AC8 PIN_AD8 PIN_AE6 PIN_AF6	SRAM Address[4]   SRAM Address[5]   SRAM Address[6]   SRAM Address[7]   SRAM Address[8]   SRAM Address[8]   SRAM Address[9]   SRAM Address[10]   SRAM Address[11]   SRAM Address[12]   SRAM Address[13]   SRAM Address[14]   SRAM Address[14]   SRAM Address[15]   SRAM Address[16]   SRAM Address[17]   SRAM Address[17]   SRAM Data[0]   SRAM Data[1]   SRAM Data[2]
SRAM_ADDR[4]  SRAM_ADDR[5]  SRAM_ADDR[6]  SRAM_ADDR[6]  SRAM_ADDR[7]  SRAM_ADDR[8]  SRAM_ADDR[10]  SRAM_ADDR[10]  SRAM_ADDR[11]  SRAM_ADDR[12]  SRAM_ADDR[13]  SRAM_ADDR[14]  SRAM_ADDR[15]  SRAM_ADDR[16]  SRAM_ADDR[17]  SRAM_ADDR[17]  SRAM_DQ[0]  SRAM_DQ[1]  SRAM_DQ[2]  SRAM_DQ[3]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_Y10 PIN_W8 PIN_W10 PIN_AB8 PIN_AC8 PIN_AC8 PIN_AC8 PIN_AC8 PIN_AD8 PIN_AC8 PIN_AD8 PIN_AC9 PIN_AF6 PIN_AA9	SRAM Address[4]     SRAM Address[5]     SRAM Address[6]     SRAM Address[7]     SRAM Address[8]     SRAM Address[9]     SRAM Address[10]     SRAM Address[11]     SRAM Address[12]     SRAM Address[13]     SRAM Address[14]     SRAM Address[15]     SRAM Address[16]     SRAM Address[17]     SRAM Address[17]     SRAM Data[0]     SRAM Data[2]     SRAM Data[3]
SRAM_ADDR[4]   SRAM_ADDR[5]   SRAM_ADDR[6]   SRAM_ADDR[6]   SRAM_ADDR[7]   SRAM_ADDR[8]   SRAM_ADDR[10]   SRAM_ADDR[10]   SRAM_ADDR[11]   SRAM_ADDR[12]   SRAM_ADDR[13]   SRAM_ADDR[14]   SRAM_ADDR[15]   SRAM_ADDR[16]   SRAM_ADDR[17]   SRAM_ADDR[17]   SRAM_DQ[0]   SRAM_DQ[1]   SRAM_DQ[2]   SRAM_DQ[3]   SRAM_DQ[4]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_W10 PIN_Y10 PIN_AB8 PIN_AC8 PIN_AC8 PIN_AC8 PIN_AC8 PIN_AD8 PIN_AC8 PIN_AD8 PIN_AC8 PIN_AD8 PIN_AC8 PIN_AA9 PIN_AA10	SRAM Address[4]     SRAM Address[5]     SRAM Address[6]     SRAM Address[7]     SRAM Address[8]     SRAM Address[9]     SRAM Address[10]     SRAM Address[11]     SRAM Address[12]     SRAM Address[13]     SRAM Address[14]     SRAM Address[15]     SRAM Address[16]     SRAM Address[17]     SRAM Address[17]     SRAM Data[0]     SRAM Data[2]     SRAM Data[4]
SRAM_ADDR[4]   SRAM_ADDR[5]   SRAM_ADDR[6]   SRAM_ADDR[6]   SRAM_ADDR[7]   SRAM_ADDR[8]   SRAM_ADDR[10]   SRAM_ADDR[11]   SRAM_ADDR[11]   SRAM_ADDR[12]   SRAM_ADDR[13]   SRAM_ADDR[14]   SRAM_ADDR[15]   SRAM_ADDR[16]   SRAM_ADDR[17]   SRAM_ADDR[17]   SRAM_DQ[0]   SRAM_DQ[0]   SRAM_DQ[1]   SRAM_DQ[2]   SRAM_DQ[4]   SRAM_DQ[4]   SRAM_DQ[5]   SRAM_DQ[6]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_W10 PIN_Y10 PIN_AB8 PIN_AC8 PIN_AB8 PIN_AC8 PIN_AD8 PIN_AD8 PIN_AD8 PIN_AD8 PIN_AB10 PIN_AB10 PIN_AB10	SRAM Address[4]     SRAM Address[5]     SRAM Address[6]     SRAM Address[7]     SRAM Address[8]     SRAM Address[9]     SRAM Address[10]     SRAM Address[11]     SRAM Address[12]     SRAM Address[13]     SRAM Address[14]     SRAM Address[15]     SRAM Address[16]     SRAM Address[17]     SRAM Address[17]     SRAM Data[0]     SRAM Data[2]     SRAM Data[4]     SRAM Data[5]     SRAM Data[6]
SRAM_ADDR[4]   SRAM_ADDR[5]   SRAM_ADDR[6]   SRAM_ADDR[6]   SRAM_ADDR[7]   SRAM_ADDR[8]   SRAM_ADDR[10]   SRAM_ADDR[11]   SRAM_ADDR[11]   SRAM_ADDR[12]   SRAM_ADDR[13]   SRAM_ADDR[14]   SRAM_ADDR[15]   SRAM_ADDR[16]   SRAM_ADDR[17]   SRAM_DQ[0]   SRAM_DQ[0]   SRAM_DQ[1]   SRAM_DQ[2]   SRAM_DQ[3]   SRAM_DQ[4]   SRAM_DQ[5]   SRAM_DQ[6]   SRAM_DQ[6]   SRAM_DQ[7]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_W10 PIN_Y10 PIN_AB8 PIN_AC8 PIN_AB8 PIN_AC8 PIN_AD8 PIN_AD8 PIN_AD8 PIN_AB10 PIN_AB10 PIN_AB11 PIN_Y11	SRAM Address[4]     SRAM Address[5]     SRAM Address[6]     SRAM Address[7]     SRAM Address[8]     SRAM Address[9]     SRAM Address[10]     SRAM Address[11]     SRAM Address[12]     SRAM Address[13]     SRAM Address[14]     SRAM Address[15]     SRAM Address[16]     SRAM Address[16]     SRAM Address[17]     SRAM Data[0]     SRAM Data[2]     SRAM Data[4]     SRAM Data[5]     SRAM Data[6]     SRAM Data[7]
SRAM_ADDR[4]  SRAM_ADDR[5]  SRAM_ADDR[6]  SRAM_ADDR[7]  SRAM_ADDR[8]  SRAM_ADDR[9]  SRAM_ADDR[10]  SRAM_ADDR[11]  SRAM_ADDR[12]  SRAM_ADDR[13]  SRAM_ADDR[14]  SRAM_ADDR[15]  SRAM_ADDR[16]  SRAM_ADDR[16]  SRAM_DQ[0]  SRAM_DQ[1]  SRAM_DQ[1]  SRAM_DQ[2]  SRAM_DQ[3]  SRAM_DQ[6]  SRAM_DQ[6]  SRAM_DQ[7]  SRAM_DQ[8]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_Y10 PIN_Y10 PIN_AB8 PIN_AC8 PIN_AC8 PIN_AC8 PIN_AC8 PIN_AD8 PIN_AC9 PIN_AB6 PIN_AF6 PIN_AF6 PIN_AA9 PIN_AA10 PIN_AB10 PIN_AB11 PIN_AB11	SRAM Address[4]     SRAM Address[5]     SRAM Address[6]     SRAM Address[7]     SRAM Address[8]     SRAM Address[9]     SRAM Address[10]     SRAM Address[11]     SRAM Address[12]     SRAM Address[13]     SRAM Address[14]     SRAM Address[15]     SRAM Address[15]     SRAM Address[16]     SRAM Address[17]     SRAM Data[0]     SRAM Data[2]     SRAM Data[3]     SRAM Data[6]     SRAM Data[7]     SRAM Data[7]     SRAM Data[8]
SRAM_ADDR[4]   SRAM_ADDR[5]   SRAM_ADDR[6]   SRAM_ADDR[7]   SRAM_ADDR[8]   SRAM_ADDR[9]   SRAM_ADDR[10]   SRAM_ADDR[11]   SRAM_ADDR[12]   SRAM_ADDR[13]   SRAM_ADDR[14]   SRAM_ADDR[15]   SRAM_ADDR[15]   SRAM_ADDR[16]   SRAM_ADDR[17]   SRAM_DQ[0]   SRAM_DQ[1]   SRAM_DQ[2]   SRAM_DQ[3]   SRAM_DQ[4]   SRAM_DQ[6]   SRAM_DQ[6]   SRAM_DQ[7]   SRAM_DQ[7]   SRAM_DQ[7]   SRAM_DQ[8]   SRAM_DQ[9]   SRAM_DQ[9]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_Y10 PIN_Y10 PIN_AB8 PIN_AC8 PIN_AC8 PIN_AC8 PIN_AC9 PIN_AC9 PIN_AD8 PIN_AC9 PIN_AF6 PIN_AF6 PIN_AA9 PIN_AA10 PIN_AB10 PIN_AB10 PIN_AB17 PIN_AE7 PIN_AE7	SRAM Address[4]     SRAM Address[5]     SRAM Address[6]     SRAM Address[7]     SRAM Address[8]     SRAM Address[9]     SRAM Address[10]     SRAM Address[11]     SRAM Address[12]     SRAM Address[13]     SRAM Address[14]     SRAM Address[15]     SRAM Address[16]     SRAM Address[16]     SRAM Address[17]     SRAM Data[0]     SRAM Data[2]     SRAM Data[4]     SRAM Data[5]     SRAM Data[6]     SRAM Data[8]     SRAM Data[9]
SRAM_ADDR[4]   SRAM_ADDR[5]   SRAM_ADDR[6]   SRAM_ADDR[7]   SRAM_ADDR[8]   SRAM_ADDR[10]   SRAM_ADDR[10]   SRAM_ADDR[11]   SRAM_ADDR[11]   SRAM_ADDR[12]   SRAM_ADDR[13]   SRAM_ADDR[14]   SRAM_ADDR[15]   SRAM_ADDR[16]   SRAM_ADDR[17]   SRAM_DQ[0]   SRAM_DQ[1]   SRAM_DQ[2]   SRAM_DQ[3]   SRAM_DQ[4]   SRAM_DQ[5]   SRAM_DQ[6]   SRAM_DQ[6]   SRAM_DQ[7]   SRAM_DQ[8]   SRAM_DQ[9]   SRAM_DQ[9]   SRAM_DQ[9]   SRAM_DQ[9]   SRAM_DQ[10]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_Y10 PIN_Y10 PIN_AB8 PIN_AC8 PIN_AC8 PIN_AC8 PIN_AC9 PIN_AF6 PIN_AF6 PIN_AA9 PIN_AA10 PIN_AB10 PIN_AB10 PIN_AB17 PIN_AE7 PIN_AE7 PIN_AE7	SRAM Address[4]     SRAM Address[5]     SRAM Address[6]     SRAM Address[7]     SRAM Address[8]     SRAM Address[9]     SRAM Address[10]     SRAM Address[11]     SRAM Address[12]     SRAM Address[13]     SRAM Address[14]     SRAM Address[15]     SRAM Address[16]     SRAM Address[16]     SRAM Address[17]     SRAM Data[0]     SRAM Data[2]     SRAM Data[3]     SRAM Data[6]     SRAM Data[7]     SRAM Data[8]     SRAM Data[9]     SRAM Data[10]
SRAM_ADDR[4]   SRAM_ADDR[5]   SRAM_ADDR[6]   SRAM_ADDR[7]   SRAM_ADDR[8]   SRAM_ADDR[9]   SRAM_ADDR[10]   SRAM_ADDR[11]   SRAM_ADDR[11]   SRAM_ADDR[12]   SRAM_ADDR[13]   SRAM_ADDR[14]   SRAM_ADDR[15]   SRAM_ADDR[16]   SRAM_ADDR[17]   SRAM_DQ[0]   SRAM_DQ[1]   SRAM_DQ[2]   SRAM_DQ[3]   SRAM_DQ[4]   SRAM_DQ[5]   SRAM_DQ[6]   SRAM_DQ[6]   SRAM_DQ[6]   SRAM_DQ[7]   SRAM_DQ[8]   SRAM_DQ[9]   SRAM_DQ[9]   SRAM_DQ[10]   SRAM_DQ[9]   SRAM_DQ[11]   SRAM_DQ[9]   SRAM_DQ[11]   SRAM_DQ[9]   SRAM_DQ[11]   SRAM_D	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_Y10 PIN_Y10 PIN_AB8 PIN_AC8 PIN_AC8 PIN_AC8 PIN_AC9 PIN_AE6 PIN_AF6 PIN_AA9 PIN_AA10 PIN_AB10 PIN_AB10 PIN_AB10 PIN_AB7 PIN_AE7 PIN_AE7 PIN_AE8 PIN_AE8	SRAM Address[4]     SRAM Address[5]     SRAM Address[6]     SRAM Address[7]     SRAM Address[8]     SRAM Address[9]     SRAM Address[10]     SRAM Address[11]     SRAM Address[12]     SRAM Address[13]     SRAM Address[14]     SRAM Address[15]     SRAM Address[16]     SRAM Address[17]     SRAM Address[17]     SRAM Data[0]     SRAM Data[1]     SRAM Data[3]     SRAM Data[4]     SRAM Data[6]     SRAM Data[9]     SRAM Data[10]     SRAM Data[10]     SRAM Data[11]
SRAM_ADDR[4]   SRAM_ADDR[5]   SRAM_ADDR[6]   SRAM_ADDR[7]   SRAM_ADDR[8]   SRAM_ADDR[9]   SRAM_ADDR[10]   SRAM_ADDR[11]   SRAM_ADDR[11]   SRAM_ADDR[12]   SRAM_ADDR[13]   SRAM_ADDR[14]   SRAM_ADDR[15]   SRAM_ADDR[16]   SRAM_ADDR[17]   SRAM_DQ[0]   SRAM_DQ[0]   SRAM_DQ[1]   SRAM_DQ[2]   SRAM_DQ[3]   SRAM_DQ[4]   SRAM_DQ[5]   SRAM_DQ[6]   SRAM_DQ[6]   SRAM_DQ[7]   SRAM_DQ[9]   SRAM_DQ[9]   SRAM_DQ[10]   SRAM_DQ[9]   SRAM_DQ[11]   SRAM_DQ[9]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[12]   SRAM_DQ[11]   SRAM_DQ[12]   SRAM	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_C7 PIN_W8 PIN_W10 PIN_W10 PIN_W10 PIN_W10 PIN_AC8 PIN_AC8 PIN_AC8 PIN_AD8 PIN_AC8 PIN_AD8 PIN_AC9 PIN_AF6 PIN_AF6 PIN_AF6 PIN_AA9 PIN_AA10 PIN_AB10 PIN_AB10 PIN_AB11 PIN_AB17 PIN_AE7 PIN_AE8 PIN_AF8 PIN_AF8 PIN_AF8	SRAM Address[4]     SRAM Address[5]     SRAM Address[6]     SRAM Address[7]     SRAM Address[8]     SRAM Address[9]     SRAM Address[10]     SRAM Address[11]     SRAM Address[12]     SRAM Address[13]     SRAM Address[14]     SRAM Address[15]     SRAM Address[16]     SRAM Address[17]     SRAM Address[17]     SRAM Data[0]     SRAM Data[1]     SRAM Data[3]     SRAM Data[4]     SRAM Data[6]     SRAM Data[9]     SRAM Data[10]     SRAM Data[10]     SRAM Data[11]     SRAM Data[12]     SRAM Data[11]     SRAM Data[12]
SRAM_ADDR[4]   SRAM_ADDR[5]   SRAM_ADDR[6]   SRAM_ADDR[7]   SRAM_ADDR[7]   SRAM_ADDR[8]   SRAM_ADDR[10]   SRAM_ADDR[11]   SRAM_ADDR[11]   SRAM_ADDR[12]   SRAM_ADDR[13]   SRAM_ADDR[14]   SRAM_ADDR[15]   SRAM_ADDR[16]   SRAM_ADDR[17]   SRAM_DQ[0]   SRAM_DQ[0]   SRAM_DQ[1]   SRAM_DQ[3]   SRAM_DQ[4]   SRAM_DQ[4]   SRAM_DQ[5]   SRAM_DQ[6]   SRAM_DQ[6]   SRAM_DQ[7]   SRAM_DQ[8]   SRAM_DQ[9]   SRAM_DQ[11]   SRAM_DQ[9]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[12]   SRAM_DQ[12]   SRAM_DQ[12]   SRAM_DQ[13]   SRAM_DQ[11]   SRAM_DQ[13]   SRAM_DQ[11]   SRAM_DQ[13]   SRAM_DQ[11]   SRAM_DQ[13]   SRAM_DQ[11]   SRAM_DQ[13]   SRA	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_W8 PIN_W10 PIN_AB8 PIN_AC8 PIN_AB8 PIN_AC8 PIN_AD8 PIN_AD8 PIN_AC9 PIN_AB1 PIN_AF6 PIN_AA9 PIN_AA10 PIN_AB10 PIN_AB10 PIN_AB10 PIN_AB10 PIN_AB7 PIN_AE7 PIN_AE7 PIN_AE8 PIN_AF8 PIN_AF8 PIN_W11 PIN_W12	SRAM Address[4]   SRAM Address[5]   SRAM Address[6]   SRAM Address[7]   SRAM Address[8]   SRAM Address[9]   SRAM Address[10]   SRAM Address[11]   SRAM Address[11]   SRAM Address[12]   SRAM Address[13]   SRAM Address[14]   SRAM Address[14]   SRAM Address[15]   SRAM Address[16]   SRAM Address[16]   SRAM Address[17]   SRAM Data[0]   SRAM Data[1]   SRAM Data[2]   SRAM Data[3]   SRAM Data[4]   SRAM Data[6]   SRAM Data[6]   SRAM Data[7]   SRAM Data[8]   SRAM Data[9]   SRAM Data[11]   SRAM Data[11]   SRAM Data[11]   SRAM Data[11]   SRAM Data[12]   SRAM Data[12]   SRAM Data[13]
SRAM_ADDR[4]   SRAM_ADDR[5]   SRAM_ADDR[6]   SRAM_ADDR[7]   SRAM_ADDR[8]   SRAM_ADDR[9]   SRAM_ADDR[10]   SRAM_ADDR[11]   SRAM_ADDR[11]   SRAM_ADDR[12]   SRAM_ADDR[13]   SRAM_ADDR[14]   SRAM_ADDR[15]   SRAM_ADDR[16]   SRAM_ADDR[17]   SRAM_DQ[0]   SRAM_DQ[1]   SRAM_DQ[2]   SRAM_DQ[3]   SRAM_DQ[4]   SRAM_DQ[5]   SRAM_DQ[6]   SRAM_DQ[6]   SRAM_DQ[7]   SRAM_DQ[9]   SRAM_DQ[9]   SRAM_DQ[9]   SRAM_DQ[10]   SRAM_DQ[10]   SRAM_DQ[10]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[12]   SRAM_DQ[11]   SRAM_DQ[12]   SRAM_DQ[11]   SRAM_DQ[12]   SR	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_C7 PIN_W8 PIN_W10 PIN_W10 PIN_W10 PIN_W10 PIN_AC8 PIN_AC8 PIN_AC8 PIN_AD8 PIN_AC8 PIN_AD8 PIN_AC9 PIN_AF6 PIN_AF6 PIN_AF6 PIN_AA9 PIN_AA10 PIN_AB10 PIN_AB10 PIN_AB11 PIN_AB17 PIN_AE7 PIN_AE8 PIN_AF8 PIN_AF8 PIN_AF8	SRAM Address[4]     SRAM Address[5]     SRAM Address[6]     SRAM Address[7]     SRAM Address[8]     SRAM Address[9]     SRAM Address[10]     SRAM Address[11]     SRAM Address[12]     SRAM Address[13]     SRAM Address[14]     SRAM Address[15]     SRAM Address[16]     SRAM Address[17]     SRAM Address[17]     SRAM Data[0]     SRAM Data[1]     SRAM Data[3]     SRAM Data[4]     SRAM Data[6]     SRAM Data[9]     SRAM Data[10]     SRAM Data[11]     SRAM Data[12]     SRAM Data[12]
SRAM_ADDR[4]   SRAM_ADDR[5]   SRAM_ADDR[6]   SRAM_ADDR[7]   SRAM_ADDR[7]   SRAM_ADDR[8]   SRAM_ADDR[10]   SRAM_ADDR[11]   SRAM_ADDR[11]   SRAM_ADDR[12]   SRAM_ADDR[13]   SRAM_ADDR[14]   SRAM_ADDR[15]   SRAM_ADDR[16]   SRAM_ADDR[17]   SRAM_DQ[0]   SRAM_DQ[0]   SRAM_DQ[1]   SRAM_DQ[3]   SRAM_DQ[4]   SRAM_DQ[4]   SRAM_DQ[5]   SRAM_DQ[6]   SRAM_DQ[6]   SRAM_DQ[7]   SRAM_DQ[8]   SRAM_DQ[9]   SRAM_DQ[11]   SRAM_DQ[9]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[12]   SRAM_DQ[11]   SRAM_DQ[12]   SRAM_DQ[11]   SRAM_DQ[12]   SRAM_DQ[13]   SRAM_DQ[11]   SRAM_DQ[13]   SRAM_DQ[11]   SRAM_DQ[13]   SRAM_DQ[11]   SRAM_DQ[13]   SRA	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_W8 PIN_W10 PIN_AB8 PIN_AC8 PIN_AB8 PIN_AC8 PIN_AD8 PIN_AD8 PIN_AC9 PIN_AB1 PIN_AF6 PIN_AA9 PIN_AA10 PIN_AB10 PIN_AB10 PIN_AB10 PIN_AB10 PIN_AB7 PIN_AE7 PIN_AE7 PIN_AE8 PIN_AF8 PIN_AF8 PIN_W11 PIN_W12	SRAM Address[4]   SRAM Address[5]   SRAM Address[6]   SRAM Address[7]   SRAM Address[8]   SRAM Address[9]   SRAM Address[10]   SRAM Address[11]   SRAM Address[11]   SRAM Address[12]   SRAM Address[13]   SRAM Address[14]   SRAM Address[14]   SRAM Address[15]   SRAM Address[16]   SRAM Address[16]   SRAM Address[17]   SRAM Data[0]   SRAM Data[1]   SRAM Data[2]   SRAM Data[3]   SRAM Data[4]   SRAM Data[6]   SRAM Data[6]   SRAM Data[7]   SRAM Data[8]   SRAM Data[9]   SRAM Data[11]   SRAM Data[11]   SRAM Data[11]   SRAM Data[11]   SRAM Data[12]   SRAM Data[12]   SRAM Data[13]
SRAM_ADDR[4]   SRAM_ADDR[5]   SRAM_ADDR[6]   SRAM_ADDR[7]   SRAM_ADDR[7]   SRAM_ADDR[8]   SRAM_ADDR[10]   SRAM_ADDR[11]   SRAM_ADDR[11]   SRAM_ADDR[12]   SRAM_ADDR[13]   SRAM_ADDR[14]   SRAM_ADDR[15]   SRAM_ADDR[15]   SRAM_ADDR[16]   SRAM_ADDR[17]   SRAM_DQ[0]   SRAM_DQ[0]   SRAM_DQ[1]   SRAM_DQ[3]   SRAM_DQ[4]   SRAM_DQ[5]   SRAM_DQ[6]   SRAM_DQ[6]   SRAM_DQ[7]   SRAM_DQ[8]   SRAM_DQ[9]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[12]   SRAM_DQ[11]   SRAM_DQ[13]   SRAM_DQ[11]   SRAM_DQ[13]   SRAM_DQ[13]   SRAM_DQ[13]   SRAM_DQ[14]   SRAM_DQ[13]   SRAM_DQ[14]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_W10 PIN_Y10 PIN_AB8 PIN_AC8 PIN_AD8 PIN_AC8 PIN_AD8 PIN_AC9 PIN_AF6 PIN_AF6 PIN_AF1 PIN_AF10 PIN_AB10 PIN_AB10 PIN_AB10 PIN_AB10 PIN_AB10 PIN_AB10 PIN_AF7 PIN_AF7 PIN_AF7 PIN_AF7 PIN_AF8 PIN_AF8 PIN_W11 PIN_W12 PIN_AC9	SRAM Address[4]   SRAM Address[5]   SRAM Address[6]   SRAM Address[6]   SRAM Address[7]   SRAM Address[8]   SRAM Address[9]   SRAM Address[10]   SRAM Address[11]   SRAM Address[11]   SRAM Address[12]   SRAM Address[13]   SRAM Address[14]   SRAM Address[15]   SRAM Address[16]   SRAM Address[16]   SRAM Address[17]   SRAM Address[17]   SRAM Data[0]   SRAM Data[1]   SRAM Data[2]   SRAM Data[3]   SRAM Data[4]   SRAM Data[6]   SRAM Data[6]   SRAM Data[7]   SRAM Data[9]   SRAM Data[10]   SRAM Data[11]   SRAM Data[11]   SRAM Data[11]   SRAM Data[12]   SRAM Data[13]   SRAM Data[13]   SRAM Data[13]   SRAM Data[13]   SRAM Data[14]   SRAM Data[13]   SRAM Data[14]   SRAM Data[14]   SRAM Data[15]   SRAM Data[16]   SRAM Data[17]   SRAM Data[17]   SRAM Data[18]   SRAM Data[19]   SRAM Data[11]   SRAM Data[13]   SRAM Data[14]
SRAM_ADDR[4]   SRAM_ADDR[5]   SRAM_ADDR[6]   SRAM_ADDR[7]   SRAM_ADDR[8]   SRAM_ADDR[9]   SRAM_ADDR[10]   SRAM_ADDR[11]   SRAM_ADDR[11]   SRAM_ADDR[12]   SRAM_ADDR[13]   SRAM_ADDR[14]   SRAM_ADDR[15]   SRAM_ADDR[15]   SRAM_ADDR[16]   SRAM_ADDR[17]   SRAM_DQ[0]   SRAM_DQ[1]   SRAM_DQ[2]   SRAM_DQ[3]   SRAM_DQ[4]   SRAM_DQ[5]   SRAM_DQ[6]   SRAM_DQ[6]   SRAM_DQ[6]   SRAM_DQ[7]   SRAM_DQ[8]   SRAM_DQ[9]   SRAM_DQ[10]   SRAM_DQ[10]   SRAM_DQ[11]   SRAM_DQ[11]   SRAM_DQ[13]   SRAM_DQ[14]   SRAM_DQ[14]   SRAM_DQ[13]   SRAM_DQ[14]   SRAM_DQ[14]   SRAM_DQ[15]	PIN_AD4 PIN_AD5 PIN_AE5 PIN_AF5 PIN_AD6 PIN_AD7 PIN_V10 PIN_V9 PIN_V9 PIN_AC7 PIN_W8 PIN_W10 PIN_W10 PIN_W10 PIN_W10 PIN_AB8 PIN_AC8 PIN_AD8 PIN_AC6 PIN_AD8 PIN_AA10 PIN_AF6 PIN_AA9 PIN_AA10 PIN_AB10 PIN_AB10 PIN_AB10 PIN_AB10 PIN_AB10 PIN_AB10 PIN_AF7 PIN_AF7 PIN_AE7 PIN_AE8 PIN_AF8 PIN_W11 PIN_W12 PIN_AC9 PIN_AC9 PIN_AC9 PIN_AC9	SRAM Address[4]   SRAM Address[5]   SRAM Address[6]   SRAM Address[6]   SRAM Address[7]   SRAM Address[8]   SRAM Address[9]   SRAM Address[10]   SRAM Address[11]   SRAM Address[12]   SRAM Address[12]   SRAM Address[13]   SRAM Address[14]   SRAM Address[15]   SRAM Address[16]   SRAM Address[16]   SRAM Address[17]   SRAM Data[0]   SRAM Data[1]   SRAM Data[2]   SRAM Data[3]   SRAM Data[4]   SRAM Data[6]   SRAM Data[6]   SRAM Data[7]   SRAM Data[9]   SRAM Data[11]   SRAM Data[11]   SRAM Data[11]   SRAM Data[11]   SRAM Data[12]   SRAM Data[13]   SRAM Data[14]   SRAM Data[14]   SRAM Data[15]   SRAM Data

SRAM_UB_N	PIN_AF9	SRAM High-byte Data Mask
SRAM_LB_N	PIN_AE9	SRAM Low-byte Data Mask
SRAM_CE_N	PIN_AC11	SRAM Chip Enable
Signal Name	FPGA Pin No.	Description
OTG_ADDR[0]	PIN_K7	ISP1362 Address[0]
OTG_ADDR[1]	PIN F2	ISP1362 Address[1]
OTG_DATA[0]	PIN_F4	ISP1362 Data[0]
OTG_DATA[1]	PIN_D2	ISP1362 Data[1]
OTG_DATA[2]	PIN_D1	ISP1362 Data[2]
OTG_DATA[3]	PIN_F7	ISP1362 Data[3]
OTG_DATA[4]	PIN_J5	ISP1362 Data[4]
OTG_DATA[5]	PIN_J8	ISP1362 Data[5]
OTG_DATA[6]	PIN_J7	ISP1362 Data[6]
OTG_DATA[7]	PIN_H6	ISP1362 Data[7]
OTG_DATA[8]	PIN_E2	ISP1362 Data[8]
OTG_DATA[9]	PIN_E1	ISP1362 Data[9]
OTG_DATA[10]	PIN_K6	ISP1362 Data[10]
OTG_DATA[11]	PIN_K5	ISP1362 Data[11]
OTG_DATA[12]	PIN_G4	ISP1362 Data[12]
OTG_DATA[13]	PIN_G3	ISP1362 Data[13]
OTG_DATA[14]	PIN_J6	ISP1362 Data[14]
OTG_DATA[15]	PIN_K8	ISP1362 Data[15]
OTG_CS_N	PIN_F1	ISP1362 Chip Select
OTG_RD_N	PIN_G2	ISP1362 Read
OTG_WR_N	PIN_G1	ISP1362 Write
OTG_RST_N	PIN_G5	ISP1362 Reset
OTG_INT0	PIN_B3	ISP1362 Interrupt 0
OTG_INT1	PIN_C3	ISP1362 Interrupt 1
OTG_DACK0_N	PIN_C2	ISP1362 DMA Acknowledge 0
OTG_DACK1_N	PIN_B2	ISP1362 DMA Acknowledge 1
OTG_DREQ0	PIN_F6	ISP1362 DMA Request 0
OTG_DREQ1	PIN_E5	ISP1362 DMA Request 1
OTG_FSPEED	PIN_F3	USB Full Speed, 0 = Enable, Z = Disable
OTG_LSPEED	PIN_G6	USB Low Speed, 0 = Enable, Z = Disable
OTG_LSPEED  Signal Name	PIN_G6 FPGA Pin No.	USB Low Speed, 0 = Enable, Z = Disable  Description
OTG_LSPEED  Signal Name  LCD_DATA[0]	PIN_G6 FPGA Pin No. PIN_J1	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]	PIN_G6 FPGA Pin No. PIN_J1 PIN_J2	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]	PIN_G6 FPGA Pin No. PIN_J1 PIN_J2 PIN_H1	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]	PIN_G6 FPGA Pin No. PIN_J1 PIN_J2 PIN_H1 PIN_H2	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]	PIN_G6 FPGA Pin No. PIN_J1 PIN_J2 PIN_H1 PIN_H2 PIN_J4	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]	PIN_G6 FPGA Pin No. PIN_J1 PIN_J2 PIN_H1 PIN_H2 PIN_J4 PIN_J3	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]	PIN_G6 FPGA Pin No. PIN_J1 PIN_J2 PIN_H1 PIN_H2 PIN_J4 PIN_J3 PIN_H4	USB Low Speed, 0 = Enable, Z = Disable    Description
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]	PIN_G6 FPGA Pin No. PIN_J1 PIN_J2 PIN_H1 PIN_H2 PIN_J4 PIN_J3 PIN_H4 PIN_H3	USB Low Speed, 0 = Enable, Z = Disable    Description
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW	PIN_G6 FPGA Pin No. PIN_J1 PIN_J2 PIN_H1 PIN_H2 PIN_J4 PIN_J3 PIN_H4 PIN_H3 PIN_H3 PIN_K4	USB Low Speed, 0 = Enable, Z = Disable    Description
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN	PIN_G6 FPGA Pin No. PIN_J1 PIN_J2 PIN_H1 PIN_H2 PIN_J4 PIN_J3 PIN_H4 PIN_H3 PIN_H3 PIN_K4 PIN_K4 PIN_K3	USB Low Speed, 0 = Enable, Z = Disable    Description
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_RS	PIN_G6 FPGA Pin No. PIN_J1 PIN_J2 PIN_H1 PIN_H2 PIN_J4 PIN_J3 PIN_H4 PIN_H3 PIN_H3 PIN_K4 PIN_K4 PIN_K3 PIN_K1	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN	PIN_G6 FPGA Pin No. PIN_J1 PIN_J2 PIN_H1 PIN_H2 PIN_J4 PIN_J3 PIN_H4 PIN_H3 PIN_H3 PIN_K4 PIN_K4 PIN_K3	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_RS  LCD_ON  LCD_BLON	PIN_G6  FPGA Pin No.  PIN_J1  PIN_J2  PIN_H1  PIN_H2  PIN_J4  PIN_J3  PIN_H4  PIN_H3  PIN_K4  PIN_K3  PIN_K4  PIN_K3  PIN_K4  PIN_K3  PIN_K4  PIN_K5	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_RS  LCD_ON  LCD_BLON  Signal Name	PIN_G6  FPGA Pin No.  PIN_J1  PIN_J2  PIN_H1  PIN_H2  PIN_J4  PIN_J3  PIN_H4  PIN_H3  PIN_K4  PIN_K3  PIN_K4  PIN_K3  PIN_K1  PIN_K4  PIN_K5  PIN_K1  PIN_L4  PIN_L4  PIN_L4  PIN_L4  PIN_K2  FPGA Pin No.	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_RS  LCD_ON  LCD_BLON  Signal Name  SD_DAT	PIN_G6  FPGA Pin No.  PIN_J1  PIN_J2  PIN_H1  PIN_H2  PIN_J4  PIN_J3  PIN_H4  PIN_H3  PIN_K4  PIN_K3  PIN_K4  PIN_K3  PIN_K1  PIN_K1  PIN_K2  FPGA Pin No.  PIN_AD24	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_RS  LCD_ON  LCD_BLON  Signal Name  SD_DAT  SD_DAT3	PIN_G6 FPGA Pin No. PIN_J1 PIN_J2 PIN_H1 PIN_H2 PIN_J4 PIN_J3 PIN_H4 PIN_H3 PIN_K4 PIN_K3 PIN_K4 PIN_K3 PIN_K1 PIN_L4 PIN_L4 PIN_L4 PIN_K2 FPGA Pin No. PIN_AD24 PIN_AC23	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF  Description  SD Card Data
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_EN  LCD_RS  LCD_ON  LCD_BLON  Signal Name  SD_DAT3  SD_CMD	PIN_G6  FPGA Pin No.  PIN_J1  PIN_J2  PIN_H1  PIN_H2  PIN_J4  PIN_J3  PIN_H4  PIN_H3  PIN_K4  PIN_K3  PIN_K4  PIN_K3  PIN_K1  PIN_L4  PIN_L4  PIN_L4  PIN_K2  FPGA Pin No.  PIN_AD24  PIN_AC23  PIN_Y21	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF  Description  SD Card Data  SD Card Data 3
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_EN  LCD_BLON  Signal Name  SD_DAT3  SD_CMD  SD_CLK	PIN_G6 FPGA Pin No. PIN_J1 PIN_J2 PIN_H1 PIN_H2 PIN_J4 PIN_J3 PIN_H4 PIN_H3 PIN_K4 PIN_K3 PIN_K4 PIN_K3 PIN_K1 PIN_L4 PIN_L4 PIN_L4 PIN_K2 FPGA Pin No. PIN_AD24 PIN_AC23	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF  Description  SD Card Data 3  SD Card Command Signal
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_EN  LCD_RS  LCD_ON  LCD_BLON  Signal Name  SD_DAT3  SD_CMD	PIN_G6  FPGA Pin No.  PIN_J1  PIN_J2  PIN_H1  PIN_H2  PIN_J4  PIN_J3  PIN_H4  PIN_H3  PIN_K4  PIN_K3  PIN_K4  PIN_K3  PIN_K4  PIN_K2  FPGA Pin No.  PIN_AD24  PIN_AC23  PIN_Y21  PIN_AD25	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF  Description  SD Card Data 3  SD Card Command Signal  SD Card Clock
OTG_LSPEED	PIN_G6  FPGA Pin No.  PIN_J1  PIN_J2  PIN_H1  PIN_H2  PIN_J3  PIN_H4  PIN_H3  PIN_K4  PIN_K4  PIN_K5  PIN_K1  PIN_L4  PIN_K2  FPGA Pin No.  PIN_AD24  PIN_AC23  PIN_Y21  PIN_AD25  FPGA Pin No.	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF  Description  SD Card Data 3  SD Card Command Signal  SD Card Clock  Description
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_EN  LCD_BLON  Signal Name  SD_DAT  SD_CMD  SD_CLK  Signal Name  TDI	PIN_G6  FPGA Pin No.  PIN_J1  PIN_J2  PIN_H1  PIN_H2  PIN_J3  PIN_H4  PIN_H3  PIN_K4  PIN_K3  PIN_K4  PIN_K5  PIN_K1  PIN_L4  PIN_L4  PIN_L4  PIN_K2  FPGA Pin No.  PIN_AD24  PIN_AC23  PIN_Y21  PIN_AD25  FPGA Pin No.  PIN_B14	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF  Description  SD Card Data 3  SD Card Command Signal  SD Card Clock  Description  CPLD -> FPGA (Data in)
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_EN  LCD_BLON  Signal Name  SD_DAT  SD_DAT3  SD_CMD  SD_CLK  Signal Name  TDI  TCS	PIN_G6  FPGA Pin No.  PIN_J1  PIN_J2  PIN_H1  PIN_H2  PIN_J3  PIN_H4  PIN_H3  PIN_K4  PIN_K3  PIN_K4  PIN_K2  FPGA Pin No.  PIN_AD24  PIN_AC23  PIN_Y21  PIN_AD25  FPGA Pin No.  PIN_B14  PIN_B14  PIN_B14  PIN_A14	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF  Description  SD Card Data 3  SD Card Command Signal  SD Card Clock  Description  CPLD -> FPGA (Data in)  CPLD -> FPGA (CS)
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_EN  LCD_BLON  Signal Name  SD_DAT  SD_DAT3  SD_CMD  SD_CLK  Signal Name  TDI  TCS  TCK  TDO	PIN_G6  FPGA Pin No.  PIN_J1  PIN_J2  PIN_H1  PIN_H2  PIN_J3  PIN_H4  PIN_H3  PIN_K4  PIN_K3  PIN_K4  PIN_K5  PIN_K4  PIN_L4  PIN_L4  PIN_L4  PIN_L4  PIN_L4  PIN_L5  FPGA Pin No.  PIN_AD24  PIN_AC23  PIN_Y21  PIN_AD25  FPGA Pin No.  PIN_B14  PIN_B14  PIN_B14  PIN_B14  PIN_B14  PIN_F14	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF  Description  SD Card Data 3  SD Card Command Signal  SD Card Clock  Description  CPLD -> FPGA (Data in)  CPLD -> FPGA (CIOck)
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_EN  LCD_RS  LCD_ON  LCD_BLON  Signal Name  SD_DAT  SD_DAT3  SD_CMD  SD_CLK  Signal Name  TDI  TCS  TCK	PIN_G6  FPGA Pin No.  PIN_J1  PIN_J2  PIN_H1  PIN_H2  PIN_J3  PIN_H4  PIN_H3  PIN_K4  PIN_K3  PIN_K4  PIN_K5  PIN_L4  PIN_L4  PIN_L4  PIN_L4  PIN_AD24  PIN_AC23  PIN_AC23  PIN_Y21  PIN_AD25  FPGA Pin No.  PIN_B14  PIN_B14  PIN_B14  PIN_B14  PIN_B14  PIN_D14	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Power ON/OFF  LCD Back Light ON/OFF  Description  SD Card Data 3  SD Card Command Signal  SD Card Clock  Description  CPLD -> FPGA (CS)  CPLD -> FPGA (Clock)  FPGA -> CPLD (Data out)
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_EN  LCD_BLON  Signal Name  SD_DAT  SD_DAT3  SD_CMD  SD_CLK  Signal Name  TDI  TCS  TCK  TDO  Signal Name	PIN_G6  FPGA Pin No.  PIN_J1  PIN_J2  PIN_H1  PIN_H2  PIN_J4  PIN_J3  PIN_H4  PIN_H3  PIN_K4  PIN_K3  PIN_K4  PIN_K2  FPGA Pin No.  PIN_AD24  PIN_AC23  PIN_AC23  PIN_Y21  PIN_AD25  FPGA Pin No.  PIN_B14  PIN_B14  PIN_B14  PIN_D14  PIN_D14  PIN_D14  PIN_F14  FPGA Pin No.	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF  Description  SD Card Data 3  SD Card Command Signal  SD Card Clock  Description  CPLD -> FPGA (Data in)  CPLD -> FPGA (Clock)  FPGA -> CPLD (Data out)  Description
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_EN  LCD_BLON  Signal Name  SD_DAT3  SD_CMD  SD_CLK  Signal Name  TDI  TCS  TCK  TDO  Signal Name  IRDA_TXD	PIN_G6  FPGA Pin No.  PIN_J1  PIN_J2  PIN_H1  PIN_H2  PIN_J4  PIN_J3  PIN_H4  PIN_H3  PIN_K4  PIN_K3  PIN_K4  PIN_K3  PIN_K4  PIN_L4  PIN_L4  PIN_L4  PIN_L4  PIN_AD24  PIN_AC23  PIN_AC23  PIN_Y21  PIN_AD25  FPGA Pin No.  PIN_B14  PIN_B14  PIN_B14  PIN_B14  PIN_B14  PIN_F14  FPGA Pin No.  PIN_F14  FPGA Pin No.	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF  Description  SD Card Data 3  SD Card Command Signal  SD Card Clock  Description  CPLD -> FPGA (Data in)  CPLD -> FPGA (Clock)  FPGA -> CPLD (Data out)  Description  IRDA Transmitter
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_EN  LCD_BLON  Signal Name  SD_DAT  SD_DAT3  SD_CMD  SD_CLK  Signal Name  TDI  TCS  TCK  TDO  Signal Name  IRDA_TXD  IRDA_RXD	PIN_G6  FPGA Pin No.  PIN_J1  PIN_J2  PIN_H1  PIN_H2  PIN_J4  PIN_J3  PIN_H4  PIN_H3  PIN_K4  PIN_K3  PIN_K4  PIN_K3  PIN_K4  PIN_L4  PIN_L4  PIN_L4  PIN_AC23  PIN_AC23  PIN_AC23  PIN_Y21  PIN_AD25  FPGA Pin No.  PIN_B14  PIN_B14  PIN_B14  PIN_B14  PIN_B14  PIN_F14  FPGA Pin No.  PIN_AE24  PIN_AE25	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF  Description  SD Card Data 3  SD Card Command Signal  SD Card Clock  Description  CPLD -> FPGA (Data in)  CPLD -> FPGA (Clock)  FPGA -> CPLD (Data out)  Description  IRDA Transmitter  IRDA Receiver
OTG_LSPEED  Signal Name  LCD_DATA[0]  LCD_DATA[1]  LCD_DATA[2]  LCD_DATA[3]  LCD_DATA[4]  LCD_DATA[5]  LCD_DATA[6]  LCD_DATA[7]  LCD_RW  LCD_EN  LCD_EN  LCD_BLON  Signal Name  SD_DAT  SD_DAT3  SD_CMD  SD_CLK  Signal Name  TDI  TCS  TCK  TDO  Signal Name  IRDA_TXD  IRDA_RXD  Signal Name	PIN_G6  FPGA Pin No.  PIN_J1  PIN_J2  PIN_H1  PIN_H2  PIN_J3  PIN_H4  PIN_H3  PIN_K4  PIN_K4  PIN_K3  PIN_K4  PIN_K4  PIN_L4  PIN_L4  PIN_L4  PIN_AC23  PIN_AC23  PIN_AC23  PIN_AD25  FPGA Pin No.  PIN_AD25  FPGA Pin No.  PIN_B14  PIN_B14	USB Low Speed, 0 = Enable, Z = Disable  Description  LCD Data[0]  LCD Data[1]  LCD Data[2]  LCD Data[3]  LCD Data[4]  LCD Data[5]  LCD Data[6]  LCD Data[7]  LCD Read/Write Select, 0 = Write, 1 = Read  LCD Enable  LCD Enable  LCD Command/Data Select, 0 = Command, 1 = Data  LCD Power ON/OFF  LCD Back Light ON/OFF  Description  SD Card Data 3  SD Card Data 3  SD Card Command Signal  SD Card Clock  Description  CPLD -> FPGA (Data in)  CPLD -> FPGA (CS)  CPLD -> FPGA (Clock)  FPGA -> CPLD (Data out)  Description  IRDA Transmitter  IRDA Receiver  Description

HEXQIS			
HEXQ 4  PIN AE11   Seven Segment Digital Q 4      HEXT 0  PIN_V14   Seven Segment Digital Q 5      HEXT 0  PIN_V20   Seven Segment Digital Q 5      HEXT 11 PIN_V21   Seven Segment Digital Q 5      HEXT 12 PIN_W21   Seven Segment Digital 1 11     HEXT 2  PIN_W21   Seven Segment Digital 1 2      HEXT 3  PIN_V22   Seven Segment Digital 1 2      HEXT 4  PIN_AA24   Seven Segment Digital 1 3      HEXT 4  PIN_AA24   Seven Segment Digital 1 5      HEXT 5  PIN_AA23   Seven Segment Digital 1 5      HEXT 6  PIN_AA24   Seven Segment Digital 1 5      HEXT 6  PIN_AB24   Seven Segment Digital 1 5      HEXT 2  PIN_A22   Seven Segment Digital 2 5      HEX2 2  PIN_AC25   Seven Segment Digital 2 5      HEX2 2  PIN_AC25   Seven Segment Digital 2 5      HEX2 3  PIN_AC26   Seven Segment Digital 2 5      HEX2 3  PIN_AC26   Seven Segment Digital 2 5      HEX2 4  PIN_AB26   Seven Segment Digital 2 5      HEX2 4  PIN_AB26   Seven Segment Digital 2 5      HEX2 6  PIN_Y24   Seven Segment Digital 2 5      HEX2 6  PIN_Y24   Seven Segment Digital 2 5      HEX2 10 PIN_AB26   Seven Segment Digital 2 5      HEX2 2  PIN_AA26   Seven Segment Digital 2 5      HEX3 1  PIN_AA26   Seven Segment Digital 3 5      HEX3 1  PIN_AA26   Seven Segment Digital 3 5      HEX3 1  PIN_AA26   Seven Segment Digital 3 5      HEX3 4  PIN_Y26   Seven Segment Digital 3 5      HEX3 4  PIN_Y26   Seven Segment Digital 3 5      HEX3 6  PIN_W24   Seven Segment Digital 3 5      HEX4 6  PIN_T25   Seven Segment Digital 3 5      HEX4 6  PIN_T3   Seven Segment Digital 3 5      HEX4 6  PIN_T3   Seven Segment Digital 4 6      HEX6 6  PIN_M3   Seven Segment Digital 4 6      HEX6 6  PIN_M3   Seven Segment Digital 4 6      HEX6 6  PIN_M3   Seven Segment Digital 4 6      HEX6 6  PIN_M4   Seven Seg	HEX0[2]	PIN_AC12	Seven Segment Digital 0[2]
HEXQ S  PIN_V13   Seven Segment Digital Q S	HEX0[3]	PIN_AD11	Seven Segment Digital 0[3]
HEXQ  6    PIN. V13	HEX0[4]	PIN_AE11	Seven Segment Digital 0[4]
HEXT(10  PIN. V21   Seven Segment Digital 1(1)     HEXT(12  PIN. V21   Seven Segment Digital 1(1)     HEXT(12  PIN. V21   Seven Segment Digital 1(1)     HEXT(13  PIN. V22   Seven Segment Digital 1(2)     HEXT(13  PIN. V22   Seven Segment Digital 1(3)     HEXT(16  PIN. AA24   Seven Segment Digital 1(4)     HEXT(16  PIN. AA24   Seven Segment Digital 1(16)     HEXT(16  PIN. AB24   Seven Segment Digital 1(16)     HEXT(16  PIN. AB24   Seven Segment Digital 1(16)     HEX2(0) PIN. AB24   Seven Segment Digital 1(16)     HEX2(11  PIN. V22   Seven Segment Digital 2(1)     HEX2(2) PIN. AC25   Seven Segment Digital 2(1)     HEX2(2) PIN. AC25   Seven Segment Digital 2(2)     HEX2(3) PIN. AC26   Seven Segment Digital 2(2)     HEX2(4) PIN. AB25   Seven Segment Digital 2(2)     HEX2(5) PIN. AB25   Seven Segment Digital 2(2)     HEX2(6) PIN. V24   Seven Segment Digital 2(2)     HEX2(6) PIN. V24   Seven Segment Digital 2(2)     HEX3(0) PIN. V24   Seven Segment Digital 2(2)     HEX3(1) PIN. V25   Seven Segment Digital 3(2)     HEX3(1) PIN. V26   Seven Segment Digital 3(2)     HEX3(2) PIN. V26   Seven Segment Digital 3(2)     HEX3(3) PIN. V26   Seven Segment Digital 3(3)     HEX3(4) PIN. V25   Seven Segment Digital 3(3)     HEX3(4) PIN. V25   Seven Segment Digital 3(3)     HEX3(4) PIN. V26   Seven Segment Digital 3(4)     HEX4(6) PIN. U22   Seven Segment Digital 3(4)     HEX4(6) PIN. U24   Seven Segment Digital 3(4)     HEX4(1) PIN. U2   Seven Segment Digital 3(4)     HEX4(2) PIN. U2   Seven Segment Digital 4(1)     HEX4(2) PIN. U3   Seven Segment Digital 4(1)     HEX4(2) PIN. U3   Seven Segment Digital 4(1)     HEX4(3) PIN. V3   Seven Segment Digital 4(1)     HEX4(4) PIN. V3   Seven Segment Digital 4(1)     HEX4(6) PIN. V3   Seven Segment	HEX0[5]	PIN_V14	Seven Segment Digital 0[5]
HEXT(10)	HEX0[6]	PIN_V13	Seven Segment Digital 0[6]
HEXT     PIN \ \frac{\text{Y} }{2}   PIN \ \text{W2}  Seven Segment Digital 1       HEXT     PIN \ \text{Y}  Seven Segment Digital 1       HEXT     PIN \ \text{A2}  Seven Segment Digital 1       HEX2     PIN \ \text{A2}  Seven Segment Digital 2       HEX3     PIN \ \text{A2}  Seven Segment Digital 2       HEX3     PIN \ \text{A2}  Seven Segment Digital 3       HEX4     PIN \ \text{A2}  Seven Segment Digital 3       HEX4     PIN \ \text{A2}  Seven Segment Digital 4       HEX4     PIN \ \text{A2}  Seven Segment Digital 4       HEX4     PIN \ \text{A2}  Seven Segment Digital 4       HEX4     PIN \ \text{A3}  Seven Segment Digital 4       H		PIN V20	Seven Segment Digital 1[0]
HEX1 2  PIN V21   Seven Segment Digital 1 2		_	0 0 11
HEX1     PIN_A24			
HEX1[4]			
HEX1 6  PIN_AB24   Seven Segment Digital 1 5			
HEX16  PIN AB24   Seven Segment Digital 1(6)		_	5 5 1
HEX2[1]			
HEX2[1]			
HEX2 2  PIN_AC25   Seven Segment Digital 2 2  HEX2 3  PIN_AC26   Seven Segment Digital 2 3  HEX2 4  PIN_AB26   Seven Segment Digital 2 3  HEX2 4  PIN_AB26   Seven Segment Digital 2 5  HEX2 6  PIN_Y24   Seven Segment Digital 2 5  PIN_Y24   Seven Segment Digital 2 5  PIN_Y24   Seven Segment Digital 3 0  PIN_Y23   Seven Segment Digital 3 0  PIN_Y23   Seven Segment Digital 3 1  PIN_AA25   Seven Segment Digital 3 1  PIN_AA26   Seven Segment Digital 3 1  PIN_Y26   Seven Segment Digital 3 2  PIN_AA26   Seven Segment Digital 3 3  PIN_Y26   Seven Segment Digital 3 5  PIN_U22   Seven Segment Digital 3 6  PIN_W24   Seven Segment Digital 3 6  PIN_W24   Seven Segment Digital 4 0  PIN_W29   Seven Segment Digital 4 1  PIN_U9   Seven Segment Digital 4 1  PIN_U9   Seven Segment Digital 4 1  PIN_U1   Seven Segment Digital 4 1  PIN_W24   Seven Segment Digital 4 1  PIN_W24   Seven Segment Digital 4 1  PIN_W39   PIN_W39		_	
HEX2[3]	HEX2[1]	PIN_V22	
HEX2[4]	HEX2[2]	PIN_AC25	Seven Segment Digital 2[2]
HEX2[5]   PIN_AB25   Seven Segment Digital 2[5]     HEX3[0]   PIN_Y24   Seven Segment Digital 2[6]     HEX3[0]   PIN_Y23   Seven Segment Digital 3[0]     HEX3[1]   PIN_AA25   Seven Segment Digital 3[0]     HEX3[2]   PIN_AA26   Seven Segment Digital 3[1]     HEX3[2]   PIN_Y26   Seven Segment Digital 3[3]     HEX3[3]   PIN_Y26   Seven Segment Digital 3[3]     HEX3[4]   PIN_Y26   Seven Segment Digital 3[3]     HEX3[5]   PIN_U22   Seven Segment Digital 3[6]     HEX3[6]   PIN_W24   Seven Segment Digital 3[6]     HEX3[6]   PIN_W24   Seven Segment Digital 3[6]     HEX4[0]   PIN_U2   Seven Segment Digital 3[6]     HEX4[1]   PIN_U1   Seven Segment Digital 4[0]     HEX4[1]   PIN_U1   Seven Segment Digital 4[1]     HEX4[2]   PIN_U2   Seven Segment Digital 4[2]     HEX4[3]   PIN_T4   Seven Segment Digital 4[2]     HEX4[4]   PIN_R7   Seven Segment Digital 4[3]     HEX4[4]   PIN_R7   Seven Segment Digital 4[4]     HEX4[5]   PIN_R6   Seven Segment Digital 4[6]     HEX4[6]   PIN_T3   Seven Segment Digital 4[6]     HEX5[0]   PIN_T2   Seven Segment Digital 4[6]     HEX5[0]   PIN_P6   Seven Segment Digital 5[0]     HEX5[1]   PIN_P6   Seven Segment Digital 5[2]     HEX5[2]   PIN_P7   Seven Segment Digital 5[2]     HEX5[3]   PIN_T9   Seven Segment Digital 5[2]     HEX5[4]   PIN_R5   Seven Segment Digital 5[2]     HEX5[4]   PIN_R5   Seven Segment Digital 5[3]     HEX5[4]   PIN_R6   Seven Segment Digital 5[3]     HEX5[6]   PIN_R4   Seven Segment Digital 5[3]     HEX5[6]   PIN_R4   Seven Segment Digital 5[6]     HEX5[6]   PIN_R9   Seven Segment Digital 5[6]     HEX5[6]   PIN_R9   Seven Segment Digital 5[6]     HEX5[6]   PIN_R9   Seven Segment Digital 5[6]     HEX5[6]   PIN_N6   Seven Segment Digital 5[6]     HEX5[6]   PIN_M9   Seven Segment Digital 5[6]     HEX5[6]   PIN_M6   Seven Segment Digital 5[6]     HEX5[6]   PIN_M9   Seven Segme	HEX2[3]	PIN_AC26	Seven Segment Digital 2[3]
HEX2[6]   PIN_Y24   Seven Segment Digital 2[6]   HEX3[0]   PIN_Y23   Seven Segment Digital 3[0]   HEX3[1]   PIN_AA25   Seven Segment Digital 3[1]   HEX3[2]   PIN_AA26   Seven Segment Digital 3[2]   HEX3[3]   PIN_Y26   Seven Segment Digital 3[2]   HEX3[3]   PIN_Y26   Seven Segment Digital 3[3]   HEX3[4]   PIN_Y25   Seven Segment Digital 3[4]   HEX3[5]   PIN_U22   Seven Segment Digital 3[6]   HEX3[6]   PIN_W24   Seven Segment Digital 3[6]   HEX3[6]   PIN_W24   Seven Segment Digital 3[6]   HEX3[6]   PIN_W24   Seven Segment Digital 3[6]   HEX3[6]   PIN_U9   Seven Segment Digital 4[0]   HEX4[1]   PIN_U1   Seven Segment Digital 4[1]   HEX4[1]   PIN_U1   Seven Segment Digital 4[1]   HEX4[2]   PIN_U2   Seven Segment Digital 4[2]   HEX4[3]   PIN_T4   Seven Segment Digital 4[2]   HEX4[4]   PIN_R7   Seven Segment Digital 4[3]   HEX4[4]   PIN_R7   Seven Segment Digital 4[6]   HEX4[6]   PIN_R6   Seven Segment Digital 4[6]   HEX4[6]   PIN_T3   Seven Segment Digital 4[6]   HEX5[1]   PIN_R6   Seven Segment Digital 4[6]   HEX5[1]   PIN_R6   Seven Segment Digital 5[1]   HEX5[1]   PIN_R6   Seven Segment Digital 5[1]   HEX5[2]   PIN_P7   Seven Segment Digital 5[1]   HEX5[3]   PIN_T9   Seven Segment Digital 5[1]   HEX5[4]   PIN_R5   Seven Segment Digital 5[2]   HEX5[3]   PIN_R8   Seven Segment Digital 5[3]   HEX5[4]   PIN_R5   Seven Segment Digital 5[4]   HEX5[6]   PIN_R1   Seven Segment Digital 5[6]   HEX5[6]   PIN_R2   Seven Segment Digital 5[6]   HEX5[6]   PIN_R1   Seven Segment Digital 5[6]   HEX5[6]   PIN_R2   Seven Segment Digital 5[6]   HEX5[6]   PIN_R1   Seven Segment Digital 5[6]   HEX5[6]   PIN_R2   Seven Segment Digital 5[6]   HEX5[6]   PIN_R4   Seven Segment Digital 5[6]   HEX5[6]   PIN_R2   Seven Segment Digital 5[6]   HEX5[6]   PIN_R4   Seven Segment Digital 5[6]   HEX5[6]   PIN_R5   Seven Segment Digital 5[6]   HEX5[6]   PIN_R4   Seven Segment Digital 5[6]   HEX5[6]   PIN_R5   Seven Segment Digital 5[6]   HEX5[6]   PIN_R5   Seven Segment Digital 7[6]   HEX5[6]   PIN_R5   Seven Segment Digital 7[6]   HEX5[6]   PIN_R	HEX2[4]	PIN_AB26	Seven Segment Digital 2[4]
HEX2[6]   PIN_Y24   Seven Segment Digital 2[6]   HEX3[0]   PIN_Y23   Seven Segment Digital 3[0]   HEX3[1]   PIN_AA25   Seven Segment Digital 3[1]   HEX3[2]   PIN_AA26   Seven Segment Digital 3[2]   HEX3[3]   PIN_Y26   Seven Segment Digital 3[2]   HEX3[3]   PIN_Y26   Seven Segment Digital 3[3]   HEX3[4]   PIN_Y25   Seven Segment Digital 3[4]   HEX3[5]   PIN_U22   Seven Segment Digital 3[6]   HEX3[6]   PIN_W24   Seven Segment Digital 3[6]   HEX3[6]   PIN_W24   Seven Segment Digital 3[6]   HEX3[6]   PIN_W24   Seven Segment Digital 3[6]   HEX3[6]   PIN_U9   Seven Segment Digital 4[0]   HEX4[1]   PIN_U1   Seven Segment Digital 4[1]   HEX4[1]   PIN_U1   Seven Segment Digital 4[1]   HEX4[2]   PIN_U2   Seven Segment Digital 4[2]   HEX4[3]   PIN_T4   Seven Segment Digital 4[2]   HEX4[4]   PIN_R7   Seven Segment Digital 4[3]   HEX4[4]   PIN_R7   Seven Segment Digital 4[6]   HEX4[6]   PIN_R6   Seven Segment Digital 4[6]   HEX4[6]   PIN_T3   Seven Segment Digital 4[6]   HEX5[1]   PIN_R6   Seven Segment Digital 4[6]   HEX5[1]   PIN_R6   Seven Segment Digital 5[1]   HEX5[1]   PIN_R6   Seven Segment Digital 5[1]   HEX5[2]   PIN_P7   Seven Segment Digital 5[1]   HEX5[3]   PIN_T9   Seven Segment Digital 5[1]   HEX5[4]   PIN_R5   Seven Segment Digital 5[2]   HEX5[3]   PIN_R8   Seven Segment Digital 5[3]   HEX5[4]   PIN_R5   Seven Segment Digital 5[4]   HEX5[6]   PIN_R1   Seven Segment Digital 5[6]   HEX5[6]   PIN_R2   Seven Segment Digital 5[6]   HEX5[6]   PIN_R1   Seven Segment Digital 5[6]   HEX5[6]   PIN_R2   Seven Segment Digital 5[6]   HEX5[6]   PIN_R1   Seven Segment Digital 5[6]   HEX5[6]   PIN_R2   Seven Segment Digital 5[6]   HEX5[6]   PIN_R4   Seven Segment Digital 5[6]   HEX5[6]   PIN_R2   Seven Segment Digital 5[6]   HEX5[6]   PIN_R4   Seven Segment Digital 5[6]   HEX5[6]   PIN_R5   Seven Segment Digital 5[6]   HEX5[6]   PIN_R4   Seven Segment Digital 5[6]   HEX5[6]   PIN_R5   Seven Segment Digital 5[6]   HEX5[6]   PIN_R5   Seven Segment Digital 7[6]   HEX5[6]   PIN_R5   Seven Segment Digital 7[6]   HEX5[6]   PIN_R	HEX2[5]	PIN_AB25	Seven Segment Digital 2[5]
HEX3 0  PIN_Y23   Seven Segment Digital 3 0  HEX3 1  PIN_AA25   Seven Segment Digital 3 1  HEX3 2  PIN_AA26   Seven Segment Digital 3 2  HEX3 2  PIN_Y26   Seven Segment Digital 3 2  HEX3 3  PIN_Y26   Seven Segment Digital 3 3  HEX3 4  PIN_Y25   Seven Segment Digital 3 3  HEX3 4  PIN_Y25   Seven Segment Digital 3 4  HEX3 5  PIN_U22   Seven Segment Digital 3 6  HEX4 0  PIN_U9   Seven Segment Digital 3 6  HEX4 0  PIN_U9   Seven Segment Digital 3 6  HEX4 1  PIN_U1   Seven Segment Digital 4 0  HEX4 1  PIN_U1   Seven Segment Digital 4 1  HEX4 2  PIN_U2   Seven Segment Digital 4 2  HEX4 3  PIN_T4   Seven Segment Digital 4 2  HEX4 3  PIN_T4   Seven Segment Digital 4 3  HEX4 4  PIN_R7   Seven Segment Digital 4 4  HEX4 5  PIN_R6   Seven Segment Digital 4 6  HEX4 6  PIN_T3   Seven Segment Digital 4 6  HEX4 6  PIN_T3   Seven Segment Digital 4 6  HEX5 0  PIN_T2   Seven Segment Digital 5 0  HEX5 1  PIN_P6   Seven Segment Digital 5 1  HEX5 2  PIN_P7   Seven Segment Digital 5 2  HEX5 3  PIN_T9   Seven Segment Digital 5 2  HEX5 3  PIN_T9   Seven Segment Digital 5 3  HEX5 4  PIN_R5   Seven Segment Digital 5 3  HEX5 4  PIN_R5   Seven Segment Digital 5 3  HEX5 6  PIN_R3   Seven Segment Digital 5 4  HEX5 6  PIN_R3   Seven Segment Digital 5 6  HEX6 0  PIN_R3   Seven Segment Digital 5 6  HEX6 0  PIN_R3   Seven Segment Digital 5 6  HEX6 0  PIN_R3   Seven Segment Digital 6 0  HEX6 1  PIN_P4   Seven Segment Digital 6 0  HEX6 1  PIN_P4   Seven Segment Digital 6 0  HEX6 1  PIN_P4   Seven Segment Digital 6 0  HEX6 1  PIN_P3   Seven Segment Digital 6 0  HEX6 1  PIN_P4   Seven Segment Digital 6 0  HEX6 1  PIN_P3   Seven Segment Digital 6 0  HEX6 1  PIN_P4   Seven Segment Digital 6 0  HEX6 1  PIN_P3   Seven Segment Digital 6 0  HEX6 1  PIN_P4   Seven Segment Digital 6 0  HEX6 1  PIN_P5   Seven Segment Digital 7 0  HEX6 1  PIN_P6   Seven Segment Digital 7 0  HEX6 1  PIN_P9   Seven Segment Digital 7 0  HEX7 1  PIN_L2   Seven Segment Digital 7 0  HEX7 1  PIN_L6   Seven Segment Digital 7 0  HEX7 1  PIN_L9   Seven Segment Digital 7 0  HEX7 1  PI			
HEX3 1  PIN_AA25   Seven Segment Digital 3 1  HEX3 2  PIN_A26   Seven Segment Digital 3 2  HEX3 3  PIN_Y26   Seven Segment Digital 3 3  HEX3 4  PIN_Y25   Seven Segment Digital 3 3  HEX3 4  PIN_Y25   Seven Segment Digital 3 3  HEX3 5  PIN_U22   Seven Segment Digital 3 5  HEX3 6  PIN_W24   Seven Segment Digital 3 6  HEX3 6  PIN_W24   Seven Segment Digital 3 6  HEX4 0  PIN_U9   Seven Segment Digital 4 0  HEX4 1  PIN_U1   Seven Segment Digital 4 0  HEX4 1  PIN_U1   Seven Segment Digital 4 1  HEX4 2  PIN_U2   Seven Segment Digital 4 2  HEX4 3  PIN_T4   Seven Segment Digital 4 3  HEX4 4  PIN_T6   Seven Segment Digital 4 5  HEX4 6  PIN_T7   Seven Segment Digital 4 5  HEX4 6  PIN_T8   Seven Segment Digital 4 5  HEX4 6  PIN_T3   Seven Segment Digital 4 6  HEX5 0  PIN_T1   Seven Segment Digital 5 0  HEX5 1  PIN_P6   Seven Segment Digital 5 0  HEX5 1  PIN_P6   Seven Segment Digital 5 0  HEX5 2  PIN_P7   Seven Segment Digital 5 0  HEX5 3  PIN_T9   Seven Segment Digital 5 3  HEX5 4  PIN_T8   Seven Segment Digital 5 3  HEX5 6  PIN_T8   Seven Segment Digital 5 3  HEX5 6  PIN_T8   Seven Segment Digital 5 3  HEX5 6  PIN_T8   Seven Segment Digital 5 4  HEX5 6  PIN_T8   Seven Segment Digital 5 6  HEX6 6  PIN_T8   Seven Segment Digital 5 6  HEX6 6  PIN_T8   Seven Segment Digital 5 6  HEX6 6  PIN_P3   Seven Segment Digital 6 6  HEX6 6  PIN_P3   Seven Segment Digital 6 6  HEX6 6  PIN_M3   Seven Segment Digital 6 6  HEX6 6  PIN_M4   Seven Segment Digital 6 6  HEX6 6  PIN_M5   Seven Segment Digital 6 6  HEX7 0  PIN_L9   Seven Segment Digital 6 6  HEX7 0  PIN_L9   Seven Segment Digital 6 6  HEX7 1  PIN_L9   Seven Segment Digital 7 0  HEX6 6  PIN_M5   Seven Segment Digital 7 0  HEX7 1  PIN_L9   Seven Segment Digital 7 0  HEX7 1  PIN			
HEX3[2]		=	
HEX3 3  PIN_Y26   Seven Segment Digital 3 3      HEX3 4  PIN_Y25   Seven Segment Digital 3 4      HEX3 5  PIN_U22   Seven Segment Digital 3 5      HEX3 6  PIN_W24   Seven Segment Digital 3 6      HEX4 0  PIN_U9   Seven Segment Digital 3 6      HEX4 0  PIN_U9   Seven Segment Digital 4 0      HEX4 1 PIN_U1   Seven Segment Digital 4 2      HEX4 2   PIN_U2   Seven Segment Digital 4 2      HEX4 3   PIN_T4   Seven Segment Digital 4 2      HEX4 4   PIN_R7   Seven Segment Digital 4 4      HEX4 5   PIN_R6   Seven Segment Digital 4 5      HEX4 6   PIN_T3   Seven Segment Digital 4 6      HEX5 0   PIN_T3   Seven Segment Digital 4 6      HEX5 1   PIN_P6   Seven Segment Digital 5 0      HEX5 1   PIN_P6   Seven Segment Digital 5 1      HEX5 2   PIN_P7   Seven Segment Digital 5 2      HEX5 3   PIN_T9   Seven Segment Digital 5 2      HEX5 4   PIN_R5   Seven Segment Digital 5 3      HEX5 5   PIN_R6   Seven Segment Digital 5 4      HEX5 6   PIN_R8   Seven Segment Digital 5 4      HEX5 6   PIN_R8   Seven Segment Digital 5 6      HEX5 6   PIN_R4   Seven Segment Digital 5 6      HEX5 6   PIN_R4   Seven Segment Digital 5 6      HEX6 0   PIN_R2   Seven Segment Digital 6 0      HEX6 1   PIN_P4   Seven Segment Digital 6 0      HEX6 2   PIN_P3   Seven Segment Digital 6 1     HEX6 3   PIN_M3   Seven Segment Digital 6 1     HEX6 4   PIN_M3   Seven Segment Digital 6 3     HEX6 5   PIN_M4   Seven Segment Digital 6 3     HEX6 6   PIN_M3   Seven Segment Digital 6 3     HEX6 6   PIN_M5   Seven Segment Digital 6 3     HEX7 1   PIN_L2   Seven Segment Digital 7 1     HEX7 2   PIN_L3   Seven Segment Digital 7 1     HEX7 3   PIN_L6   Seven Segment Digital 7 3     HEX7 4   PIN_L7   Seven Segment Digital 7 3     HEX7 5   PIN_P9   Seven Segment Digital 7 3     HEX7 6   PIN_N9   Seven Segment Digital 7 5     HEX7 6   PIN_N9   Seven Segment Digital 7 5     HEX7 6   PIN_N9   Seven Segment Digital 7 5     HEX7 6   PIN_N9   Seven Segment Digital 7 6     Signal Name   FPGA Pin No.   Description     LEDRed 1   LED Red 2		_	
HEX3[4]		_	
HEX3[6]			
HEX3[6]		=	0 0 11
HEX4[0]			
HEX4[1]	HEX3[6]	PIN_W24	Seven Segment Digital 3[6]
HEX4[2]	HEX4[0]	PIN_U9	Seven Segment Digital 4[0]
HEX4[3]	HEX4[1]	PIN_U1	Seven Segment Digital 4[1]
HEX4[4]	HEX4[2]	PIN_U2	Seven Segment Digital 4[2]
HEX4[4]	HEX4[3]	PIN T4	Seven Segment Digital 4[3]
HEX4[5]			5 5 1
HEX4[6]		_	
HEX5[0]		_	
HEX5[1]		_	<u> </u>
HEX5[2]		_	<u> </u>
HEX5[3]		_	
HEX5[4]		-	
HEX5[5]   PIN_R4   Seven Segment Digital 5[5]     HEX5[6]   PIN_R3   Seven Segment Digital 5[6]     HEX6[0]   PIN_R2   Seven Segment Digital 6[0]     HEX6[1]   PIN_P4   Seven Segment Digital 6[1]     HEX6[2]   PIN_P3   Seven Segment Digital 6[2]     HEX6[3]   PIN_M2   Seven Segment Digital 6[3]     HEX6[4]   PIN_M3   Seven Segment Digital 6[3]     HEX6[5]   PIN_M5   Seven Segment Digital 6[4]     HEX6[6]   PIN_M4   Seven Segment Digital 6[6]     HEX7[0]   PIN_L3   Seven Segment Digital 7[0]     HEX7[1]   PIN_L2   Seven Segment Digital 7[0]     HEX7[2]   PIN_L9   Seven Segment Digital 7[2]     HEX7[3]   PIN_L6   Seven Segment Digital 7[2]     HEX7[4]   PIN_L7   Seven Segment Digital 7[3]     HEX7[5]   PIN_P9   Seven Segment Digital 7[4]     HEX7[6]   PIN_N9   Seven Segment Digital 7[6]     Signal Name   FPGA Pin No.   Description     KEY[0]   PIN_D23   Push Button[0]     KEY[1]   PIN_W26   Push Button[1]     KEY[2]   PIN_W26   Push Button[2]     KEY[3]   PIN_W26   Push Button[3]     Signal Name   FPGA Pin No.   Description     LEDR[0]   PIN_AE23   LED Red[1]     LEDR[1]   PIN_AE23   LED Red[1]     LEDR[2]   PIN_AB21   LED Red[2]		_	
HEX5[6]		PIN_R5	Seven Segment Digital 5[4]
HEX6[0]	HEX5[5]	PIN_R4	Seven Segment Digital 5[5]
HEX6[1]	HEX5[6]	PIN_R3	Seven Segment Digital 5[6]
HEX6[2]	HEX6[0]	PIN_R2	Seven Segment Digital 6[0]
HEX6[2]	HEX6[1]	PIN_P4	Seven Segment Digital 6[1]
HEX6[3]	HEX6[2]	PIN P3	
HEX6[4]			0 0 11
HEX6[5]			
HEX6[6]			
HEX7[0]   PIN_L3   Seven Segment Digital 7[0]     HEX7[1]   PIN_L2   Seven Segment Digital 7[1]     HEX7[2]   PIN_L9   Seven Segment Digital 7[2]     HEX7[3]   PIN_L6   Seven Segment Digital 7[3]     HEX7[4]   PIN_L7   Seven Segment Digital 7[4]     HEX7[5]   PIN_P9   Seven Segment Digital 7[5]     HEX7[6]   PIN_N9   Seven Segment Digital 7[6]     Signal Name   FPGA Pin No.   Description     KEY[0]   PIN_G26   Push Button[0]     KEY[1]   PIN_N23   Push Button[1]     KEY[2]   PIN_P23   Push Button[2]     KEY[3]   PIN_W26   Push Button[3]     Signal Name   FPGA Pin No.   Description     LEDR[0]   PIN_AE23   LED Red[0]     LEDR[1]   PIN_AF23   LED Red[1]     LEDR[2]   PIN_AB21   LED Red[2]		=	
HEX7[1]		_	
HEX7[2]		_	
HEX7[3]		_	
HEX7[4]         PIN_L7         Seven Segment Digital 7[4]           HEX7[5]         PIN_P9         Seven Segment Digital 7[5]           HEX7[6]         PIN_N9         Seven Segment Digital 7[6]           Signal Name         FPGA Pin No.         Description           KEY[0]         PIN_G26         Push Button[0]           KEY[1]         PIN_N23         Push Button[1]           KEY[2]         PIN_P23         Push Button[2]           KEY[3]         PIN_W26         Push Button[3]           Signal Name         FPGA Pin No.         Description           LEDR[0]         PIN_AE23         LED Red[0]           LEDR[1]         PIN_AF23         LED Red[1]           LEDR[2]         PIN_AB21         LED Red[2]			
HEX7[5]   PIN_P9   Seven Segment Digital 7[5]     HEX7[6]   PIN_N9   Seven Segment Digital 7[6]     Signal Name   FPGA Pin No.   Description     KEY[0]   PIN_G26   Push Button[0]     KEY[1]   PIN_N23   Push Button[1]     KEY[2]   PIN_P23   Push Button[2]     KEY[3]   PIN_W26   Push Button[3]     Signal Name   FPGA Pin No.   Description     LEDR[0]   PIN_AE23   LED Red[0]     LEDR[1]   PIN_AF23   LED Red[1]     LEDR[2]   PIN_AB21   LED Red[2]			
HEX7[6]   PIN_N9   Seven Segment Digital 7[6]	HEX7[4]		
Signal Name         FPGA Pin No.         Description           KEY[0]         PIN_G26         Push Button[0]           KEY[1]         PIN_N23         Push Button[1]           KEY[2]         PIN_P23         Push Button[2]           KEY[3]         PIN_W26         Push Button[3]           Signal Name         FPGA Pin No.         Description           LEDR[0]         PIN_AE23         LED Red[0]           LEDR[1]         PIN_AF23         LED Red[1]           LEDR[2]         PIN_AB21         LED Red[2]	HEX7[5]	PIN_P9	Seven Segment Digital 7[5]
KEY[0]         PIN_G26         Push Button[0]           KEY[1]         PIN_N23         Push Button[1]           KEY[2]         PIN_P23         Push Button[2]           KEY[3]         PIN_W26         Push Button[3]           Signal Name         FPGA Pin No.         Description           LEDR[0]         PIN_AE23         LED Red[0]           LEDR[1]         PIN_AF23         LED Red[1]           LEDR[2]         PIN_AB21         LED Red[2]	HEX7[6]	PIN_N9	Seven Segment Digital 7[6]
KEY[0]         PIN_G26         Push Button[0]           KEY[1]         PIN_N23         Push Button[1]           KEY[2]         PIN_P23         Push Button[2]           KEY[3]         PIN_W26         Push Button[3]           Signal Name         FPGA Pin No.         Description           LEDR[0]         PIN_AE23         LED Red[0]           LEDR[1]         PIN_AF23         LED Red[1]           LEDR[2]         PIN_AB21         LED Red[2]	Signal Name	FPGA Pin No.	Description
KEY[1]         PIN_N23         Push Button[1]           KEY[2]         PIN_P23         Push Button[2]           KEY[3]         PIN_W26         Push Button[3]           Signal Name         FPGA Pin No.         Description           LEDR[0]         PIN_AE23         LED Red[0]           LEDR[1]         PIN_AF23         LED Red[1]           LEDR[2]         PIN_AB21         LED Red[2]	KEY[0]		
KEY[2]         PIN_P23         Push Button[2]           KEY[3]         PIN_W26         Push Button[3]           Signal Name         FPGA Pin No.         Description           LEDR[0]         PIN_AE23         LED Red[0]           LEDR[1]         PIN_AF23         LED Red[1]           LEDR[2]         PIN_AB21         LED Red[2]		PIN_N23	
KEY[3]         PIN_W26         Push Button[3]           Signal Name         FPGA Pin No.         Description           LEDR[0]         PIN_AE23         LED Red[0]           LEDR[1]         PIN_AF23         LED Red[1]           LEDR[2]         PIN_AB21         LED Red[2]			
Signal Name         FPGA Pin No.         Description           LEDR[0]         PIN_AE23         LED Red[0]           LEDR[1]         PIN_AF23         LED Red[1]           LEDR[2]         PIN_AB21         LED Red[2]			- 1
LEDR[0]         PIN_AE23         LED Red[0]           LEDR[1]         PIN_AF23         LED Red[1]           LEDR[2]         PIN_AB21         LED Red[2]		_	
LEDR[1]         PIN_AF23         LED Red[1]           LEDR[2]         PIN_AB21         LED Red[2]			·
LEDR[2] PIN_AB21 LED Red[2]		_	
			• •
LEDR[3] PIN_AC22 LED Red[3]			
	LEDK[3]	PIN_AC22	LED Ked[3]

	_	,
LEDR[4]	PIN_AD22	LED Red[4]
LEDR[5]	PIN_AD23	LED Red[5]
LEDR[6]	PIN_AD21	LED Red[6]
LEDR[7]	PIN_AC21	LED Red[7]
LEDR[8]	PIN AA14	LED Red[8]
LEDR[9]	PIN_Y13	LED Red[9]
LEDR[10]	PIN_AA13	LED Red[10]
	_	
LEDR[11]	PIN_AC14	LED Red[11]
LEDR[12]	PIN_AD15	LED Red[12]
LEDR[13]	PIN_AE15	LED Red[13]
LEDR[14]	PIN_AF13	LED Red[14]
LEDR[15]	PIN_AE13	LED Red[15]
LEDR[16]	PIN_AE12	LED Red[16]
LEDR[17]	PIN_AD12	LED Red[17]
LEDG[0]	PIN_AE22	LED Green[0]
LEDG[1]	PIN_AF22	LED Green[1]
LEDG[2]	PIN W19	LED Green[2]
LEDG[3]	PIN_V18	LED Green[2]
LEDG[4]	PIN_U18	LED Green[4]
LEDG[5]	PIN_U17	LED Green[5]
LEDG[6]	PIN_AA20	LED Green[6]
LEDG[7]	PIN_Y18	LED Green[7]
LEDG[8]	PIN_Y12	LED Green[8]
Signal Name	FPGA Pin No.	Description
CLOCK_27	PIN_D13	On Board 27 MHz
CLOCK_50	PIN_N2	On Board 50 MHz
EXT_CLOCK	PIN_P26	External Clock
Signal Name	FPGA Pin No.	Description Description
		UART Receiver
UART_RXD	PIN_C25	
UART_TXD	PIN_B25	UART Transmitter
Signal Name	FPGA Pin No.	Description
PS2_CLK	PIN_D26	PS2 Data
PS2_DAT	PIN_D26 PIN_C24	PS2 Clock
PS2_DAT	PIN_C24	PS2 Clock
PS2_DAT Signal Name	PIN_C24 FPGA Pin No.	PS2 Clock  Description
PS2_DAT Signal Name I2C_SCLK I2C_SDAT	PIN_C24 FPGA Pin No. PIN_A6 PIN_B6	PS2 Clock  Description  I2C Data  I2C Clock
PS2_DAT Signal Name I2C_SCLK I2C_SDAT Signal Name	PIN_C24 FPGA Pin No. PIN_A6 PIN_B6 FPGA Pin No.	PS2 Clock  Description  I2C Data  I2C Clock  Description
PS2_DAT Signal Name I2C_SCLK I2C_SDAT Signal Name TD_DATA[0]	PIN_C24 FPGA Pin No. PIN_A6 PIN_B6 FPGA Pin No. PIN_J9	PS2 Clock  Description  I2C Data  I2C Clock  Description  TV Decoder Data[0]
PS2_DAT Signal Name I2C_SCLK I2C_SDAT Signal Name TD_DATA[0] TD_DATA[1]	PIN_C24 FPGA Pin No. PIN_A6 PIN_B6 FPGA Pin No. PIN_J9 PIN_E8	PS2 Clock  Description  I2C Data  I2C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]	PIN_C24 FPGA Pin No. PIN_A6 PIN_B6 FPGA Pin No. PIN_J9 PIN_E8 PIN_H8	PS2 Clock  Description  I2C Data  I2C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10	PS2 Clock  Description  12C Data  12C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9	PS2 Clock  Description  I2C Data  I2C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9	PS2 Clock  Description  I2C Data  I2C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_D7	PS2 Clock  Description  I2C Data  I2C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9	PS2 Clock  Description  I2C Data  I2C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_D7	PS2 Clock  Description  12C Data  12C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_D7  PIN_C7	PS2 Clock  Description  I2C Data  I2C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]  TD_DATA[7]  TD_DATA[7]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_D7  PIN_C7  PIN_D5	PS2 Clock  Description  I2C Data  I2C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder H_SYNC
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[7]  TD_DATA[7]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_D7  PIN_C7  PIN_D5  PIN_K9	PS2 Clock  Description  I2C Data  I2C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder H_SYNC  TV Decoder V_SYNC
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]  TD_DATA[7]  TD_DATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_D7  PIN_C7  PIN_D5  PIN_K9  PIN_C4	PS2 Clock  Description  I2C Data  I2C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder Data[7]  TV Decoder V_SYNC  TV Decoder Reset
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[6]  TD_DATA[6]  TD_DATA[7]  TD_DATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_D7  PIN_C7  PIN_C7  PIN_D5  PIN_K9  PIN_K9  PIN_C4  FPGA Pin No.  PIN_C8	PS2 Clock  Description  12C Data  12C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder H_SYNC  TV Decoder Reset  Description  VGA Red[0]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[7]  TD_DATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]  VGA_R[1]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H10  PIN_G9  PIN_F9  PIN_C7  PIN_C7  PIN_D5  PIN_K9  PIN_K9  PIN_C4  FPGA Pin No.  PIN_C8  PIN_C8  PIN_C8	PS2 Clock  Description  12C Data  12C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder Data[7]  TV Decoder H_SYNC  TV Decoder V_SYNC  TV Decoder Reset  Description  VGA Red[0]  VGA Red[1]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]  TD_DATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]  VGA_R[1]  VGA_R[2]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_C7  PIN_C7  PIN_D5  PIN_K9  PIN_K9  PIN_C4  FPGA Pin No.  PIN_C8  PIN_C8  PIN_F10  PIN_C10	PS2 Clock  Description  12C Data  12C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder Data[7]  TV Decoder M_SYNC  TV Decoder V_SYNC  TV Decoder Reset  Description  VGA Red[0]  VGA Red[1]  VGA Red[2]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[7]  TD_DATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]  VGA_R[1]  VGA_R[3]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_D7  PIN_C7  PIN_D5  PIN_C7  PIN_D5  PIN_K9  PIN_C4  FPGA Pin No.  PIN_C8  PIN_C4  FPGA Pin No.  PIN_C8  PIN_C8  PIN_C8  PIN_C9  PIN_D9	PS2 Clock  Description  12C Data  12C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder Data[7]  TV Decoder Lesync  TV Decoder Lesync  TV Decoder V_SYNC  TV Decoder Reset  Description  VGA Red[0]  VGA Red[2]  VGA Red[3]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[7]  TD_DATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]  VGA_R[1]  VGA_R[2]  VGA_R[4]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_D7  PIN_C7  PIN_D5  PIN_C7  PIN_D5  PIN_K9  PIN_C4  FPGA Pin No.  PIN_C4  FPGA Pin No.  PIN_C8  PIN_C8  PIN_C8  PIN_C9  PIN_C9	PS2 Clock  Description  12C Data  12C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder Data[7]  TV Decoder H_SYNC  TV Decoder V_SYNC  TV Decoder Reset  Description  VGA Red[0]  VGA Red[1]  VGA Red[3]  VGA Red[4]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]  TD_DATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]  VGA_R[1]  VGA_R[2]  VGA_R[4]  VGA_R[5]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_D7  PIN_C7  PIN_C7  PIN_D5  PIN_K9  PIN_K9  PIN_C4  FPGA Pin No.  PIN_C4  FPGA Pin No.  PIN_C8  PIN_C8  PIN_C9  PIN_C9  PIN_C9  PIN_A10	PS2 Clock  Description  12C Data  12C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder LSYNC  TV Decoder LSYNC  TV Decoder V_SYNC  TV Decoder Reset  Description  VGA Red[0]  VGA Red[1]  VGA Red[3]  VGA Red[4]  VGA Red[5]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]  TD_DATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]  VGA_R[1]  VGA_R[2]  VGA_R[4]  VGA_R[5]  VGA_R[6]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_C7  PIN_C7  PIN_D5  PIN_C4  FPGA Pin No.  PIN_C4  FPGA Pin No.  PIN_C8  PIN_C9  PIN_C9  PIN_C9  PIN_C9  PIN_G10  PIN_D9  PIN_C9  PIN_A8  PIN_H11	PS2 Clock  Description  12C Data  12C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder H_SYNC  TV Decoder V_SYNC  TV Decoder Reset  Description  VGA Red[0]  VGA Red[1]  VGA Red[3]  VGA Red[5]  VGA Red[6]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]  TD_DATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]  VGA_R[1]  VGA_R[2]  VGA_R[4]  VGA_R[6]  VGA_R[6]  VGA_R[6]  VGA_R[7]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_C7  PIN_C7  PIN_C5  PIN_C4  FPGA Pin No.  PIN_C4  FPGA Pin No.  PIN_C8  PIN_F10  PIN_G10  PIN_D9  PIN_C9  PIN_C9  PIN_C9  PIN_A8  PIN_H11  PIN_H12	PS2 Clock    Description     12C Data     12C Clock     Description     TV Decoder Data[0]     TV Decoder Data[1]     TV Decoder Data[2]     TV Decoder Data[3]     TV Decoder Data[4]     TV Decoder Data[5]     TV Decoder Data[6]     TV Decoder Data[7]     TV Decoder Data[7]     TV Decoder H_SYNC     TV Decoder V_SYNC     TV Decoder Reset     Description     VGA Red[0]     VGA Red[1]     VGA Red[4]     VGA Red[5]     VGA Red[6]     VGA Red[6]     VGA Red[7]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]  TD_DATA[7]  TD_BATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]  VGA_R[1]  VGA_R[2]  VGA_R[3]  VGA_R[6]  VGA_R[6]  VGA_R[6]  VGA_R[7]  VGA_R[7]  VGA_R[7]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_C7  PIN_C7  PIN_D5  PIN_C4  FPGA Pin No.  PIN_C4  FPGA Pin No.  PIN_C8  PIN_C9  PIN_C9  PIN_C9  PIN_C9  PIN_G10  PIN_D9  PIN_C9  PIN_A8  PIN_H11	PS2 Clock  Description  12C Data  12C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder H_SYNC  TV Decoder V_SYNC  TV Decoder Reset  Description  VGA Red[0]  VGA Red[1]  VGA Red[3]  VGA Red[4]  VGA Red[6]  VGA Red[6]  VGA Red[7]  VGA Red[7]  VGA Red[7]  VGA Red[7]  VGA Red[8]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]  TD_DATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]  VGA_R[1]  VGA_R[2]  VGA_R[4]  VGA_R[6]  VGA_R[6]  VGA_R[6]  VGA_R[7]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_C7  PIN_C7  PIN_C5  PIN_C4  FPGA Pin No.  PIN_C4  FPGA Pin No.  PIN_C8  PIN_F10  PIN_G10  PIN_D9  PIN_C9  PIN_C9  PIN_C9  PIN_A8  PIN_H11  PIN_H12	PS2 Clock    Description     12C Data     12C Clock     Description     TV Decoder Data[0]     TV Decoder Data[1]     TV Decoder Data[2]     TV Decoder Data[3]     TV Decoder Data[4]     TV Decoder Data[5]     TV Decoder Data[6]     TV Decoder Data[7]     TV Decoder Data[7]     TV Decoder H_SYNC     TV Decoder V_SYNC     TV Decoder Reset     Description     VGA Red[0]     VGA Red[1]     VGA Red[4]     VGA Red[5]     VGA Red[6]     VGA Red[6]     VGA Red[7]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]  TD_DATA[7]  TD_BATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]  VGA_R[1]  VGA_R[2]  VGA_R[3]  VGA_R[6]  VGA_R[6]  VGA_R[6]  VGA_R[7]  VGA_R[7]  VGA_R[7]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_C7  PIN_C7  PIN_C5  PIN_C4  FPGA Pin No.  PIN_C4  FPGA Pin No.  PIN_C8  PIN_F10  PIN_C8  PIN_F10  PIN_G10  PIN_C9  PIN_C9  PIN_C9  PIN_A8  PIN_H11  PIN_H12  PIN_F11	PS2 Clock  Description  12C Data  12C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder H_SYNC  TV Decoder V_SYNC  TV Decoder Reset  Description  VGA Red[0]  VGA Red[1]  VGA Red[3]  VGA Red[4]  VGA Red[6]  VGA Red[6]  VGA Red[7]  VGA Red[7]  VGA Red[7]  VGA Red[7]  VGA Red[8]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]  TD_DATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]  VGA_R[1]  VGA_R[2]  VGA_R[3]  VGA_R[6]  VGA_R[6]  VGA_R[7]  VGA_R[8]  VGA_R[9]  VGA_R[9]  VGA_G[0]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_C7  PIN_C7  PIN_C5  PIN_C4  FPGA Pin No.  PIN_C4  FPGA Pin No.  PIN_C8  PIN_F10  PIN_C8  PIN_F10  PIN_C9  PIN_D9  PIN_C9  PIN_L09  PIN_L09	PS2 Clock   Description   I2C Data   I2C Clock   Description   TV Decoder Data[0]   TV Decoder Data[1]   TV Decoder Data[2]   TV Decoder Data[3]   TV Decoder Data[4]   TV Decoder Data[5]   TV Decoder Data[6]   TV Decoder Data[6]   TV Decoder Data[7]   TV Decoder Data[7]   TV Decoder Data[7]   TV Decoder H_SYNC   TV Decoder V_SYNC   TV Decoder Reset   Description   VGA Red[0]   VGA Red[1]   VGA Red[2]   VGA Red[3]   VGA Red[4]   VGA Red[6]   VGA Red[6]   VGA Red[6]   VGA Red[7]   VGA Red[8]   VGA Red[9]   VGA Red[9]   VGA Red[9]   VGA Red[9]   VGA Red[9]   VGA Green[0]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]  TD_DATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]  VGA_R[1]  VGA_R[2]  VGA_R[3]  VGA_R[4]  VGA_R[5]  VGA_R[6]  VGA_R[6]  VGA_R[7]  VGA_R[8]  VGA_R[8]  VGA_R[9]  VGA_G[0]  VGA_G[0]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_D7  PIN_C7  PIN_D5  PIN_C7  PIN_D5  PIN_K9  PIN_C4  FPGA Pin No.  PIN_C8  PIN_C4  FPGA Pin No.  PIN_C8  PIN_F10  PIN_G10  PIN_D9  PIN_C9  PIN_C9  PIN_A8  PIN_H11  PIN_H12  PIN_F11  PIN_E10  PIN_B9  PIN_A9	PS2 Clock  Description  12C Data  12C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder Data[7]  TV Decoder H_SYNC  TV Decoder V_SYNC  TV Decoder Reset  Description  VGA Red[0]  VGA Red[1]  VGA Red[2]  VGA Red[4]  VGA Red[5]  VGA Red[6]  VGA Red[7]  VGA Red[8]  VGA Red[9]  VGA Green[0]  VGA Green[1]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]  TD_DATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]  VGA_R[1]  VGA_R[2]  VGA_R[3]  VGA_R[4]  VGA_R[5]  VGA_R[6]  VGA_R[6]  VGA_R[7]  VGA_R[8]  VGA_R[9]  VGA_G[1]  VGA_G[1]  VGA_G[1]  VGA_G[1]  VGA_G[1]  VGA_G[1]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_C7  PIN_D7  PIN_C7  PIN_D5  PIN_C4  FPGA Pin No.  PIN_C4  FPGA Pin No.  PIN_C8  PIN_C4  PIN_C9  PIN_C9  PIN_C9  PIN_C9  PIN_B10	PS2 Clock  Description  12C Data  12C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder Data[7]  TV Decoder H_SYNC  TV Decoder V_SYNC  TV Decoder Reset  Description  VGA Red[0]  VGA Red[1]  VGA Red[2]  VGA Red[4]  VGA Red[6]  VGA Red[7]  VGA Red[8]  VGA Red[9]  VGA Green[0]  VGA Green[1]  VGA Green[1]
PS2_DAT  Signal Name  I2C_SCLK  I2C_SDAT  Signal Name  TD_DATA[0]  TD_DATA[1]  TD_DATA[2]  TD_DATA[3]  TD_DATA[4]  TD_DATA[5]  TD_DATA[6]  TD_DATA[6]  TD_DATA[7]  TD_HS  TD_VS  TD_RESET  Signal Name  VGA_R[0]  VGA_R[1]  VGA_R[2]  VGA_R[3]  VGA_R[4]  VGA_R[5]  VGA_R[6]  VGA_R[6]  VGA_R[7]  VGA_R[8]  VGA_R[8]  VGA_R[9]  VGA_G[0]  VGA_G[0]	PIN_C24  FPGA Pin No.  PIN_A6  PIN_B6  FPGA Pin No.  PIN_J9  PIN_E8  PIN_H8  PIN_H10  PIN_G9  PIN_F9  PIN_D7  PIN_C7  PIN_D5  PIN_C7  PIN_D5  PIN_K9  PIN_C4  FPGA Pin No.  PIN_C8  PIN_C4  FPGA Pin No.  PIN_C8  PIN_F10  PIN_G10  PIN_D9  PIN_C9  PIN_C9  PIN_A8  PIN_H11  PIN_H12  PIN_F11  PIN_E10  PIN_B9  PIN_A9	PS2 Clock  Description  12C Data  12C Clock  Description  TV Decoder Data[0]  TV Decoder Data[1]  TV Decoder Data[2]  TV Decoder Data[3]  TV Decoder Data[4]  TV Decoder Data[5]  TV Decoder Data[6]  TV Decoder Data[6]  TV Decoder Data[7]  TV Decoder Data[7]  TV Decoder H_SYNC  TV Decoder V_SYNC  TV Decoder Reset  Description  VGA Red[0]  VGA Red[1]  VGA Red[2]  VGA Red[4]  VGA Red[5]  VGA Red[6]  VGA Red[7]  VGA Red[8]  VGA Red[9]  VGA Green[0]  VGA Green[1]

VGA_G[5]	PIN_A10	VGA Green[5]
VGA_G[6]	PIN_G11	VGA Green[6]
VGA_G[7]	PIN_D11	VGA Green[7]
VGA_G[8]	PIN_E12	VGA Green[8]
VGA_G[9]	PIN D12	VGA Green[9]
VGA B[0]	PIN_J13	VGA Blue[0]
VGA_B[1]	PIN_J14	VGA Blue[1]
VGA_B[2]	PIN_F12	VGA Blue[2]
VGA_B[3]	PIN_G12	VGA Blue[3]
VGA_B[4]	PIN_J10	VGA Blue[4]
VGA_B[5]	PIN_J11	VGA Blue[5]
VGA_B[6]	PIN_C11	VGA Blue[6]
VGA_B[7]	PIN_B11	VGA Blue[7]
VGA_B[8]	PIN_C12	VGA Blue[8]
VGA_B[9]	PIN_B12	- 1
		VGA Blue[9]
VGA_CLK	PIN_B8	VGA Clock
VGA_BLANK	PIN_D6	VGA BLANK
VGA_HS	PIN_A7	VGA H_SYNC
VGA_VS	PIN_D8	VGA V_SYNC
VGA_SYNC	PIN_B7	VGA SYNC
Signal Name	FPGA Pin No.	Description
AUD_ADCLRCK	PIN_C5	Audio CODEC ADC LR Clock
AUD_ADCDAT	PIN B5	Audio CODEC ADC EN Clock  Audio CODEC ADC Data
	PIN_B5	
AUD_DACLRCK		Audio CODEC DAC LR Clock
AUD_DACDAT	PIN_A4	Audio CODEC DAC Data
AUD_XCK	PIN_A5	Audio CODEC Chip Clock
AUD_BCLK	PIN_B4	Audio CODEC Bit-Stream Clock
Signal Name	FPGA Pin No.	Description
ENET_DATA[0]	PIN_D17	DM9000A DATA[0]
ENET_DATA[1]	PIN_C17	DM9000A DATA[1]
ENET_DATA[2]	PIN_B18	DM9000A DATA[2]
ENET_DATA[3]	PIN_A18	DM9000A DATA(3)
ENET_DATA[4]	PIN_B17	DM9000A DATA[4]
ENET_DATA[5]	PIN_A17	DM9000A DATA[5]
		DMOOOOA DATAICI
ENET_DATA[6]	PIN_B16	DM9000A DATA[6]
ENET_DATA[6] ENET_DATA[7]	PIN_B16 PIN_B15	DM9000A DATA[6]  DM9000A DATA[7]
ENET_DATA[7]	PIN_B15	
ENET_DATA[7] ENET_DATA[8]	PIN_B15 PIN_B20	DM9000A DATA[7] DM9000A DATA[8]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9]	PIN_B15 PIN_B20 PIN_A20	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10]	PIN_B15 PIN_B20 PIN_A20 PIN_C19	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_A19	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_A19 PIN_E18	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_B19 PIN_A19 PIN_E18 PIN_D18 PIN_D18	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]  DM9000A DATA[15]  DM9000A Clock 25 MHz
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_B19 PIN_A19 PIN_E18 PIN_D18 PIN_D18 PIN_B24 PIN_A21	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]  DM9000A DATA[15]  DM9000A DATA[15]  DM9000A Clock 25 MHz  DM9000A Command/Data Select, 0 = Command, 1 = Data
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_B19 PIN_A19 PIN_E18 PIN_D18 PIN_D18 PIN_B24 PIN_A21 PIN_A23	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]  DM9000A DATA[15]  DM9000A Clock 25 MHz  DM9000A Command/Data Select, 0 = Command, 1 = Data  DM9000A Chip Select
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_B19 PIN_B18 PIN_E18 PIN_D18 PIN_B24 PIN_A21 PIN_A23 PIN_B21	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]  DM9000A DATA[15]  DM9000A Clock 25 MHz  DM9000A Command/Data Select, 0 = Command, 1 = Data  DM9000A Chip Select  DM9000A Interrupt
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_A19 PIN_E18 PIN_D18 PIN_B24 PIN_A21 PIN_A23 PIN_B21 PIN_B21 PIN_B21 PIN_B21 PIN_B21	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]  DM9000A DATA[15]  DM9000A Clock 25 MHz  DM9000A Command/Data Select, 0 = Command, 1 = Data  DM9000A Chip Select  DM9000A Read
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_WR_N	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_A19 PIN_E18 PIN_D18 PIN_B24 PIN_A21 PIN_A21 PIN_A23 PIN_B21 PIN_B21 PIN_B21 PIN_B22 PIN_B22	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]  DM9000A DATA[15]  DM9000A Clock 25 MHz  DM9000A Command/Data Select, 0 = Command, 1 = Data  DM9000A Chip Select  DM9000A Interrupt  DM9000A Read  DM9000A Write
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_A19 PIN_E18 PIN_D18 PIN_B24 PIN_A21 PIN_A23 PIN_B21 PIN_B21 PIN_B21 PIN_B21 PIN_B21	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]  DM9000A DATA[15]  DM9000A Clock 25 MHz  DM9000A Command/Data Select, 0 = Command, 1 = Data  DM9000A Chip Select  DM9000A Read
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_WR_N	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_A19 PIN_E18 PIN_D18 PIN_B24 PIN_A21 PIN_A21 PIN_A23 PIN_B21 PIN_B21 PIN_B21 PIN_B22 PIN_B22	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]  DM9000A DATA[15]  DM9000A Clock 25 MHz  DM9000A Command/Data Select, 0 = Command, 1 = Data  DM9000A Chip Select  DM9000A Interrupt  DM9000A Read  DM9000A Write
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_WR_N ENET_RST_N	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_A19 PIN_E18 PIN_D18 PIN_B24 PIN_A21 PIN_A21 PIN_A22 PIN_B21 PIN_B22 PIN_B22 PIN_B23	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]  DM9000A DATA[15]  DM9000A Clock 25 MHz  DM9000A Clock 25 MHz  DM9000A Chip Select  DM9000A Interrupt  DM9000A Read  DM9000A Write  DM9000A Reset
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_WR_N ENET_WR_N ENET_RST_N Signal Name GPIO_0[0]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_A19 PIN_E18 PIN_D18 PIN_B24 PIN_A21 PIN_A21 PIN_A23 PIN_B21 PIN_B21 PIN_B22 PIN_B22 PIN_B22 PIN_B23 FFGA Pin No. PIN_D25	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]  DM9000A DATA[15]  DM9000A Clock 25 MHz  DM9000A Command/Data Select, 0 = Command, 1 = Data  DM9000A Chip Select  DM9000A Interrupt  DM9000A Read  DM9000A Write  DM9000A Reset  DM9000A Reset  DM9000A Reset  DM9000A GPIO Connection 0[0]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_RD_N ENET_WR_N ENET_RST_N Signal Name GPIO_0[0] GPIO_0[1]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_A19 PIN_E18 PIN_D18 PIN_B24 PIN_A21 PIN_A21 PIN_A23 PIN_B21 PIN_B22 PIN_B22 PIN_B22 PIN_B22 PIN_B23 FPGA PIN NO. PIN_D25 PIN_J22	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]  DM9000A DATA[15]  DM9000A Clock 25 MHz  DM9000A Command/Data Select, 0 = Command, 1 = Data  DM9000A Chip Select  DM9000A Interrupt  DM9000A Read  DM9000A Write  DM9000A Reset  DM9000A Reset  DM9000A Reset  DM9000A GPIO Connection 0[0]  GPIO Connection 0[1]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_WR_N ENET_WR_N ENET_WR_N ENET_RST_N Signal Name GPIO_0[0] GPIO_0[1]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_B19 PIN_E18 PIN_D18 PIN_B24 PIN_A21 PIN_A21 PIN_A23 PIN_B24 PIN_B24 PIN_B25 PIN_B22 PIN_B22 PIN_B22 PIN_B22 PIN_B23 FPGA Pin No. PIN_D25 PIN_J22 PIN_E26	DM9000A DATA[7]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_WR_N ENET_WR_N ENET_RST_N Signal Name GPIO_0[0] GPIO_0[1] GPIO_0[2] GPIO_0[3]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_B19 PIN_E18 PIN_B24 PIN_A21 PIN_A21 PIN_A23 PIN_B24 PIN_B24 PIN_B21 PIN_B25 PIN_B22 PIN_B22 PIN_B22 PIN_B23 FPGA PIN NO. PIN_D25 PIN_J22 PIN_E26 PIN_E25	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]  DM9000A DATA[15]  DM9000A Clock 25 MHz  DM9000A Command/Data Select, 0 = Command, 1 = Data  DM9000A Chip Select  DM9000A Interrupt  DM9000A Read  DM9000A Write  DM9000A Reset  Description  GPIO Connection 0[0]  GPIO Connection 0[1]  GPIO Connection 0[2]  GPIO Connection 0[3]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_WR_N ENET_WR_N ENET_RST_N Signal Name GPIO_0[0] GPIO_0[1] GPIO_0[2] GPIO_0[3] GPIO_0[4]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_B19 PIN_E18 PIN_D18 PIN_B24 PIN_A21 PIN_A21 PIN_A23 PIN_B21 PIN_B21 PIN_B25 PIN_B22 PIN_B22 PIN_B23 FPGA PIN NO. PIN_D25 PIN_D25 PIN_E26 PIN_E25 PIN_E25 PIN_F24	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]  DM9000A DATA[15]  DM9000A Clock 25 MHz  DM9000A Command/Data Select, 0 = Command, 1 = Data  DM9000A Chip Select  DM9000A Interrupt  DM9000A Read  DM9000A Write  DM9000A Write  DM9000A Reset  Description  GPIO Connection 0[0]  GPIO Connection 0[1]  GPIO Connection 0[2]  GPIO Connection 0[3]  GPIO Connection 0[4]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_WR_N ENET_WR_N ENET_RST_N Signal Name GPIO_0[0] GPIO_0[1] GPIO_0[2] GPIO_0[4] GPIO_0[5]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_B19 PIN_E18 PIN_D18 PIN_B24 PIN_A21 PIN_A21 PIN_A23 PIN_B21 PIN_B21 PIN_B21 PIN_B22 PIN_B22 PIN_B22 PIN_B23 FPGA PIN NO. PIN_D25 PIN_J22 PIN_E26 PIN_E25 PIN_F24 PIN_F23	DM9000A DATA[7]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_WR_N ENET_WR_N ENET_RST_N Signal Name GPIO_0[0] GPIO_0[1] GPIO_0[2] GPIO_0[3] GPIO_0[4] GPIO_0[6]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_B19 PIN_E18 PIN_D18 PIN_B24 PIN_A21 PIN_A21 PIN_A23 PIN_B21 PIN_B21 PIN_B25 PIN_B22 PIN_B22 PIN_B23 FPGA PIN NO. PIN_D25 PIN_D25 PIN_E26 PIN_E25 PIN_E25 PIN_F24	DM9000A DATA[7]  DM9000A DATA[8]  DM9000A DATA[9]  DM9000A DATA[10]  DM9000A DATA[10]  DM9000A DATA[11]  DM9000A DATA[12]  DM9000A DATA[13]  DM9000A DATA[14]  DM9000A DATA[15]  DM9000A Clock 25 MHz  DM9000A Command/Data Select, 0 = Command, 1 = Data  DM9000A Chip Select  DM9000A Interrupt  DM9000A Read  DM9000A Write  DM9000A Write  DM9000A Reset  Description  GPIO Connection 0[0]  GPIO Connection 0[1]  GPIO Connection 0[2]  GPIO Connection 0[3]  GPIO Connection 0[4]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_WR_N ENET_WR_N ENET_RST_N Signal Name GPIO_0[0] GPIO_0[1] GPIO_0[2] GPIO_0[4] GPIO_0[5]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_B19 PIN_E18 PIN_D18 PIN_B24 PIN_A21 PIN_A21 PIN_A23 PIN_B21 PIN_B21 PIN_B21 PIN_B22 PIN_B22 PIN_B22 PIN_B23 FPGA PIN NO. PIN_D25 PIN_J22 PIN_E26 PIN_E25 PIN_F24 PIN_F23	DM9000A DATA[7]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_WR_N ENET_WR_N ENET_RST_N Signal Name GPIO_0[0] GPIO_0[1] GPIO_0[2] GPIO_0[3] GPIO_0[4] GPIO_0[6]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_B19 PIN_E18 PIN_D18 PIN_B24 PIN_A21 PIN_A23 PIN_B21 PIN_A23 PIN_B21 PIN_B21 PIN_B22 PIN_B22 PIN_B22 PIN_B23 FPGA PIN NO. PIN_D25 PIN_J22 PIN_E26 PIN_E25 PIN_E25 PIN_F24 PIN_F23 PIN_J21	DM9000A DATA[7]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_WR_N ENET_WR_N ENET_WR_N ENET_RST_N Signal Name GPIO_0[0] GPIO_0[1] GPIO_0[2] GPIO_0[3] GPIO_0[6] GPIO_0[6] GPIO_0[6] GPIO_0[7] GPIO_0[8]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_B19 PIN_B18 PIN_B24 PIN_B24 PIN_A21 PIN_A23 PIN_B21 PIN_B22 PIN_B22 PIN_B22 PIN_B22 PIN_B23 FPGA PIN NO. PIN_D25 PIN_J22 PIN_E26 PIN_E25 PIN_F24 PIN_F23 PIN_F24 PIN_J20 PIN_F25	DM9000A DATA[7]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_WR_N ENET_WR_N ENET_WR_N ENET_RST_N Signal Name GPIO_0[0] GPIO_0[1] GPIO_0[2] GPIO_0[3] GPIO_0[4] GPIO_0[6] GPIO_0[6] GPIO_0[7] GPIO_0[8] GPIO_0[9]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_B19 PIN_B18 PIN_B24 PIN_B24 PIN_A21 PIN_B24 PIN_B21 PIN_B22 PIN_B22 PIN_B22 PIN_B22 PIN_B22 PIN_B23 FPGA Pin No. PIN_D25 PIN_J22 PIN_E26 PIN_E25 PIN_F24 PIN_F23 PIN_J21 PIN_J20 PIN_F25 PIN_F25 PIN_F25 PIN_F26	DM9000A DATA[7]
ENET_DATA[7] ENET_DATA[8] ENET_DATA[9] ENET_DATA[10] ENET_DATA[11] ENET_DATA[11] ENET_DATA[12] ENET_DATA[13] ENET_DATA[14] ENET_DATA[15] ENET_CLK ENET_CMD ENET_CS_N ENET_INT ENET_RD_N ENET_WR_N ENET_WR_N ENET_WR_N ENET_RST_N Signal Name GPIO_0[0] GPIO_0[1] GPIO_0[2] GPIO_0[3] GPIO_0[6] GPIO_0[6] GPIO_0[6] GPIO_0[7] GPIO_0[8]	PIN_B15 PIN_B20 PIN_A20 PIN_C19 PIN_D19 PIN_B19 PIN_B19 PIN_B18 PIN_B24 PIN_B24 PIN_A21 PIN_A23 PIN_B21 PIN_B22 PIN_B22 PIN_B22 PIN_B22 PIN_B23 FPGA PIN NO. PIN_D25 PIN_J22 PIN_E26 PIN_E25 PIN_F24 PIN_F23 PIN_F24 PIN_J20 PIN_F25	DM9000A DATA[7]

GPIO_0[12]	PIN_G23	GPIO Connection 0[12]
GPIO_0[13]	PIN_G24	GPIO Connection 0[13]
GPIO_0[14]	PIN_K22	GPIO Connection 0[14]
GPIO_0[15]	PIN_G25	GPIO Connection 0[15]
GPIO_0[16]	PIN_H23	GPIO Connection 0[16]
GPIO_0[17]	PIN_H24	GPIO Connection 0[17]
GPIO_0[18]	PIN_J23	GPIO Connection 0[18]
GPIO_0[19]	PIN_J24	GPIO Connection 0[19]
GPIO 0[20]	PIN_H25	GPIO Connection 0[20]
GPIO 0[21]	PIN H26	GPIO Connection 0[21]
GPIO_0[22]	PIN H19	GPIO Connection 0[22]
GPIO_0[23]	PIN_K18	GPIO Connection 0[23]
GPIO_0[24]	PIN_K19	GPIO Connection 0[24]
GPIO_0[25]	PIN_K21	GPIO Connection 0[25]
GPIO_0[26]	PIN_K23	GPIO Connection 0[26]
GPIO_0[27]	PIN K24	GPIO Connection 0[27]
GPIO_0[28]	PIN L21	GPIO Connection 0[28]
GPIO_0[29]	PIN_L20	GPIO Connection 0[29]
GPIO_0[30]	PIN_J25	GPIO Connection 0[30]
GPIO_0[31]	PIN J26	GPIO Connection 0[31]
GPIO_0[32]	PIN_L23	GPIO Connection 0[32]
GPIO_0[33]	PIN L24	GPIO Connection 0[33]
GPIO_0[34]	PIN L25	GPIO Connection 0[34]
GPIO_0[35]	PIN_L19	GPIO Connection 0[35]
GPIO_1[0]	PIN_K25	GPIO Connection 1[0]
GPIO_1[1]	PIN_K26	GPIO Connection 1[1]
GPIO_1[2]	PIN_M22	GPIO Connection 1[2]
GPIO_1[3]	PIN_M23	GPIO Connection 1[3]
GPIO_1[4]	PIN M19	GPIO Connection 1[4]
GPIO_1[5]	PIN_M20	GPIO Connection 1[5]
GPIO_1[6]	PIN_N20	GPIO Connection 1[6]
GPIO_1[7]	PIN_M21	GPIO Connection 1[7]
GPIO_1[8]	PIN_M24	GPIO Connection 1[8]
GPIO_1[9]	PIN_M25	GPIO Connection 1[9]
GPIO_1[10]	PIN N24	GPIO Connection 1[10]
GPIO_1[11]	PIN_P24	GPIO Connection 1[11]
GPIO_1[12]	PIN_R25	GPIO Connection 1[12]
GPIO_1[13]	PIN_R24	GPIO Connection 1[13]
GPIO_1[14]	PIN_R20	GPIO Connection 1[14]
GPIO_1[15]	PIN_T22	GPIO Connection 1[15]
GPIO_1[16]	PIN_T23	GPIO Connection 1[16]
GPIO_1[17]	PIN_T24	GPIO Connection 1[17]
GPIO_1[18]	PIN_T25	GPIO Connection 1[18]
GPIO_1[19]	PIN_T18	GPIO Connection 1[19]
GPIO_1[20]	PIN_T21	GPIO Connection 1[20]
GPIO_1[21]	PIN_T20	GPIO Connection 1[21]
GPIO_1[22]	PIN_U26	GPIO Connection 1[21]
GPIO_1[23]	PIN_U25	GPIO Connection 1[23]
GPIO_1[24]	PIN_U23	GPIO Connection 1[24]
GPIO_1[25]	PIN_U24	GPIO Connection 1[25]
GPIO_1[26]	PIN_R19	GPIO Connection 1[26]
GPIO_1[27]	PIN_T19	GPIO Connection 1[27]
GPIO_1[28]	PIN_U20	GPIO Connection 1[28]
GPIO_1[29]	PIN U21	GPIO Connection 1[29]
GPIO_1[30]	PIN_V26	GPIO Connection 1[30]
GPIO_1[31]	PIN_V25	GPIO Connection 1[31]
GPIO_1[32]	PIN_V24	GPIO Connection 1[32]
GPIO_1[33]	PIN_V23	GPIO Connection 1[33]
GPIO_1[34]	PIN_W25	GPIO Connection 1[34]
GPIO_1[35]	PIN_W23	GPIO Connection 1[35]
2: :2[00]		The Common Mod