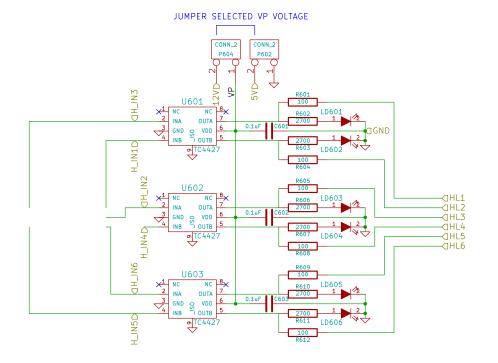
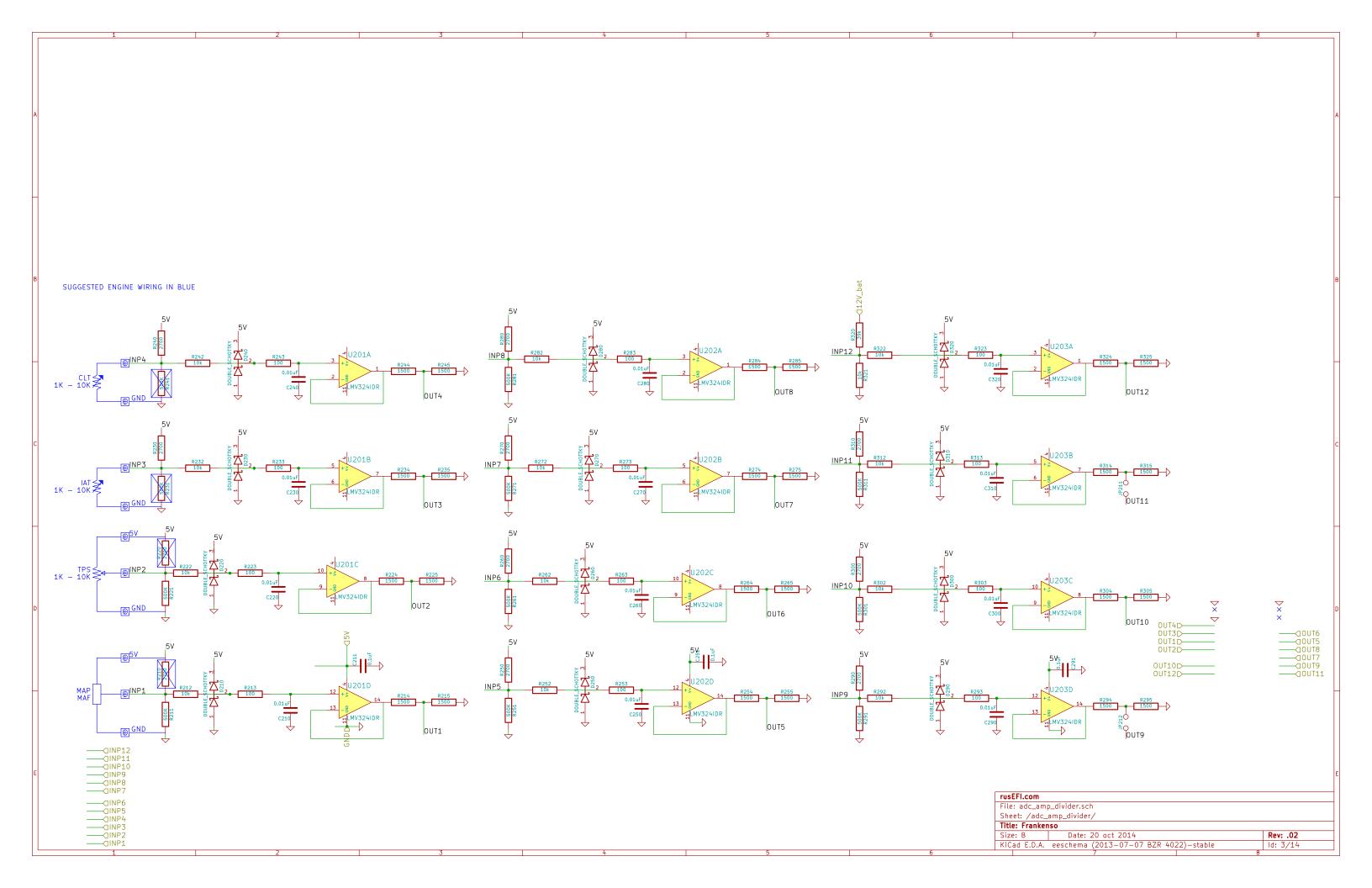
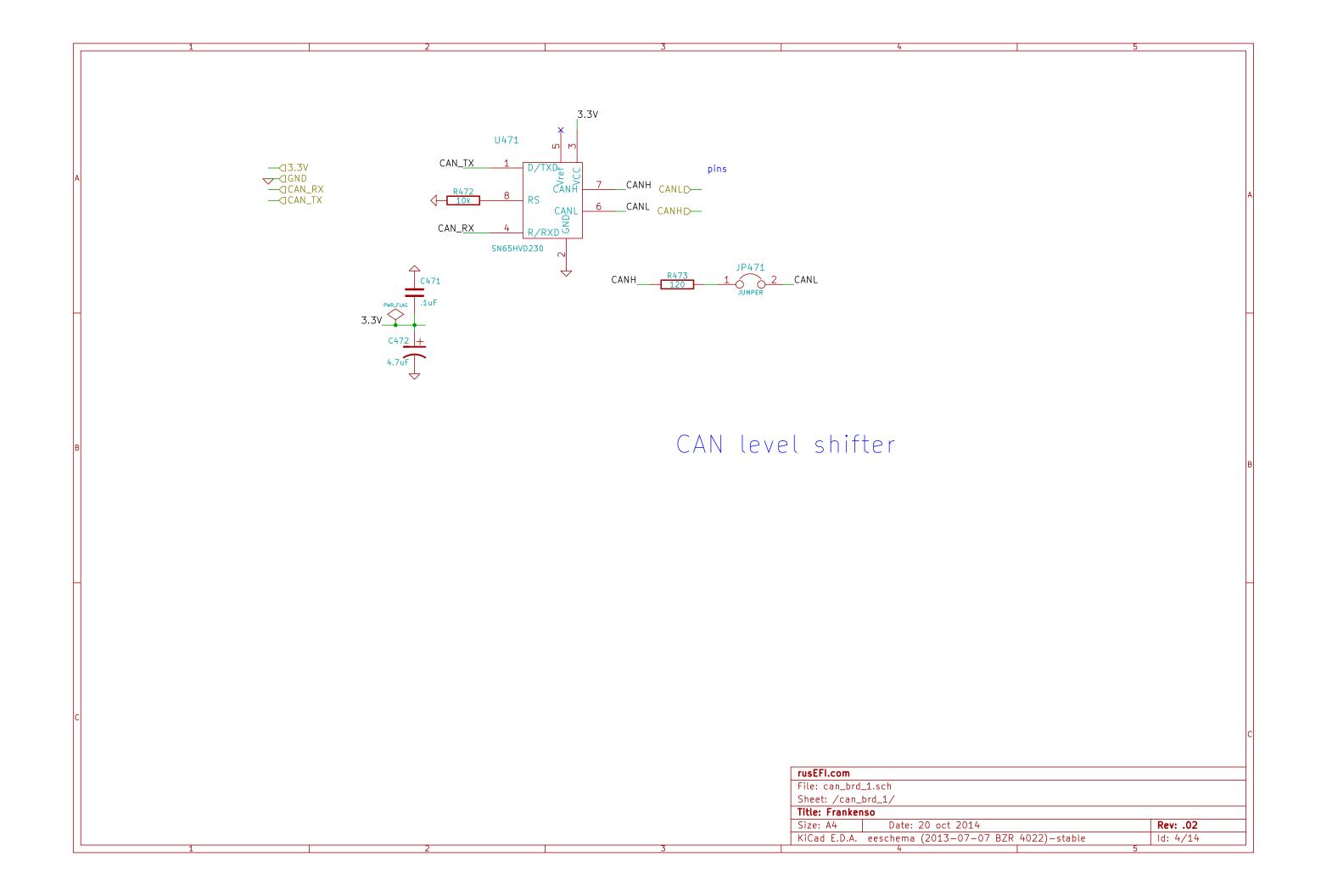


6 channel high / low side driver

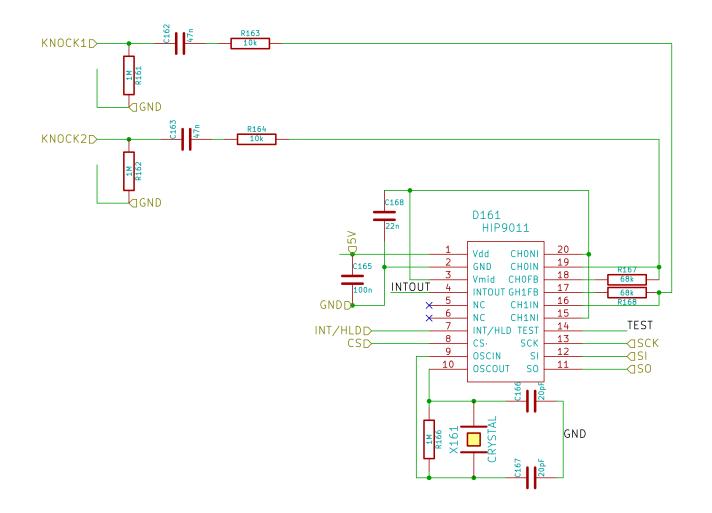


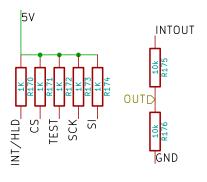
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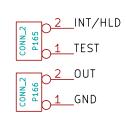




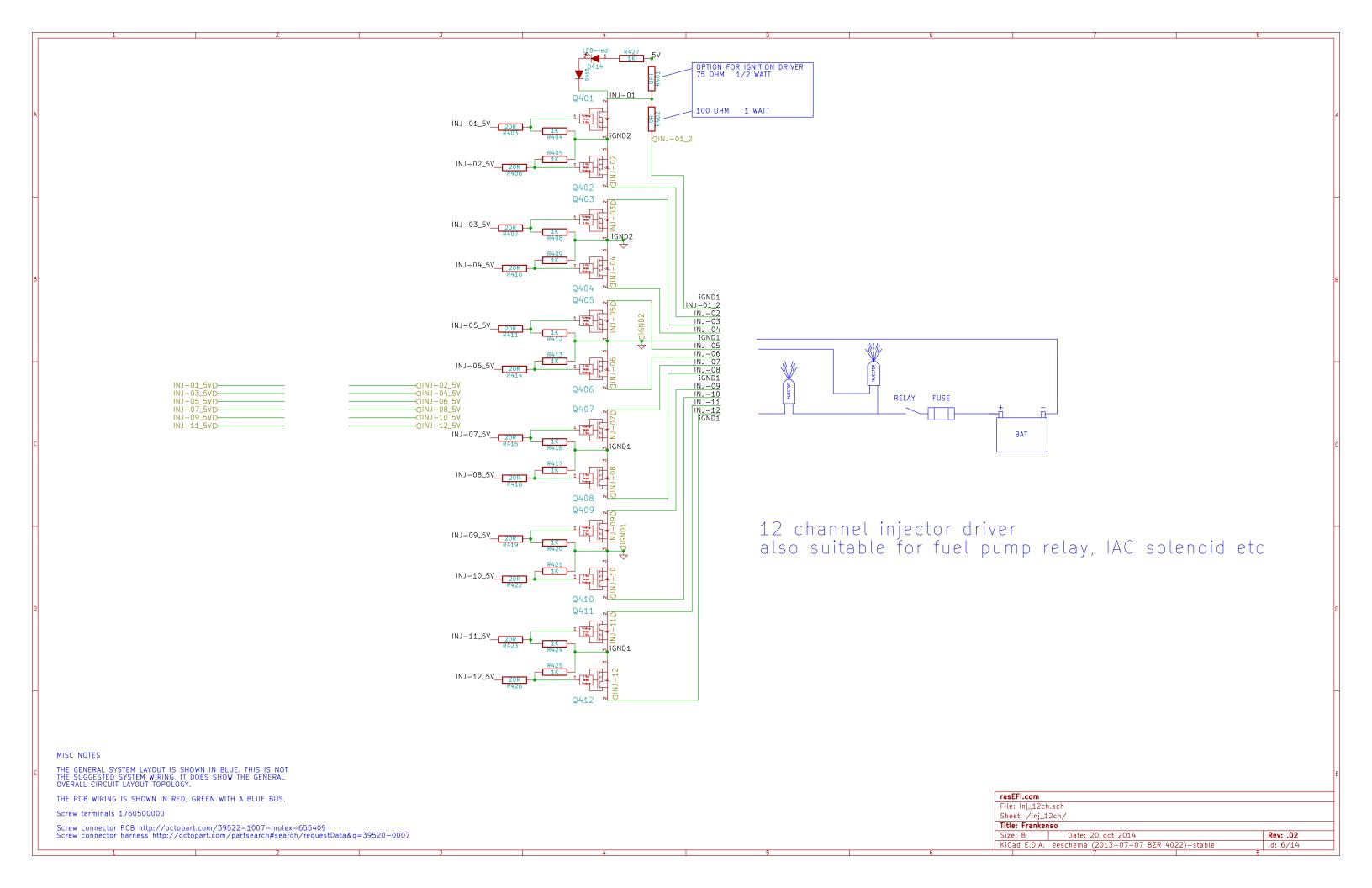
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WJO1 IS A BACKUP PLAN. THE VOLTAGE DROP ACROSS D703 MAY BE NOT TOLERABLE, SO WE HAVE A BACK UP PLAN IF WE NEED TO BYPASS THE DIODE WITH A LOWER VOLTAGE DROP U351 C702 ──USART_TX ──USART_RX RXD 16 USBD-15 USBD+ SHIELD 6 Shield RESET 19 RESET 4700pF × 27 × 28 OSCO CBUS1 C351 13 CB2 2 1 14 D702LED-grn CBUS2 CBUS3 3V30UT CBUS4 C356 RESET____RI FT232RL For right conn — GCS_SD_MODULE — GSPI_MOSI — G3.3V Vdd - ☐SPI_SCK - ☐GND SCLK DO -√SPI_MISO DATA1 CDN SD card slot USB TTL module rusEFI.com File: mmc_usb_1.sch Sheet: /mmc_usb_1/ Title: Frankenso Size: A4 Date: 20 oct 2014 Rev: .02 KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable ld: 7/14

