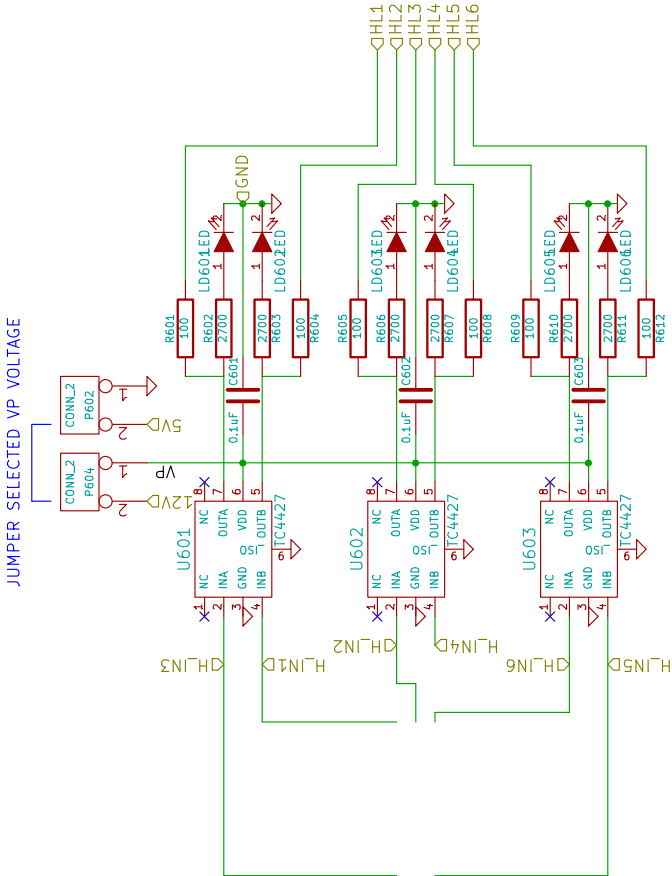


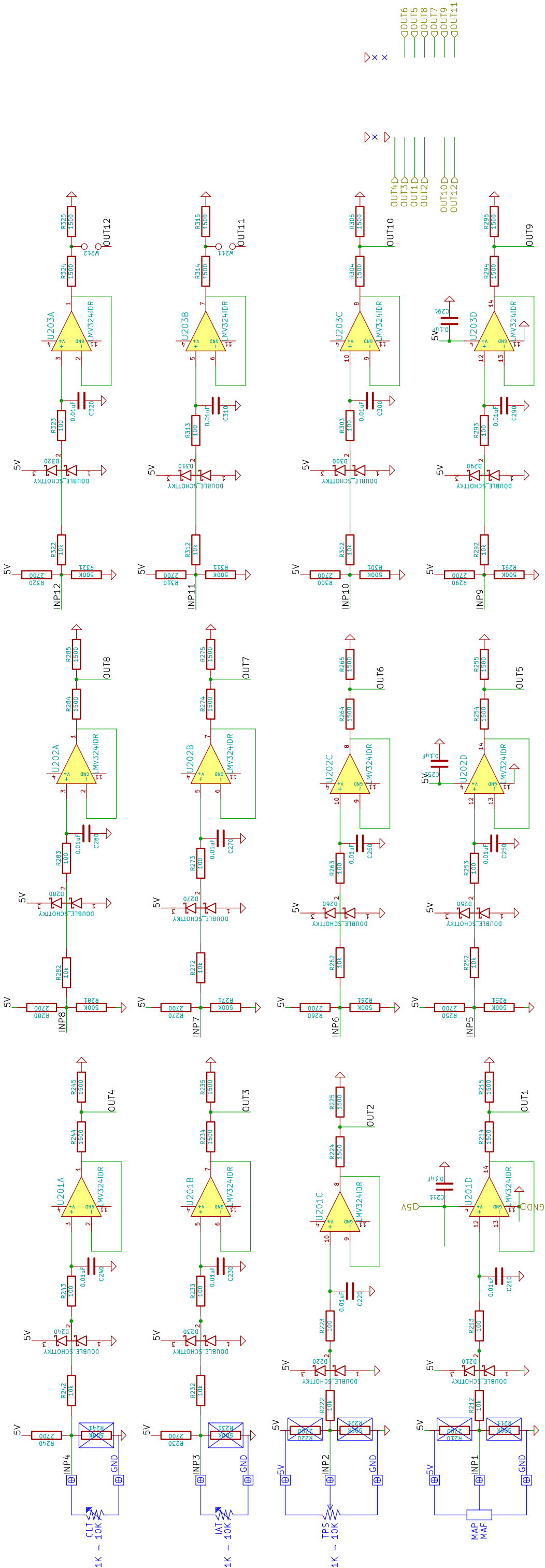
These two jumpers are here to accomodate stm32f4discovery

That's alternative signal OUTPUT — these traces should be routable to PC6 and PA5 via jumpers.
 (alternative to W212 and W212 routing of op—amps ch 11 and ch 12

6 channel high / low side driver



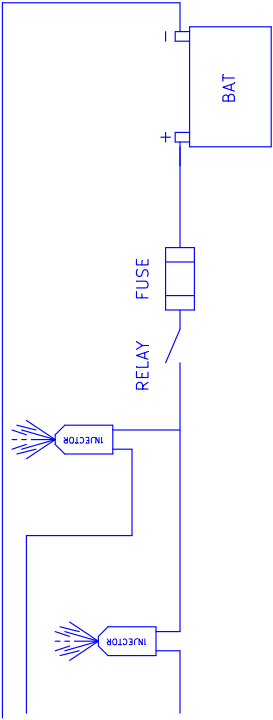
SUGGESTED ENGINE WIRING IN BLUE



QINP12
QINP11
QINP10
QINP9
QINP8
QINP7
QINP6
QINP5
QINP4
QINP3
QINP2
QINP1

QOUT6
QOUT5
QOUT4
QOUT3
QOUT2
QOUT1
QOUT0

QOUT6
QOUT5
QOUT4
QOUT3
QOUT2
QOUT1
QOUT0



MISC NOTES

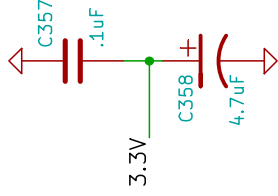
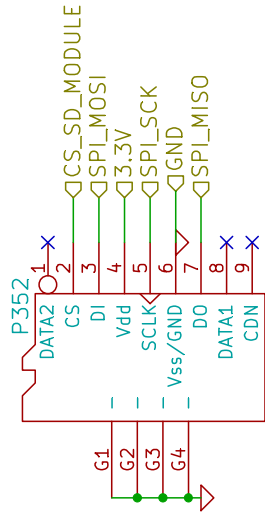
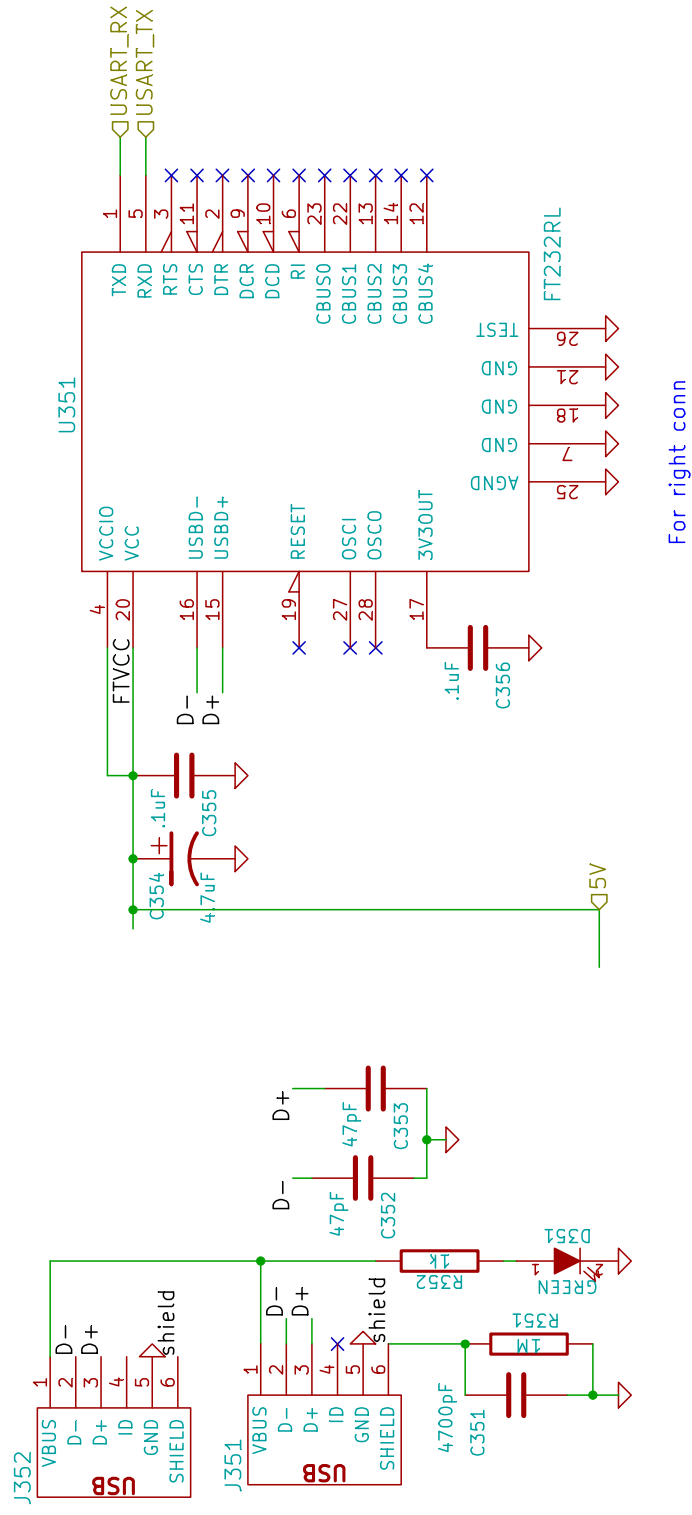
THE GENERAL SYSTEM LAYOUT IS SHOWN IN BLUE. THIS IS NOT THE SUGGESTED SYSTEM WIRING, IT DOES SHOW THE GENERAL OVERALL CIRCUIT LAYOUT TOPOLOGY.

THE PCB WIRING IS SHOWN IN RED, GREEN WITH A BLUE BUS.

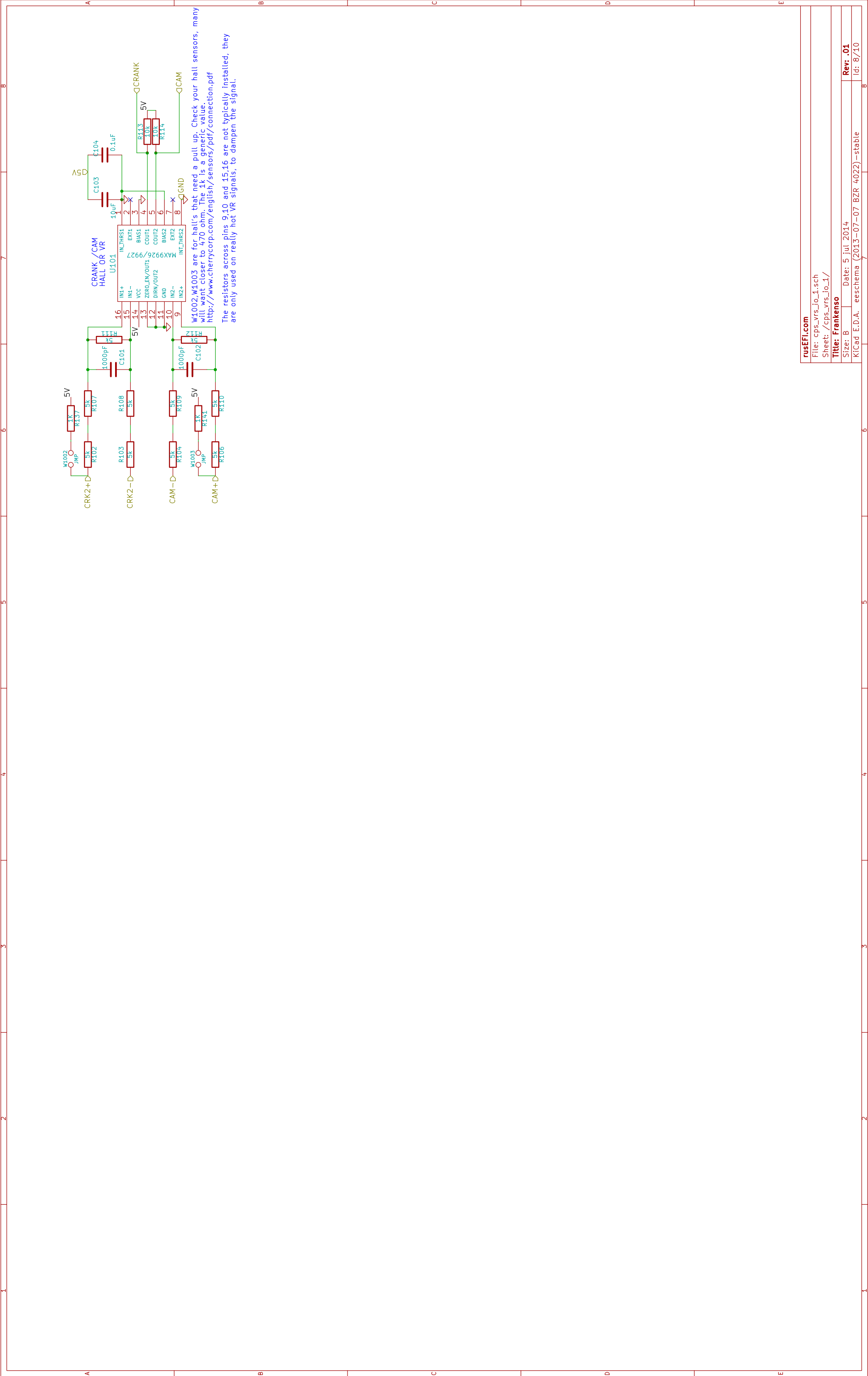
Screw terminals 1760500000

Screw connector PCB <http://octopart.com/39522-1007-molex-655409>

Screw connector PCB <http://octopart.com/39522-1007-motex-635409>
Screw connector harness <http://octopart.com/partsearch#search/requestData&q=39520-0007>



SD card slot
USB-TTL module



rusEFI.com	
File: cps_vrs_io_1.sch	
Sheet: /cps_vrs_io_1/	
Title: Frankenso	
Size: B	Date: 5 jul 2014
KiCad E.D.A.	eeschema (2013-07-07 BZR 4022) -stable
6	8

