PORT MULTIPLEXER ENABLE
PORT->Group[g_APinDescription(x).ulPort].PINCNFG[g_APinDescription[x].ulPin].bit.PMUXEN=1

SET PWM: REG_TCCX_CCBW

N = 1,2,4,8,64,256,1024

GCLK 4 to 7

GENDIV 1 to 250

GCLK_GENDIV_DIV()

PRESCALER CTRLA
(default = 16)

TCC_CTRLA_PRESCALER_DIV()

OSC

XOSC32K - 32.768 KHz
OSCULP32K - 32Khz low power
OSC8M - 8 MHz precision
DFLL48M - 48 MHz PLL

TCC0 - 24 Bit

TCC1 - 24 Bit

TCC2- 16 Bit

UP/DOWN dual slope timers
REG_TCCX_PER =..........

MULTIPLEXER

CCB0

TCC0   PA8 - Even   D2
TCC1   PA14 - Even   D4
TCC2   PA16-Even   D11

CCB1

TCC0   PA15- Odd   D5
TCC1   PA9 - Odd   D3
TCC2   PA17-Odd   D13

CCB2

TCC0   PA20 - Even   D6

CCB3

TCC0   PA21 - Odd   D7

ODD

EVEN

CONNECT THE TCCX TIMERS TO THE OUTPUTS
PORT->Group(g_APinDescription[y].ulPort).PMUX[g_APinDescription[y].ulPin>>1].reg = PORT_PMUX_PMUXO_F| PORT_PMUX_PMUXE_F

ARDUINO ZERO FUNCTIONAL DIAGRAM FOR FREQUENCY - PWM REGISTER SETTINGS
VER. 1.6 - 28/03/2016