				JWITCHED I/O FORTS			
\$40	(read_n / write)	ID \$08 ID \$D4	MSX2+ FS-A1 Series MSX++ 1chipMSX, Zemmix Neo, etc.	Z80B 5.37MHz OCM-PLD v2.4 or later			SMART COMMANDS TABLE
		BIT 0			\$00	(000)	Null Command (reserved)
\$41	(read_n / write)	BIT 1 BIT 2			\$01 \$02	(001)	Set Turbo Pana Redirection OFF (default) Set Turbo Pana Redirection ON
		BIT 3	Smart Command ID	CPL or \$FF (Null)	\$03	(003)	Set Standard Speed 3.58MHz
		BIT 4 BIT 5			\$04 \$05	(004)	Set Custom Speed 4.10MHz Set Custom Speed 4.48MHz
		BIT 6 BIT 7			\$06 \$07	(006)	Set Custom Speed 4.90MHz Set Custom Speed 5.39MHz
		BIT 0	CPU Clock	Virtual DIP-SW1	\$08	(007)	Set Custom Speed 6.10MHz
\$42	(read / write_n)	BIT 1	Video Output (MSB)	Virtual DIP-SW2	\$09	(009)	Set Custom Speed 6.96MHz
		BIT 2 BIT 3	Video Output (LSB) Cartridge Slot-1	Virtual DIP-SW3 Virtual DIP-SW4	\$0A \$0B	(010)	Set Custom Speed 8.06MHz (aka "Turbo 10MHz") (default) Set Turbo MegaSD OFF
,74Z		BIT 4 BIT 5	Cartridge Slot-2 (MSB) Cartridge Slot-2 (LSB)	Virtual DIP-SW5 Virtual DIP-SW6	\$0C \$0D	(012)	Set Turbo MegaSD ON (default) Set External Slot-1 + External Slot-2
		BIT 6	Current Mapper Size	Virtual DIP-SW7	\$0E	(014)	Set Internal SCC-I Slot-1 + External Slot-2
		BIT 7 BIT 0	Current MegaSD Mode	Virtual DIP-SW8 CPU Clock	\$0F \$10	(015)	Set External Slot-1 + Internal SCC-I Slot-2 Set Internal SCC-I Slot-1 + Internal SCC-I Slot-2
	(road (write n)	BIT 1		Video Output	\$11	(017)	Set External Slot-1 + Internal ASCII-8K Slot-2
		BIT 2 BIT 3		Audio Mixer & SCRLK Cartridge Slot-1	\$12 \$13	(018)	Set Internal SCC-I Slot-1 + Internal ASCII-8K Slot-2 Set External Slot-1 + Internal ASCII-16K Slot-2
\$43	(read / write_n)	BIT 4	Lock Mask of the Toggles	Cartridge Slot-2	\$14	(020)	Set Internal SCC-I Slot-1 + Internal ASCII-16K Slot-2 Set Japanese Keyboard Layout
		BIT 5 BIT 6		Hard Reset Key Internal Mapper	\$15 \$16	(021) (022)	Set Non-Japanese Keyboard Layout
		BIT 7 BIT 0		Internal MegaSD Led 1 Status	\$17 \$18	(023)	Set Display Mode 15KHz Composite/S-Video Set Display Mode 15KHz RGB w/ Audio Out
	(read / write_n)	BIT 1		Led 1 Status Led 2 Status	\$18	(024)	Set Display Mode 31Khz VGA for LED TV or LED Display also HDMI AV on SM-X
		BIT 2 BIT 3	Lights I/O	Led 3 Status Led 4 Status	\$1A \$1B	(026) (027)	Set Display Mode 31Khz VGA+ for CRT Monitor (legacy output) also HDMI AV on SM-X Set VDP Speed Normal Mode (default)
\$44		BIT 4	(SM-X and SX-2 only use Led 8 but	Led 5 Status	\$1C	(028)	Set VDP Speed Fast Mode (V9958 only)
		BIT 5 BIT 6	this register works the same way)	Led 6 Status Led 7 Status	\$1D \$1E	(029)	Reserve MegaSD OFF (warm reset to go) Reserve MegaSD ON (warm reset to go)
		BIT 7		Led 8 Status	\$1F	(031)	Set MegaSD Blink OFF
\$45	(read / write_n)	BIT 0 BIT 1	PSG Volume Level	BIT 0 (LSB) BIT 1	\$20 \$21	(032)	Set MegaSD Blink ON (default) Set Lights Mode OFF w/ Auto LEDs Control (default)
		BIT 2	PSG Mute	BIT 2 (MSB)	\$22	(034)	Set Lights Mode ON + Red Led OFF Set Lights Mode ON + Red Led ON
	(read only)	BIT 4		Status BIT 0 (LSB)	\$23 \$24	(035)	Internal Audio Preset #1 "Mute Sound"
	(read / write_n)	BIT 5 BIT 6	Master Volume Level	BIT 1 BIT 2 (MSB)	\$25 \$26	(037)	Internal Audio Preset #2 "Middle Sound" Internal Audio Preset #3 "High Sound" (default)
	(read only)	BIT 7	Master Mute	Status	\$27	(039)	Set CMT OFF (default) (disabled w/ MSXtR BIOS) n/a on SM-X / SX-2
	(read / write_n)	BIT 0 BIT 1	OPLL Volume Level	BIT 0 (LSB) BIT 1	\$28 \$29	(040) (041)	Set CMT ON (needs a cassette recorder) (disabled w/ MSXtR BIOS) n/a on SM-X / SX-2 Lock Turbo Toggles
\$46		BIT 2		BIT 2 (MSB)	\$2A	(042)	Unlock Turbo Toggles
	(read only)	BIT 3 BIT 4	OPLL Mute	Status BIT 0 (LSB)	\$2B \$2C	(043) (044)	Lock Display Toggles Unlock Display Toggles
	(read / write_n)	BIT 5	SCC-I Volume Level	BIT 1	\$2D	(045)	Lock Audio Mixer & SCRLK Toggles
	(read only)	BIT 6 BIT 7	SCC-I Mute	BIT 2 (MSB) Status	\$2E \$2F	(046) (047)	Unlock Audio Mixer & SCRLK Toggles Lock Slot-1 Toggles
		BIT 0		BIT 0 (LSB)	\$30	(048)	Unlock Slot-1 Toggles
\$47	(read only)	BIT 1 BIT 2	CPU Custom Speed Level	BIT 1 BIT 2 (MSB)	\$31 \$32	(049)	Lock Slot-2 Toggles Unlock Slot-2 Toggles
		BIT 3	Turbo MegaSD (tMSD)	Status	\$33	(051)	Lock Slot-1 & Slot-2 Toggles Unlock Slot-1 & Slot-2 Toggles
		BIT 4 BIT 5	Turbo Pana Redirection (tPR) VDP Speed Mode	Status 0=Normal, 1=Fast	\$34 \$35	(052)	Lock Hard Reset Key
		BIT 6 BIT 7	Mapper Size Req MegaSD Mode Req	0=2048kB, 1=4096kB Status	\$36 \$37	(054)	Unlock Hard Reset Key Lock Mapper Toggle
		BIT 0	Turbo Pana	Status	\$38	(056)	Unlock Mapper Toggle
\$48	(read only)	BIT 1 BIT 2	Current Keyboard Layout SCRLK Toggle	0=JP, 1=Non-JP Status	\$39 \$3A	(057)	Lock MegaSD Toggle Unlock MegaSD Toggle
		BIT 3	Lights Mode	0=Auto, 1=ON	\$3B	(059)	Lock All Toggles
		BIT 4 BIT 5	Red Mode (Led 0) Last Reset Flag	Status 0=Cold, 1=Warm	\$3C \$3D	(060) (061)	Unlock All Toggles (default) Set Pseudo-Stereo OFF (default)
		BIT 6 BIT 7	Reset Required Flag	Status Status	\$3E	(062)	Set Pseudo-Stereo ON (needs an external sound cartridge) Sync External Bus Clock to CPU Speed (default)
		BIT 0	MegaSD Blink Pseudo Stereo	Status	\$3F \$40	(063)	Set External Bus Clock 3.58MHz
	(read only)	BIT 1	External Clock Mode	0=Sync to CPU, 1=3.58Mhz BIT 0 (LSB)	\$41	(065)	Set Turbo Pana 5.37MHz Set Right Inverse Audio OFF (default)
\$49		BIT 2 BIT 3	Machine Type ID (0=1chipMSX, 1=Zemmix Neo/SX-1 and related,		\$42 \$43	(066) (067)	Set Right Inverse Audio ON
Ş4S		BIT 4 BIT 5	2=SM-X/MC2P, 3=SX-2, 4=SM-X Mini/SMX-HB, 5=DE0CV, 6-14=Free, 15=Unknown)	BIT 2 BIT 3 (MSB)	\$44 \$45	(068)	Internal Audio Preset #4 "Emphasis PSG Sound" Internal Audio Preset #5 "Emphasis SCC-I Sound"
		BIT 6	NTSC/PAL Type	0=Forced, 1=Auto	\$46	(070)	Internal Audio Preset #6 "Emphasis OPLL Sound"
\$4A		BIT 7 BIT 0	Forced Video Mode Right Inverse Audio	0=60Hz (NTSC), 1=50Hz (PAL) Status	\$47 \$48	(071)	Vertical Offset 16 (useful for Ark-A-Noah) Vertical Offset 17
	(read only)	BIT 1	•	BIT 0 (LSB)	\$49	(073)	Vertical Offset 18
		BIT 2 BIT 3	Pixel Ratio 1:1 for LED Display	BIT 1 BIT 2 (MSB)	\$4A \$4B	(074) (075)	Vertical Offset 19 (default) Vertical Offset 20
		BIT 4 BIT 5	Centering YJK Modes/R25 Mask Assignment of Legacy Output	Status 0=To VGA, 1=To VGA+	\$4C \$4D	(076)	Vertical Offset 21 Vertical Offset 22
		BIT 6	Internal Slot-1 Linear	Status	\$4E	(078)	Vertical Offset 23
		BIT 7	Internal Slot-2 Linear	Status BIT 0 (LSB) only for SM-X / SX-2	\$4F \$50 \$53	(079)	Vertical Offset 24 (useful for Space Manbow) Set VGA Scanlines 0% 25% 50% 75% (default is 0%) only for SM-X / SX-2
	IN A,(\$44) : if A=\$00	BIT 1	VGA Scanlines Level (ID*25%)	BIT 1 (MSB) only for SM-X / SX-2	\$54	(084)	Set Internal PSG2 OFF (default) only for SM-X / SX-2
		BIT 2 BIT 3	Internal PSG2 SDRAM Size	Status only for SM-X / SX-2 BIT 0 (LSB)	\$55 \$56	(085)	Set Internal PSG2 ON (this second PSG acts as an external PSG) only for SM-X / SX-2 Set Extra-Mapper 4096 kB OFF (default)
\$4B	(read only)	BIT 4	(0=8MB, 1=16MB, 2=32MB, 3=reserved)	BIT 1 (MSB)	\$57	(087)	Set Extra-Mapper 4096 kB ON (only available if SDRAM > 8MB)
	[dynamic port 4B]	BIT 5 BIT 6	OCM-BIOS Reloading Req Extra-Mapper 4096kB Req	Status Status	\$58 \$59	(088) (089)	Set Extended MegaROM Reading OFF (default for compatibility) Set Extended MegaROM Reading ON (ASCII-8K/16K max size playable)
		BIT 7	Slot-0 Mode Req	0=Primary, 1=Expanded			Pixel Ratio 1:1 for LED Display (default is 0) (range 0-7) (60Hz only)
\$4B	IN A,(\$44) : if A=\$01 (read only)	BIT 0	Extended MegaROM Reading	Status	\$7F \$80	(127) (128)	Null Command (useful for programming)
94B	[dynamic port 4B]	BIT 1-7	Free	%111111 (Null)	\$81 \$82	(129)	Assign Legacy Output to VGA Assign Legacy Output to VGA+ (default)
	IN A,(\$44) : if A>\$01				\$83	(131)	Set Internal Slot-1 Linear OFF (default)
\$4B	(read only)	BIT 0-7	Free	\$FF (Null)	\$84 \$85	(132) (133)	Set Internal Slot-1 Linear ON (requires SCC-I preset) Set Internal Slot-2 Linear OFF (default)
	[dynamic port 4B]	DI= -		U. Janacua	\$86	(134)	Set Internal Slot-2 Linear ON (requires SCC-I or ASCII-8K/16K preset)
\$4C	(read only)	BIT 0 BIT 1	CPU Clock Video Output (MSB)	Hard DIP-SW1 Hard DIP-SW2	\$87 \$88	(135) (136)	Set Internal OPL3 OFF (default) only for SM-X / SX-2 Set Internal OPL3 ON only for SM-X / SX-2
		BIT 2	Video Output (LSB)	Hard DIP-SW3	\$89 \$8F	(137 143)	Reserved (Ducasp) only for SM-X / SX-2
		BIT 3 BIT 4	Cartridge Slot-1 Cartridge Slot-2 (MSB)	Hard DIP-SW4 Hard DIP-SW5	\$B0 \$B7	(176 183)	Set Master Volume 0 7 (default level is 7)
		BIT 5 BIT 6	Cartridge Slot-2 (LSB) Internal Mapper	Hard DIP-SW6 Hard DIP-SW7	\$B8 \$BF \$C0 \$C7	(184 191) (192 199)	Set PSG Volume 0 7 (default level is 4) Set SCC-I Volume 0 7 (default level is 4)
		BIT 7	Internal MegaSD	Hard DIP-SW8	\$C8 \$CF	(200 207)	Set OPLL Volume 0 7 (default level is 4)
\$4D	(read / write_n)	BIT 0 BIT 1	CALD VOALACIE (D. C.	BIT 0 (LSB) BIT 1	\$D0 \$D1	(208) (209)	Force NTSC Mode Standard NTSC/PAL Mode (bound by Control Register 9) (default)
		BIT 2	64kB VRAM Slot ID (Page 0)	BIT 2	\$D2	(210)	Force PAL Mode
		BIT 3 BIT 4		BIT 3 (MSB) BIT 0 (LSB)	\$D3 \$D4	(211) (212)	Restore Default Keyboard Layout Null Command (reserved)
		BIT 5 BIT 6	64kB VRAM Slot ID (Page 1)	BIT 1 BIT 2	\$D5 \$D6	(213) (214)	Restore Default Turbo Mode Set Centering YJK Modes/R25 Mask OFF (default)
		BIT 7		BIT 3 (MSB)	\$D7	(215)	Set Centering YJK Modes/R25 Mask ON
\$4E	(read only)	BIT 0 BIT 1		BIT 0 (LSB) BIT 1	\$F8 \$F9	(248) (249)	Reserve OCM-BIOS Reloading (cold reset or warm reset to go) Reserve Slot-0 Primary Mode (warm reset to go) (internal OPLL disabled) (C-BIOS support)
		BIT 2	OCH DIE	BIT 2	\$FA	(250)	Reserve System Logo ON (warm reset only)
		BIT 3 BIT 4	OCM-PLD main version X.Y(.z) (range 0.0.z - 25.5.z)	BIT 3 BIT 4	\$FB \$FC	(251) (252)	Cold Reset Warm Reset w/ Mapper 2048kB (RAM size 6144kB if Extra-Mapper is ON)
		BIT 5		BIT 5	\$FD	(253)	Warm Reset
		BIT 6 BIT 7		BIT 6 BIT 7 (MSB)	\$FE \$FF	(254) (255)	Warm Reset w/ Mapper 4096kB (RAM size 8192kB if Extra-Mapper is ON) Restore All Default + Reserve Default Mapper & MegaSD
\$4F	(read only)	BIT 0) L 2 I/O Revision ID (0 - 31)	BIT 0 (LSB)			
		BIT 1 BIT 2		BIT 1 BIT 2	More info on Switched I/O ports at MSX Assembly Page! < http://map.grauw.nl/resources/msx_io_ports.php#switch_io >		
		BIT 3 BIT 4		BIT 3 BIT 4 (MSB)			0 = OFF (read (with)
		BIT 5	OCM-PLD sub version (x.y).Z	BIT 0 (LSB)	R/W Logic	Positive	1 = ON (read / write)
		BIT 6 BIT 7	(range x.y.0 - x.y.3) Default Keyboard Layout	BIT 1 (MSB) Status	n, Logic	Negative	0 = ON 1 = OFF (read_n / write_n)
						CPU Clor	ck [F12] or [DIP-SW1]
			Reserved to IPL-ROM			Video Out Audio Mixer 8	
\$4C	(write only)	BIT 0 - 7	VDP ID Selector	0-1=ID0 (V9938), 2-255=ID2 (V9958)	Toggles	Cartridge S	lot-1 [SHIFT+F12] or [DIP-SW4] on the type of machine
\$4E	(write_n only)	BIT 0-6 BIT 7	- JIS2 Enabler	- 0=JIS1+JIS2, 1=JIS1 only	. 088163	Cartridge S System Re	
\$4F	(write_n only)	BIT 0 - 6	-	-		Internal Ma	apper [DIP-SW7] only
y	(BIT 7	F4 Device Mode	0=Normal, 1=Inverted		Internal Me	gaSD [DIP-SW8] only