

\$40	(read_n / write)	ID 508 ID 504	MSX2+ FS-A1 Series MSX++ Computers and Compatibles	Z80B 5.37MHz OCM-PLD v2.4 or later
\$41	(read_n / write)	BIT 0	Smart Command ID (see the table on the right)	CPL or \$FF (null)
		BIT 1		
		BIT 2		
		BIT 3		
		BIT 4		
		BIT 5		
		BIT 6		
\$42	(read / write_n)	BIT 7		
		BIT 0	CPU Clock	Virtual DIP-SW1
		BIT 1	Video Output (MSB)	Virtual DIP-SW2
		BIT 2	Video Output (LSB)	Virtual DIP-SW3
		BIT 3	Cartridge Slot-1	Virtual DIP-SW4
		BIT 4	Cartridge Slot-2 (MSB)	Virtual DIP-SW5
		BIT 5	Cartridge Slot-2 (LSB)	Virtual DIP-SW6
\$43	(read / write_n)	BIT 6	Current Mapper Size	Virtual DIP-SW7
		BIT 7	Current MegaSD Mode	Virtual DIP-SW8
		BIT 0	Lock Mask of the Toggles	CPU Clock
		BIT 1		Video Output
		BIT 2		Audio Mixer & SCRLK
		BIT 3		Cartridge Slot-1
		BIT 4		Cartridge Slot-2
BIT 5	System Reset			
BIT 6	Internal Mapper			
\$44	(read / write_n)	BIT 7	Internal MegaSD	
		BIT 0	Led 1 Status	
		BIT 1	Led 2 Status	
		BIT 2	Led 3 Status	
		BIT 3	Led 4 Status	
		BIT 4	Led 5 Status	
		BIT 5	Led 6 Status	
\$45	(read / write_n)	BIT 6	Led 7 Status	
		BIT 7	Led 8 Status	
		BIT 0	PSG Volume Level (0 - 7)	BIT 0 (LSB)
		BIT 1		BIT 1 (MSB)
		BIT 2		Status
		BIT 3		BIT 0 (LSB)
		BIT 4		BIT 1
BIT 5	BIT 2 (MSB)			
\$46	(read / write_n)	BIT 6		Master Volume Level (0 - 7)
		BIT 7	Master Mute	Status
		BIT 0	OPLL Volume Level (0 - 7)	BIT 0 (LSB)
		BIT 1		BIT 1
		BIT 2		BIT 2 (MSB)
		BIT 3		Status
		BIT 4		BIT 0 (LSB)
BIT 5	BIT 1			
\$47	(read only)	BIT 6		SCC-I Volume Level (0 - 7)
		BIT 7	SCC-I Mute	Status
		BIT 0	CPU Custom Speed Level (1 - 7)	BIT 0 (LSB)
		BIT 1		BIT 1
		BIT 2		BIT 2 (MSB)
		BIT 3		Status
		BIT 4		Turbo Pana Redirection (tPR)
BIT 5	Turbo MegaSD (tMSD)			
\$48	(read only)	BIT 6		Turbo Pana
		BIT 7	Current Keyboard Layout	0=JP, 1=Non-JP
		BIT 0	SCRLK Toggle	Status
		BIT 1	Lights Mode	0=Auto, 1=ON
		BIT 2	Red Mode (Led 0)	Status
		BIT 3	Last Reset Ack	0=Cold Reset ack, 1=Warm Reset ack
		BIT 4	Reset Required Flag	Status
\$49	(read only)	BIT 5	MegaSD Blink	Status
		BIT 6	Pseudo Stereo	Status
		BIT 7	External Clock Mode	0=Sync to CPU, 1=3.58MHz
		BIT 0	Machine Type ID	BIT 0 (LSB)
		BIT 1	(0=1chipMSX, 1=2emmx Neo/SX-1 and related, 2=SM-X/MC2P, 3=5X-2, 4=SM-X Mini/SM-X HB/u2-SX, 5=DEOCV+DEOCM, 6=5X-ESX-Lite, 7-15=Unknown)	BIT 1
		BIT 2	NTSC/PAL Type	BIT 2
		BIT 3	Forced Video Mode	BIT 3 (MSB)
\$4A	(read only)	BIT 4	Right Inverse Audio	0=Forced, 1=Auto
		BIT 5	Pixel Ratio 1:1 for LED Display	0=60Hz (NTSC), 1=50Hz (PAL)
		BIT 6	Centering YJK Modes/R25 Mask	Status
		BIT 7	Assignment of Legacy Output	0=To VGA, 1=To VGA+
		BIT 0	Internal Slot-1 Linear	Status
		BIT 1	Internal Slot-2 Linear	Status
		\$4B	(read only)	BIT 2
BIT 3	(0=None, 1=Light, 2=Medium, 3=Heavy)			BIT 1 (MSB)
BIT 4	Internal PS2			Status
BIT 5	SDRAM Size			BIT 0 (LSB)
BIT 6	(0=8 MB, 1=16 MB, 2=32 MB, 3=Over 32 MB)			BIT 1 (MSB)
BIT 7	OCM-BIOS Reloading Req			Status
\$4C	(read only)			BIT 0
		BIT 1	Slot-0 Mode Req	0=Primary, 1=Expanded
		BIT 2	Extended MegaROM Reading	Status
		BIT 3	Auxiliary SDRAM Size	BIT 0 (LSB)
		BIT 4	(0=64 MB, 1=128 MB, 2=192 MB, 3=256 MB, 4=384 MB, 5=512 MB, 6=768 MB, 7=1024 MB)	BIT 1
		BIT 5	Vertical Offset (Status+12)	BIT 2 (MSB)
		BIT 6		BIT 3
\$4D	(read only)	BIT 7		
		BIT 0	Sprite Limit	0=4/8 (standard), 1=8/8 (enhanced)
		BIT 1	VGA Interface Field	0=Single, 1=Duplicate
		BIT 2	VGA Scanlines Variant	0=Low-Scale, 1=High-Scale
		BIT 3	C-BIOS Mode	Status
		BIT 4	Extra-Mapper 4096 KB Ack	Status
		BIT 5	Current Slot-0 Mode	0=Primary, 1=Expanded
\$4E	(read only)	BIT 6	Safe Mode	Status
		BIT 7	F2 Device (ESP8266 BIOS)	Status
		BIT 0	CPU Clock	Hard DIP-SW1
		BIT 1	Video Output (MSB)	Hard DIP-SW2
		BIT 2	Video Output (LSB)	Hard DIP-SW3
		BIT 3	Cartridge Slot-1	Hard DIP-SW4
		BIT 4	Cartridge Slot-2 (MSB)	Hard DIP-SW5
\$4F	(read only)	BIT 5	Cartridge Slot-2 (LSB)	Hard DIP-SW6
		BIT 6	Internal Mapper	Hard DIP-SW7
		BIT 7	Internal MegaSD	Hard DIP-SW8
		BIT 0	64 KB VRAM Slot ID (Page 0)	BIT 0 (LSB)
		BIT 1		BIT 1
		BIT 2		BIT 2
		BIT 3		BIT 3 (MSB)
BIT 4	BIT 0 (LSB)			
BIT 5	BIT 1			
BIT 6	BIT 2			
\$4D	(read / write_n)	BIT 7	64 KB VRAM Slot ID (Page 1)	BIT 3 (MSB)
		BIT 0	I/O Revision ID (0 - 31)	BIT 0 (LSB)
		BIT 1		BIT 1
		BIT 2		BIT 2
		BIT 3		BIT 3
		BIT 4		BIT 4 (MSB)
		BIT 5		BIT 0 (LSB)
\$4E	(read only)	BIT 6		OCM-PLD main version X.Y{z}
		BIT 7	(range 0.0.z - 25.5.z)	BIT 6
		BIT 0	OCM-PLD sub version (x.y).Z	BIT 7 (MSB)
		BIT 1		BIT 0 (LSB)
		BIT 2		BIT 1 (MSB)
		BIT 3		BIT 6
		BIT 4		BIT 7
BIT 5	Status			
\$4F	(read only)	BIT 6		Default Keyboard Layout
		BIT 7		
		BIT 0	I/O Revision ID (0 - 31)	BIT 0 (LSB)
		BIT 1		BIT 1
		BIT 2		BIT 2
		BIT 3		BIT 3
		BIT 4		BIT 4 (MSB)
BIT 5	BIT 0 (LSB)			
\$40	(read / write)	BIT 6		OCM-PLD sub version (x.y).Z
		BIT 7	(range x.y.0 - x.y.3)	BIT 6
		BIT 0	Default Keyboard Layout	BIT 7 (MSB)
		BIT 1		BIT 0 (LSB)
		BIT 2		BIT 1 (MSB)
		BIT 3		BIT 6
		BIT 4		BIT 7
BIT 5	Status			