\$40	( read_n / write )	ID \$08 ID \$D4	MSX2+ FS-A1 Series MSX++ 1chipMSX, Zemmix Neo, etc.	Z80B 5.37MHz OCM-PLD v2.4 or later			SMART COMMANDS TABLE	
		BIT 0	MSX++ 1chipMSX, Zeminix Neo, etc.	OCM-PED V2.4 of later	\$00	(000)	Null Command (reserved) Set Turbo Pana Redirection OFF (default)	
\$41	( read_n / write )	BIT 1			\$01 \$02	(001)	Set Turbo Pana Redirection OFF (default) Set Standard Speed 3.58MHz	
		BIT 3 BIT 4 BIT 5	Smart Command ID	CPL or \$FF (Null)	\$03 \$04 \$05	( 003 ) ( 004 ) ( 005 )	Set Custom Speed 4.10MHz Set Custom Speed 4.48MHz	
		BIT 5 BIT 6 BIT 7			\$06 \$07	(006)	Set Custom Speed 4.90MHz Set Custom Speed 5.39MHz	
	( read / write_n )	BIT 0 BIT 1	CPU Clock	Virtual DIP-SW1 Virtual DIP-SW2	\$08 \$09	(008)	Set Custom Speed 6.10MHz Set Custom Speed 6.96MHz	
		BIT 2	Video Output (MSB) Video Output (LSB)	Virtual DIP-SW3	\$0A	(010)	Set Custom Speed 8.06MHz (aka "Turbo 10MHz") (default)	
\$42		BIT 3	Cartridge Slot-1 Cartridge Slot-2 (MSB)	Virtual DIP-SW4 Virtual DIP-SW5	\$0B \$0C	(011)	Set Turbo MegaSD OFF Set Turbo MegaSD ON (default) Set External Slot-1 + External Slot-2	
		BIT 5	Cartridge Slot-2 (LSB) Current Mapper Size	Virtual DIP-SW6 Virtual DIP-SW7	\$0D \$0E	(013)	Set Internal SCC-I Slot-1 + External Slot-2	
		BIT 7	Current MegaSD Mode	Virtual DIP-SW8 CPU Clock	\$0F \$10	(015)	Set External Slot-1 + Internal SCC-I Slot-2 Set Internal SCC-I Slot-1 + Internal SCC-I Slot-2	
	( read / write_n )	BIT 1 BIT 2		Video Output Audio Mixer & SCRLK	\$11 \$12	(017) (018)	Set External Slot-1 + Internal ASCII-8K Slot-2 Set Internal SCC-I Slot-1 + Internal ASCII-8K Slot-2	
\$43		BIT 3 BIT 4	Lock Mask of the Toggles	Cartridge Slot-1 Cartridge Slot-2	\$13 \$14	( 019 ) ( 020 )	Set External Slot-1 + Internal ASCII-16K Slot-2 Set Internal SCC-I Slot-1 + Internal ASCII-16K Slot-2	
		BIT 5 BIT 6		Hard Reset Key Internal Mapper	\$15 \$16	( 021 ) ( 022 )	Set Japanese Keyboard Layout Set Non-Japanese Keyboard Layout	
		BIT 7		Internal MegaSD Led 1 Status	\$17 \$18	(023)	Set Display Mode 15KHz Composite/S-Video Set Display Mode 15KHz RGB w/ Audio Out	
		BIT 1 BIT 2		Led 2 Status Led 3 Status	\$19 \$1A	( 025 ) ( 026 )	Set Display Mode 31Khz VGA for LED TV or LED Display Set Display Mode 31Khz VGA+ for CRT Monitor (legacy output)	also HDMI AV on SM-X also HDMI AV on SM-X
\$44	( read / write_n )	BIT 3 BIT 4	Lights I/O (SM-X and SX-2 only use Led 8 but	Led 4 Status Led 5 Status	\$1B \$1C	( 027 ) ( 028 )	Set VDP Speed Normal Mode (default) Set VDP Speed Fast Mode (V9958 only)	
		BIT 5	this register works the same way)	Led 6 Status Led 7 Status	\$1D \$1E	(029)	Reserve MegaSD OFF (warm reset to go) Reserve MegaSD ON (warm reset to go)	
		BIT 7		Led 8 Status BIT 0 (LSB)	\$1F \$20	(031)	Set MegaSD Blink OFF Set MegaSD Blink ON (default)	
\$45	( read / write_n )	BIT 1 BIT 2	PSG Volume Level	BIT 1 BIT 2 (MSB)	\$21 \$22	(033)	Set Lights Mode OFF w/ Auto LEDs Control (default) Set Lights Mode ON + Red Led OFF	
	( read only )	BIT 3 BIT 4	PSG Mute	Status BIT 0 (LSB)	\$23 \$24	(035)	Set Lights Mode ON + Red Led ON Internal Audio Preset #1 "Mute Sound"	
	( read / write_n )	BIT 5 BIT 6	Master Volume Level	BIT 1 BIT 2 (MSB)	\$25 \$26	(037)	Internal Audio Preset #2 "Middle Sound" Internal Audio Preset #3 "High Sound" (default)	
	( read only )	BIT 7	Master Mute	Status BIT 0 (LSB)	\$27 \$28	( 039 )	Set CMT OFF (default) (disabled w/ MSXtR BIOS)  Set CMT ON (needs a cassette recorder) (disabled w/ MSXtR BIOS)	n/a on SM-X / SX-2 n/a on SM-X / SX-2
\$46	( read / write_n )	BIT 1 BIT 2	OPLL Volume Level	BIT 1 BIT 2 (MSB)	\$29 \$2A	(041)	Lock Turbo Toggles Unlock Turbo Toggles	
	( read only )	BIT 3 BIT 4	OPLL Mute	Status BIT 0 (LSB)	\$2B \$2C	( 043 ) ( 044 )	Lock Display Toggles Unlock Display Toggles	
	( read / write_n )	BIT 5 BIT 6	SCC-I Volume Level	BIT 1 BIT 2 (MSB)	\$2D \$2E	( 045 ) ( 046 )	Lock Audio Mixer & SCRLK Toggles Unlock Audio Mixer & SCRLK Toggles	
	( read only )	BIT 7	SCC-I Mute	Status BIT 0 (LSB)	\$2F \$30	(047)	Lock Slot-1 Toggles Unlock Slot-1 Toggles	
\$47		BIT 1 BIT 2	CPU Custom Speed Level	BIT 1 BIT 2 (MSB)	\$31 \$32	(049)	Lock Slot-2 Toggles Unlock Slot-2 Toggles	
	( read only )	BIT 3	Turbo MegaSD (tMSD) Turbo Pana Redirection (tPR)	Status Status	\$33 \$34	(051)	Lock Slot-1 & Slot-2 Toggles Unlock Slot-1 & Slot-2 Toggles	
		BIT 5 BIT 6	VDP Speed Mode Mapper Size Reg	0=Normal, 1=Fast 0=2048kB, 1=4096kB	\$35 \$36	(053)	Lock Hard Reset Key Unlock Hard Reset Key	
		BIT 7	MegaSD Mode Req Turbo Pana	Status Status	\$37 \$38	(055)	Lock Mapper Toggle Unlock Mapper Toggle	
\$48	( read only )	BIT 1 BIT 2	Current Keyboard Layout SCRLK Toggle	0=JP, 1=Non-JP Status	\$39 \$3A	(057)	Lock MegaSD Toggle Unlock MegaSD Toggle	
		BIT 3	Lights Mode Red Mode (Led 0)	0=Auto, 1=ON Status	\$3B \$3C	(059)	Lock All Toggles Unlock All Toggles (default)	
		BIT 5 BIT 6	Last Reset Flag Reset Required Flag	0=Cold, 1=Warm Status	\$3D \$3E	(061)	Set Pseudo-Stereo OFF (default) Set Pseudo-Stereo ON (needs an external sound cartridge)	
		BIT 7	MegaSD Blink Pseudo Stereo	Status Status	\$3F \$40	(063)	Sync External Bus Clock to CPU Speed (default) Set External Bus Clock 3.58MHz	
\$49	( read only )	BIT 1 BIT 2	External Clock Mode	0=Sync to CPU, 1=3.58Mhz BIT 0 (LSB)	\$41 \$42	( 065 ) ( 066 )	Set Turbo Pana 5.37MHz Set Right Inverse Audio OFF (default)	
		BIT 3 BIT 4	Machine Type ID (0=1chipMSX, 1=Zemmix Neo/SX-1 and related, 2=SM-X/MC2P, 3=SX-2, 4=SM-X Mini/SMX-HB,	BIT 1	\$43 \$44	(067)	Set Right Inverse Audio ON Internal Audio Preset #4 "Emphasis PSG Sound"	
		BIT 5 BIT 6	5=DEOCV, 6-14=Free, 15=Unknown) NTSC/PAL Type	BIT 3 (MSB) 0=Forced, 1=Auto	\$45 \$46	( 069 ) ( 070 )	Internal Audio Preset #5 "Emphasis SCC-I Sound" Internal Audio Preset #6 "Emphasis OPLL Sound"	
\$4A	( read only )	BIT 7	Forced Video Mode	0=60Hz (NTSC), 1=50Hz (PAL) Status	\$47 \$48	(071)	Vertical Offset 16 (useful for Ark-A-Noah)  Vertical Offset 17	
		BIT 1 BIT 2	Right Inverse Audio Pixel Ratio 1:1 for LED Display	BIT 0 (LSB) BIT 1	\$48 \$49 \$4A	(072)	Vertical Offset 18 Vertical Offset 19 (default)	
		BIT 3 BIT 4		BIT 2 (MSB) Status	\$4B \$4C	(075)	Vertical Offset 20 Vertical Offset 21	
		BIT 5	Centering YJK Modes/R25 Mask Assignment of Legacy Output	0=To VGA, 1=To VGA+	\$4D \$4E	( 076 ) ( 077 )	Vertical Offset 22 Vertical Offset 23	
		BIT 6	Internal Slot-1 Linear Internal Slot-2 Linear	Status Status	\$4F	(078)	Vertical Offset 24 (useful for Space Manbow)	only for SM-X / SX-2
	IN A,(\$44) : if A=\$00	BIT 0	VGA Scanlines Level (ID*25%) Internal PSG2	BIT 0 (LSB) only for SM-X / SX-2 BIT 1 (MSB) only for SM-X / SX-2 Status only for SM-X / SX-2	\$50 \$53 \$54	(080 083 )	Set VGA Scanlines 0% 25% 50% 75% (default is 0%) Internal PSG2 OFF (default) Internal PSG2 ON (this second PSG acts as an external PSG)	only for SM-X / SX-2 only for SM-X / SX-2 only for SM-X / SX-2
\$4B	( read only )	BIT 2 BIT 3 BIT 4	SDRAM Size	BIT 0 (LSB)	\$55 \$56	(085)	Set Extra-Mapper 4096 kB OFF (default)	Ully IUI SWI-X / SX-2
	[dynamic port 4B]	BIT 4 BIT 5 BIT 6	(0=8MB, 1=16MB, 2=32MB, 3=reserved) OCM-BIOS Reloading Req Extra-Mapper 4096kB Req	BIT 1 (MSB) Status Status	\$57 	( 087 ) 	Set Extra-Mapper 4096 kB ON (only available if SDRAM > 8MB)	
	[dynamic port 45]	BIT 7	Slot-0 Mode Req	0=Primary, 1=Expanded	\$7F	(127)	Pixel Ratio 1:1 for LED Display (default is 0) (range 0-7) (60Hz only)	
	IN A,(\$44) : if A>\$00	BIT 0		BIT 0 (LSB)	\$80 \$81	(128)	Null Command (useful for programming) Assign Legacy Output to VGA	
\$4B	( read only )	BIT 2 BIT 3	Free	BIT 2 BIT 3	\$82 \$83	(130) (131)	Assign Legacy Output to VGA+ (default) Set Internal Slot-1 Linear OPF (default) Set Internal Slot-1 Linear ON (requires SCC-1 preset)	
		BIT 4		BIT 4 BIT 5	\$84 \$85	(132)	Set Internal Slot-1 Linear ON (requires SCC-I preset) Set Internal Slot-2 Linear OFF (default) Set Internal Slot-2 Linear ON (requires SCC-I or ASCII-8K/16K preset)	
	[dynamic port 4B]	BIT 6 BIT 7	2201 20 1	BIT 6 BIT 7 (MSB)	\$86 \$87	(134)	Set Internal OPL3 OFF ON (default)	only for SM-X / SX-2
\$4C	( read only )	BIT 0	CPU Clock Video Output (MSB)	Hard DIP-SW1 Hard DIP-SW2	\$88 \$89 \$8F	( 136 ) ( 137 143 )	Set Internal OPL3 ON Reserved (Ducasp)	only for SM-X / SX-2 only for SM-X / SX-2
		BIT 2 BIT 3 BIT 4	Video Output (LSB)  Cartridge Slot-1  Cartridge Slot-2 (MSB)	Hard DIP-SW3 Hard DIP-SW4 Hard DIP-SW5	ćpo	(176 183 )	Set Master Volume 0 7 (default level is 7)	
		BIT 5 BIT 6	Cartridge Slot-2 (MSB) Cartridge Slot-2 (LSB) Internal Mapper	Hard DIP-SW5 Hard DIP-SW6 Hard DIP-SW7	\$80 \$87 \$88 \$8F \$C0 \$C7	( 176 183 ) ( 184 191 ) ( 192 199 )	Set Master Volume U 7 (default level is 7) Set PSG Volume 0 7 (default level is 4) Set SCC-I Volume 0 7 (default level is 4)	
		BIT 7	Internal Mapper Internal MegaSD	Hard DIP-SW8	\$C8 \$CF	( 200 207 )	Set OPLL Volume 0 7 (default level is 4)  Force NTSC Mode	
\$4D	( read / write_n )	BIT 0	64kB VRAM Slot ID (Page 0)	BIT 0 (LSB) BIT 1	\$D0 \$D1	(208)	Force NTSC Mode Standard NTSC/PAL Mode (bound by Control Register 9) (default) Force PAL Mode	
		BIT 2 BIT 3		BIT 2 BIT 3 (MSB)	\$D2 \$D3	(210) (211)	FORCE PAL MODE Restore Default Keyboard Layout Null Command (reserved)	
		BIT 4	64kB VRAM Slot ID (Page 1)	BIT 0 (LSB) BIT 1	\$D4 \$D5 \$D6	(212) (213)	Restore Default Turbo Mode	
		BIT 6 BIT 7		BIT 2 BIT 3 (MSB)	\$D6 \$D7	(214) (215)	Set Centering YJK Modes/R25 Mask OFF (default) Set Centering YJK Modes/R25 Mask ON	
\$4E	( read only )	BIT 0		BIT 0 (LSB) BIT 1	\$F8 \$F9	( 248 ) ( 249 )	Reserve OCM-BIOS Reloading (cold reset or warm reset to go) Reserve Slot-0 Primary Mode (warm reset to go) (internal OPLL disabled) Reserve System Logo ON (warm reset only)	
		BIT 2 BIT 3	OCM-PLD main version X.Y(.z)	BIT 2 BIT 3	\$FA \$FB	( 250 ) ( 251 )	Cold Reset	
		BIT 4 BIT 5	(range 0.0.z - 25.5.z)	BIT 4 BIT 5	\$FC \$FD	( 252 ) ( 253 )	Warm Reset w/ Mapper 2048kB (RAM size 6144kB if Extra-Mapper is ON) Warm Reset Warm Reset w/ Mapper 4096kB (RAM size 8192kB if Extra-Mapper is ON)	
		BIT 6 BIT 7		BIT 6 BIT 7 (MSB)	\$FE \$FF	( 254 ) ( 255 )	Warm Reset w/ Mapper 4096kB (RAM size 8192kB if Extra-Mapper is ON) Restore All Default + Reserve Default Mapper & MegaSD	
\$4F	( read only )	BIT 0	1/0 5 12 /	BIT 0 (LSB) BIT 1	More in	ifo on Switched I/O i	ports at MSX Assembly Page! < http://map.grauw.nl/resources/msx_io_ports.p	hp#switch_io >
		BIT 2 BIT 3	I/O Revision ID (0 - 31)	BIT 2 BIT 3				
		BIT 4	OCM-PLD sub version (x.y).Z	BIT 4 (MSB) BIT 0 (LSB)	R/W Logic	Positive	0 = OFF 1 = ON 0 = ON	
		BIT 6 BIT 7	(range x.y.0 - x.y.3) Default Keyboard Layout	BIT 1 (MSB) Status		Negative	0 = ON 1 = OFF (read_n / write_n)	
			Reserved to IPL-ROM			CPU Cloc Video Out	put [(SHIFT+)PRTSCR] or [DIP-SW2/3]	SCRLK key could handle
\$4C	( write only )	BIT 0-7	VDP ID Selector	0-1=ID0 (V9938), 2-255=ID2 (V9958)	Toggles	Audio Mixer & Cartridge Sl	lot-1 [SHIFT+F12] or [DIP-SW4]	CMT or OPL3 depending on the type of machine
\$4E	( write_n only)	BIT 0 - 6 BIT 7	- JIS2 Enabler	- 0=JIS1+JIS2, 1=JIS1 only	i oggies	Cartridge SI System Re	lot-2 [SHIFT+SCRLK] or [DIP-SW5/6]	
\$4F	( write_n only)	BIT 0 - 6 BIT 7	- F4 Device Mode	- 0=Normal, 1=Inverted		Internal Ma Internal Me	pper [DIP-SW7] only	