ROACH0: F-Engine 160 MHz Clock, Time Division Multiplex factor 4 S-Engine Input Cornerturn XAUI (5. bit Quantization 128×32 .12 2048 (QDR) Amplitude Equalization Muxed Time sample to FFT Window Reorder ant Gb/s) 32x 40 MS/s TI ADS5272 Phase Equalization point Biplex Real 2048 point X-Engine Input 12bit ADC PFB Cornerturn (QDR) 128 x 1024 chan XAUI (5.12 Antenna bit Quantization 뀕 Reorder Gb/s) **Amp Coefficients Phase Coefficients**