

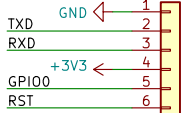
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# Programming Header

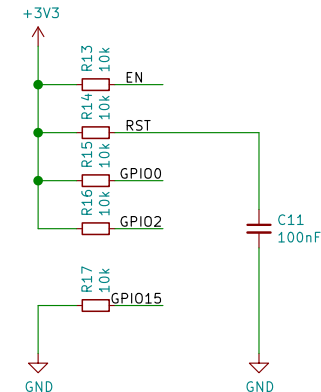
Diagram of a 6-pin programming header (J2) with the following connections:

| Signal | Pin |
|--------|-----|
| GND    | 1   |
| TXD    | 2   |
| RXD    | 3   |
| +3V3   | 4   |
| GPIO0  | 5   |
| RST    | 6   |



The diagram shows an ESP8266 module (U2, ESP-12E) with the following components and connections:

- Power Supply:**
  - +24V:** Connected to R18 (430k) and R19 (10k). R19 is connected to GND. A green line connects the node between R18 and R19 to the EN pin (pin 3).
  - +3V3:** Connected to pin 8 (VCC), pin 15 (GND), and pin 4 (GND). Pin 15 is connected to C9 (10uF) to GND. Pin 4 is connected to C10 (100nF) to GND.
- Reset:** RST pin (pin 1) is connected to a green line that also connects to the EN pin (pin 3).
- GPIOs and Test Points:**
  - GPIO0 (pin 18) is connected to TXD (pin 22).
  - GPIO2 (pin 17) is connected to RXD (pin 21).
  - GPIO4 (pin 19) is connected to IO12 TestPoint (pin 6).
  - GPIO5 (pin 20) is connected to IO16 TestPoint (pin 5).
  - GPIO15 (pin 4) is connected to R17 (10k) to GND.
- Other Connections:**
  - CS0 (pin 9) is connected to GND.
  - MISO (pin 10) is connected to GND.
  - GPIO9 (pin 11) is connected to GND.
  - GPIO10 (pin 12) is connected to GND.
  - MOSI (pin 13) is connected to GND.
  - SCLK (pin 14) is connected to GND.



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