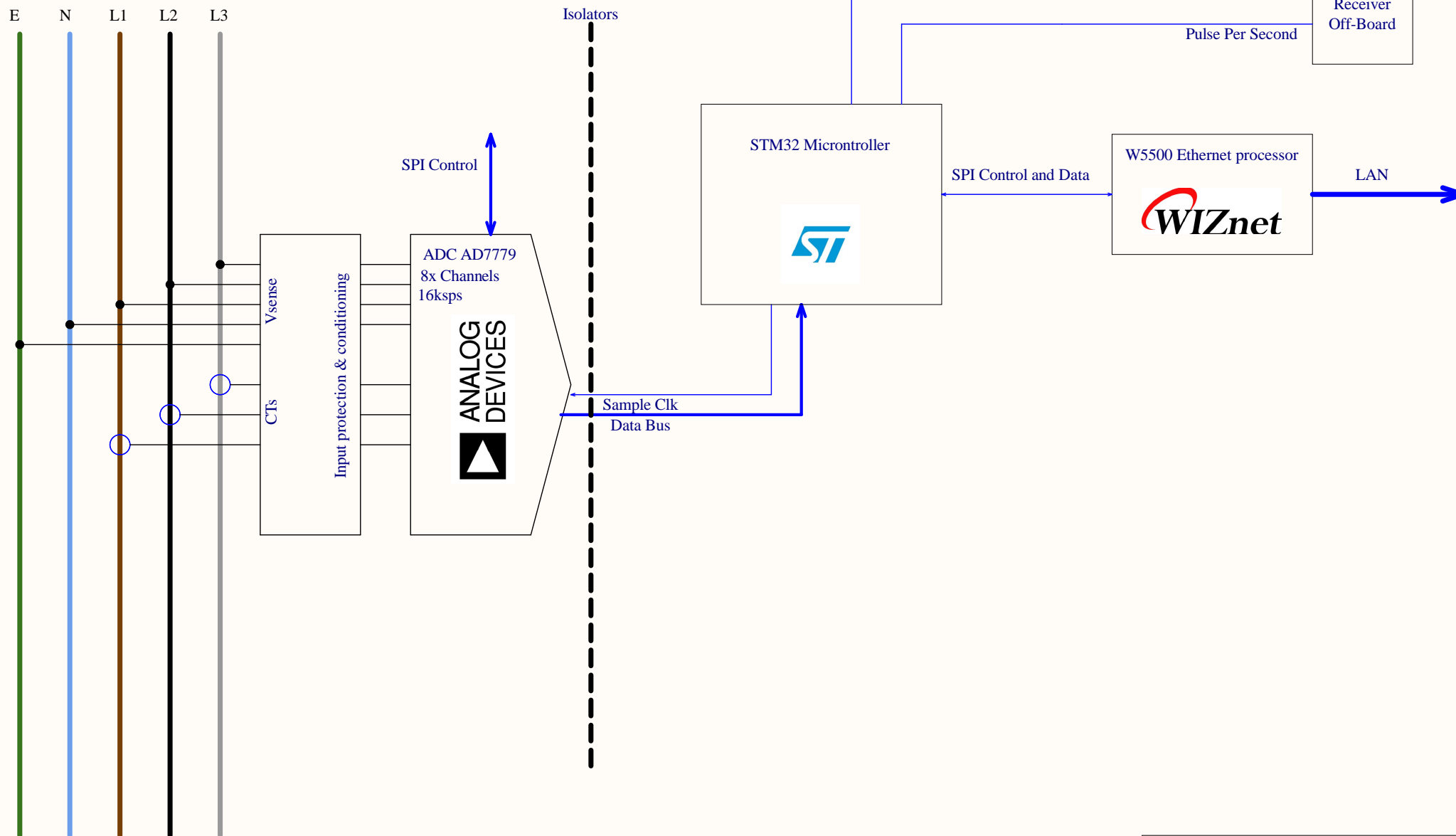


Open Power System Estimation Unit 0.2



Title System Diagram		
Size A3	Number	Revision
Date: 7/26/2020	Sheet of	
File: C:\Source\...\011 - CoverSheet.SchDoc	Drawn By:	

1		2		3		4																																																		
A	<div>PCB Issues</div> <div>Link speed LED +/- are back the front in the sch symbol</div> <div>The Ethernet RMII TXD0/1 and RXD0/1 are not differential pairs! Routing should be changed to space the signals so they don't couple.</div>							A																																																
B								B																																																
C								C																																																
D	<table><tr><td colspan="8">Title</td></tr><tr><td colspan="8">README</td></tr><tr><td colspan="2">Size</td><td colspan="4">Number</td><td colspan="2">Revision</td></tr><tr><td colspan="2">A4</td><td colspan="4"></td><td colspan="2"></td></tr><tr><td colspan="2">Date:</td><td colspan="4">7/26/2020</td><td colspan="2">Sheet of</td></tr><tr><td colspan="2">File:</td><td colspan="4">C:\Source\...\[02] - Notes.SchDoc</td><td colspan="2">Drawn By:</td></tr></table>							Title								README								Size		Number				Revision		A4								Date:		7/26/2020				Sheet of		File:		C:\Source\...\[02] - Notes.SchDoc				Drawn By:		D
Title																																																								
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A

B

C

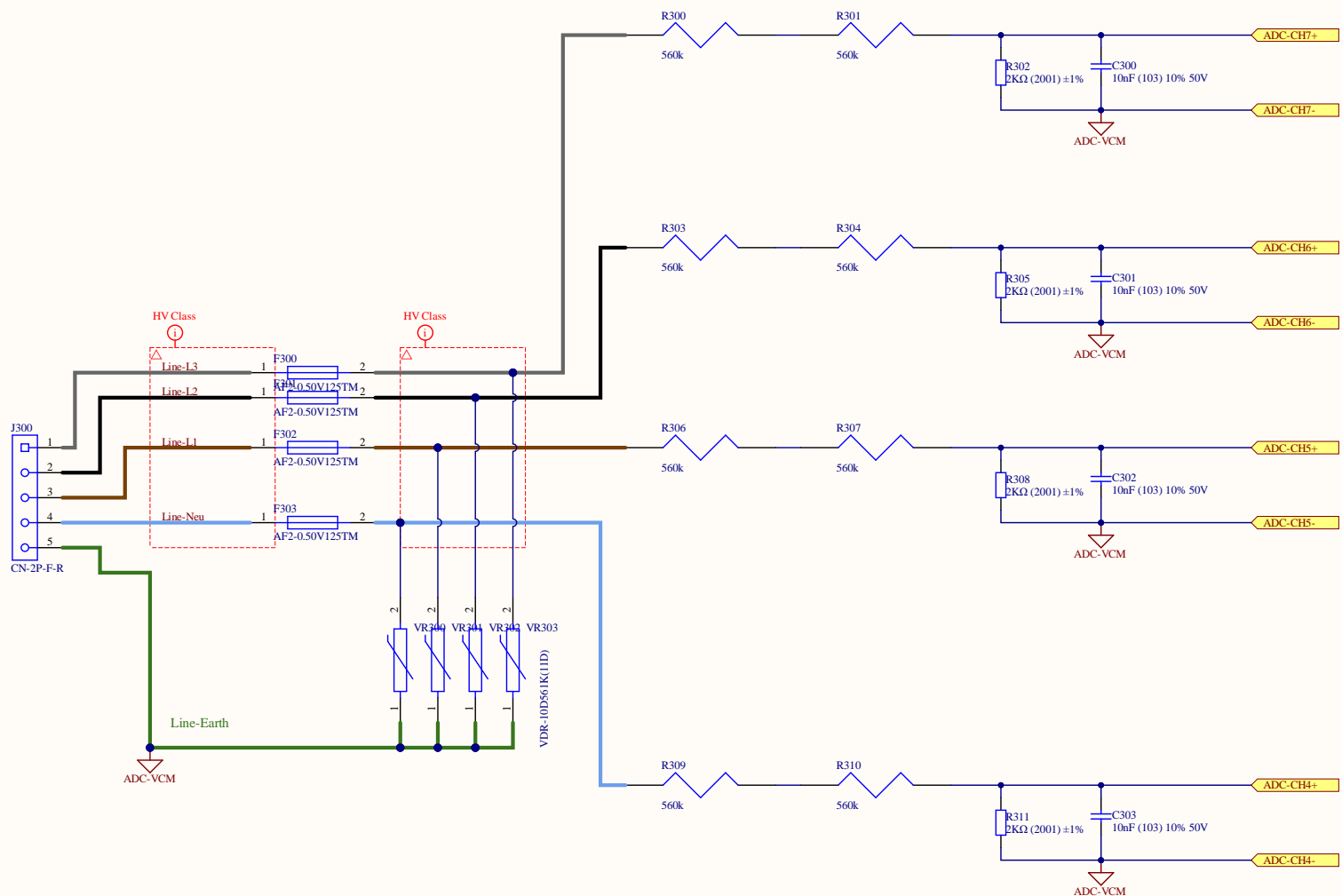
D

A

B

C

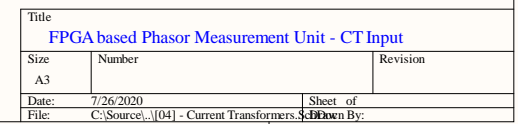
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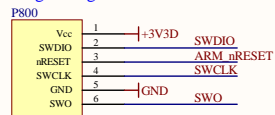


Vin nom. (p-p)= $250*\sqrt{2}*2=707V$
Vadc-input (peak-peak nominal) = $707*2/(560*2+2) = 1.26V$
Vadc ref = 2.5V

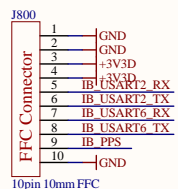
Input f-3db = $1/(2 * \pi * 2k * C)$
Input f-3db = $1/(2 * \pi * 2k * 10n) = 8kHz$

Title		
HV Input		
Size	Number	Revision
A3		
Date:	7/26/2020	Sheet of
File:	C:\Source\...\03] - HV Input.SchDoc	Drawn By:

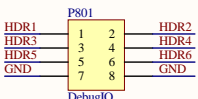




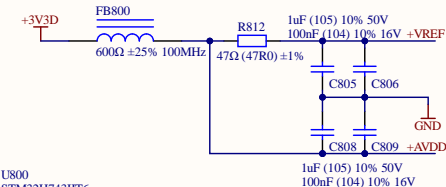
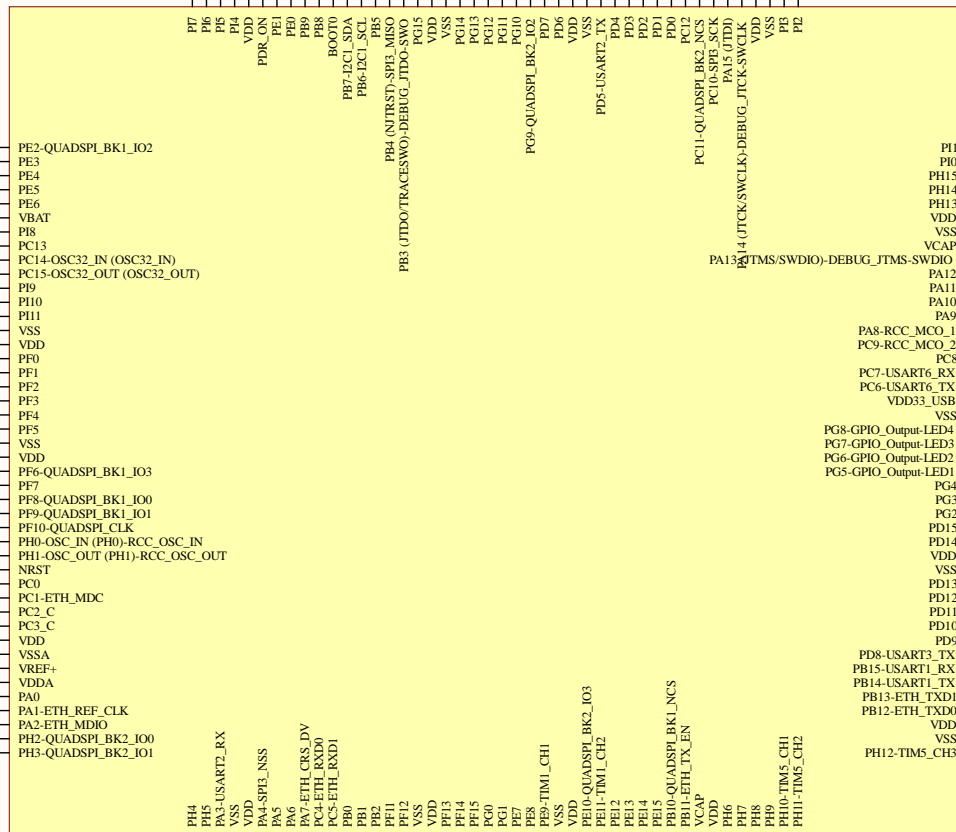
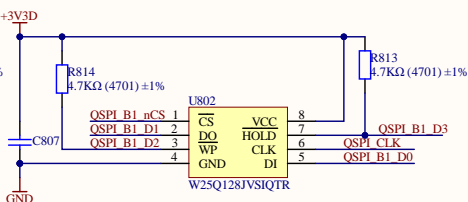
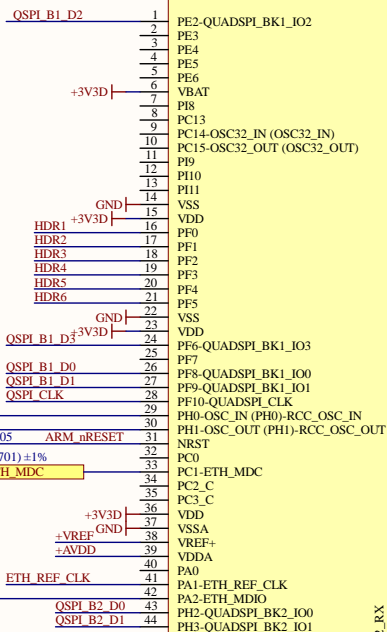
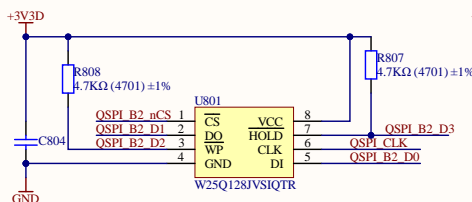
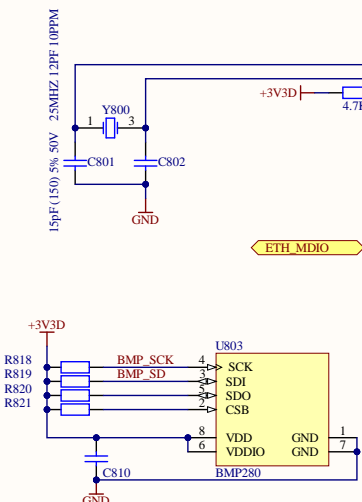
tag_connect_TC2030



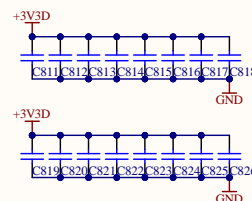
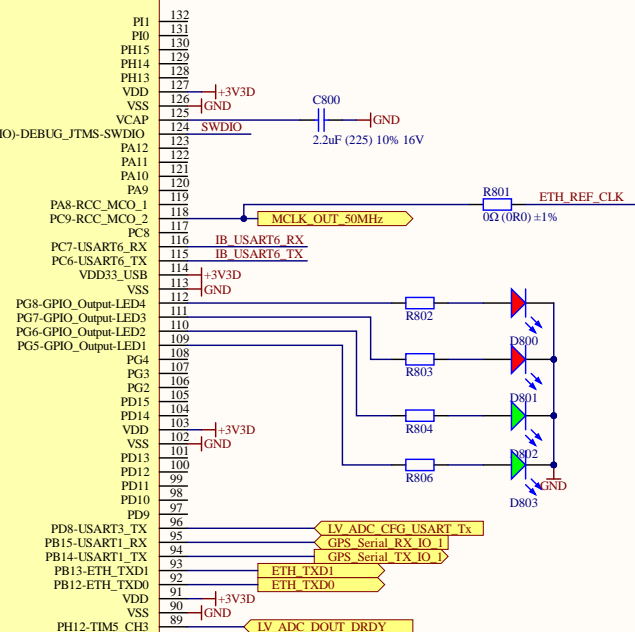
10pin 10mm FFC



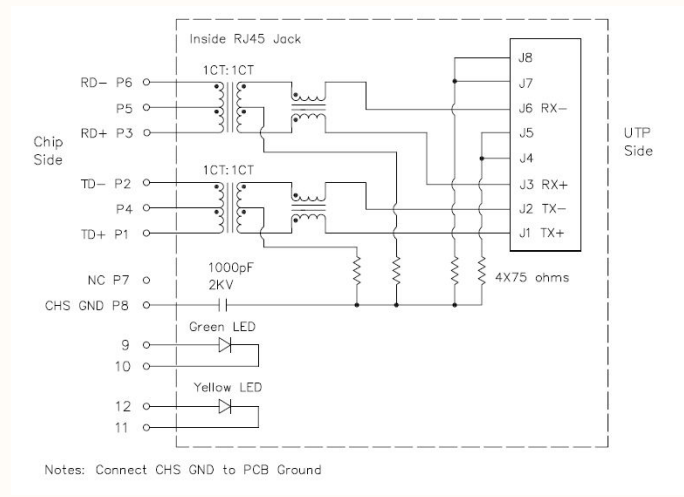
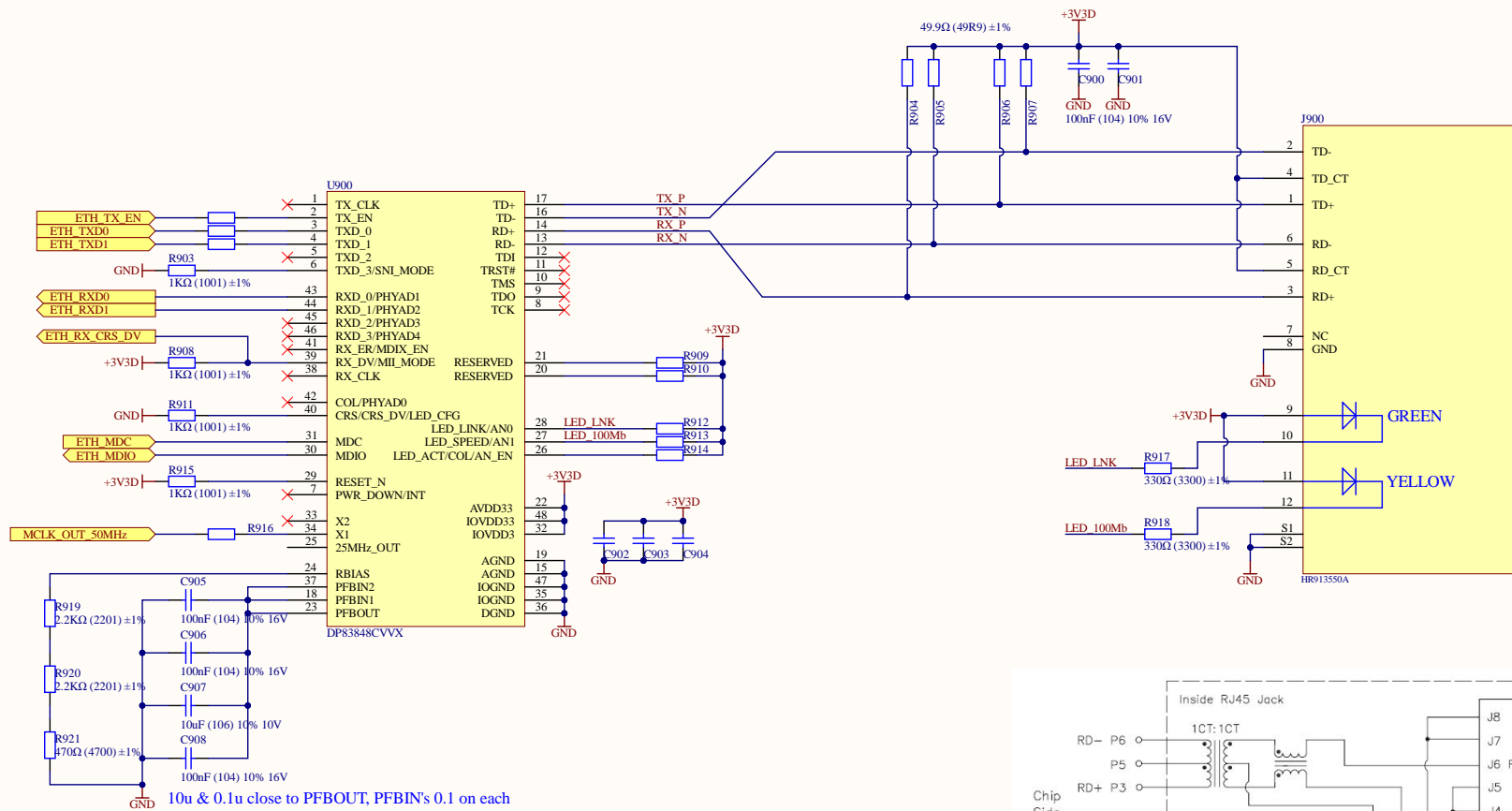
Debug



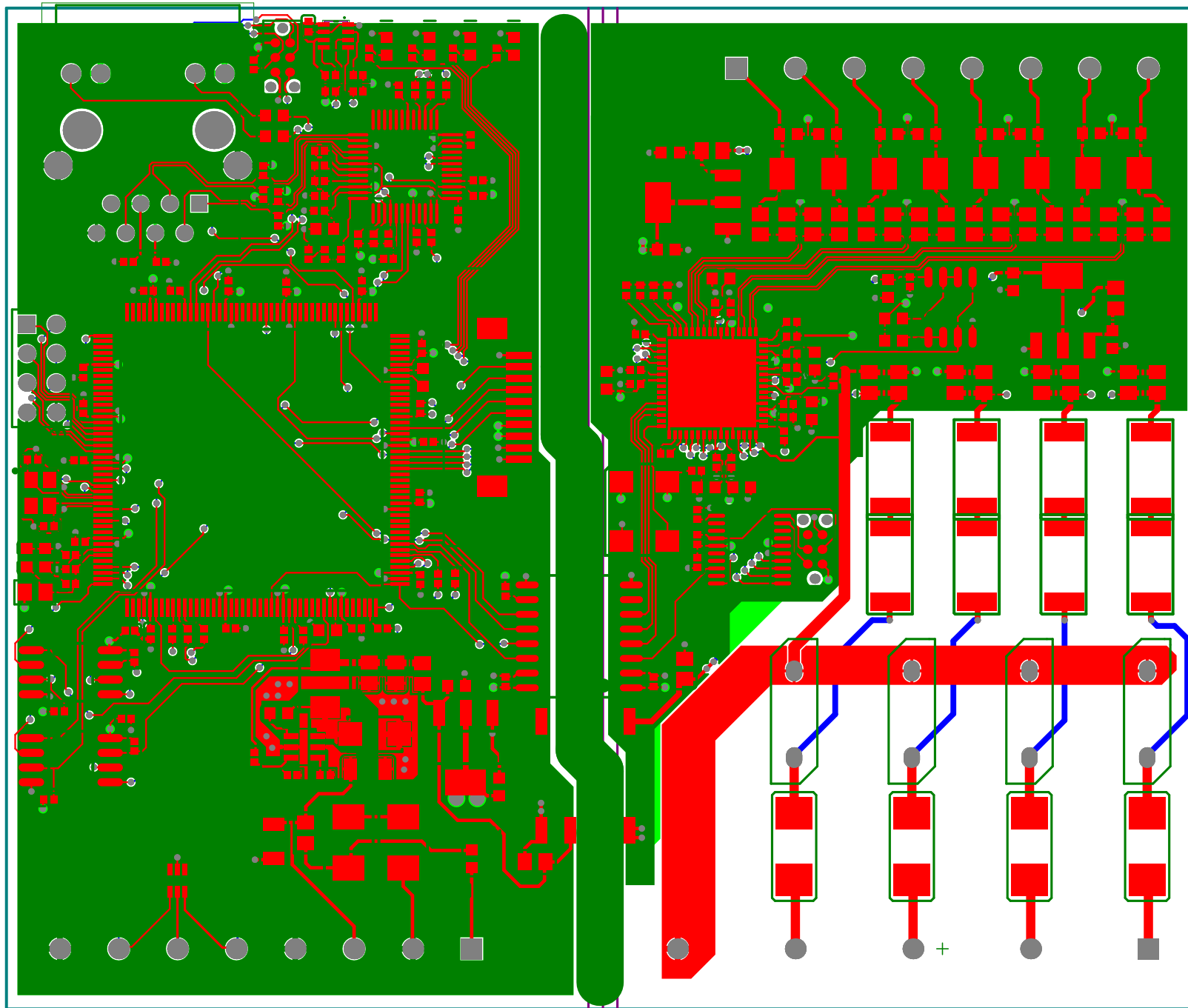
U800
STM32H743IIT6



Title		
STM32 - Main Processor		
Size	Number	Revision
A3		
Date:	7/26/2020	Sheet of
File:	C:\Source\...\I081 - Microcontroller.SchDoc Drawn By:	



Title		Ethernet PHYTER	
Size	Number	Revision	
A3			
Date:	7/26/2020	Sheet of	
File:	C:\Source\...\[09] - Ethernet.SchDoc	Drawn By:	



Box - CNMB/6

PCB size - Slot A -86.5x103mm

61010 - <300VAC minimum clearance distance 1.5mm