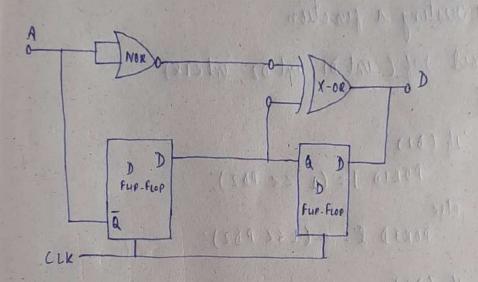
GATE EE 2023

COMPLETED

a 56. Neglecting the delays due to the logic gates in the corcint shown in figure, the decimal equivalent of the bimary sequence [ABCD] of initial logic states, which will not change with clock, is _____



SOLUTION :-

f from circuit (1998) 1)

$$D_1 = B \oplus C = A_2 \oplus A_1$$

$$D_2 = A_1$$

let, a, a, = 0 0

 \bar{A}_2 A_2 A_1 $(A_1 \oplus A_2)$

the decimal equivalent of 1000 is (8), = 8

```
CODE:-

> # include < avr/io.h>

> # include < util/delay.h>

> 11 Declaring all Variables as integers

> int Di, D2, CLK;

> int Q1=0, Q2=0;
```

> void ref (int D1, int D2, int (1x)

PORTD 1= (1 44 PD2);

PORT) &= ~ (1 << PD2);

PORT D 1= (1 << PD3);

PORTD & = ~ (1 < 7 D3);

PORT B 1= (1 < < PB3);

PORT B L = ~ (1 << PB3)

11 Setting DDR registers for required pins

-> 11 Oreating a function

"好()1)

4 ()2)

-) if ((LK)

int main (void) {

```
-> DDRD 1= (1 << PD2) 1 (1 << PD3);
```

- -> 11 the loop function runs over a over again
- > while (1) f

-> }

7 7

COMPONENTS ; -

^{*} Arduino UNO

^{* 7-} Segment Display

^{* 7447 1}C

PATM:-

14 Nov 12:35

PERSONAL STANDARD

/sdeard / digital-derign/av7-gu/setup/codes

18

* Makefile & avor elf * main. e * main. her

* wor c * avr. here * main eep * main. o

* avr. eep * avr. o * main. elf

To open file code = nvin aur.c

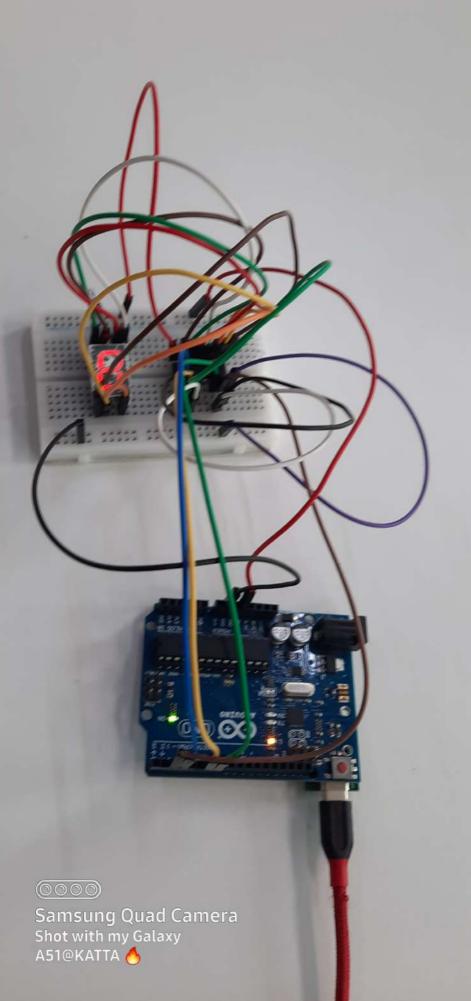
To run file code = make

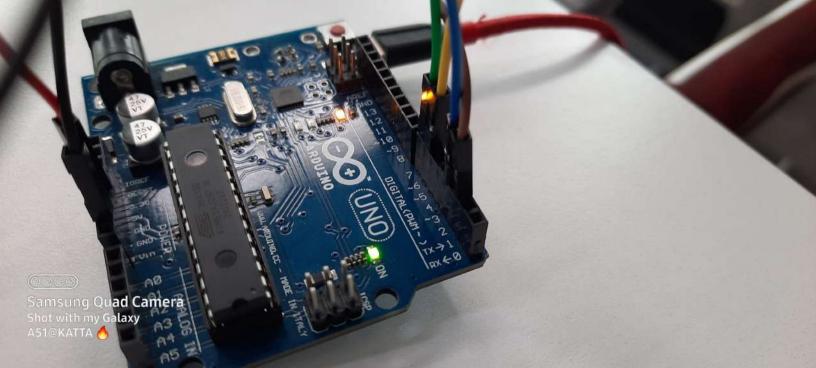
wer. hen

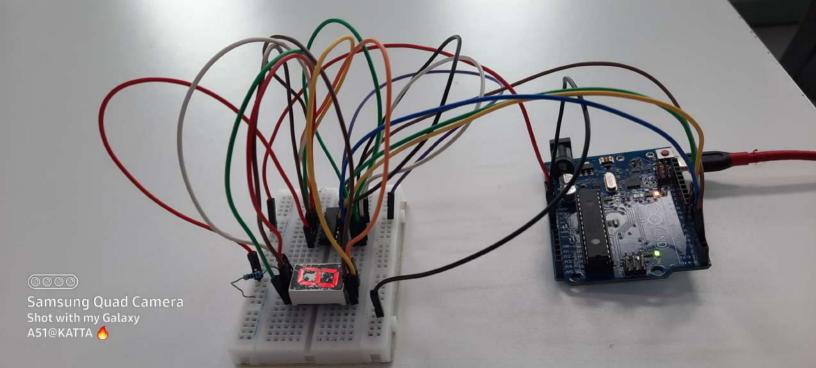
SKETCH UPLOAD IN ARDUINODROID=>

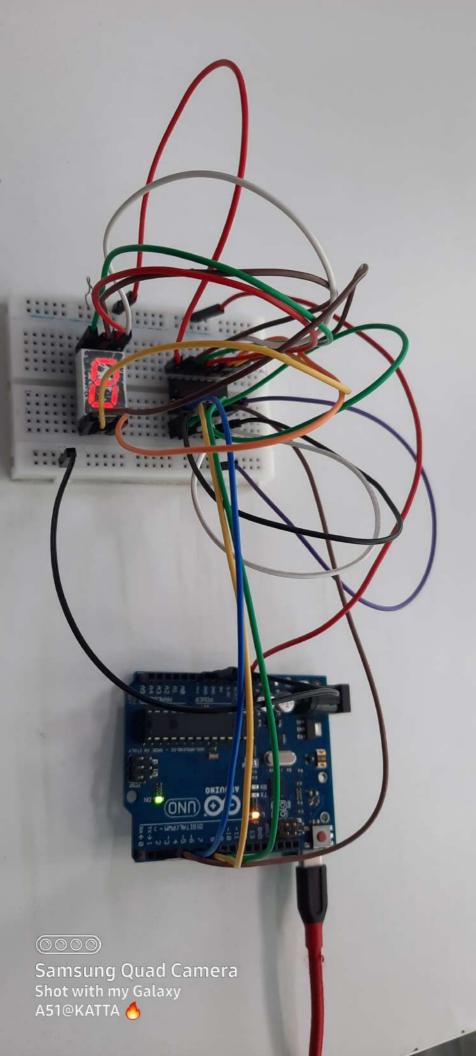
Actions -> Upload -> Upload precompiled -> digital - design -> aver-gu -> setup -> codes ->

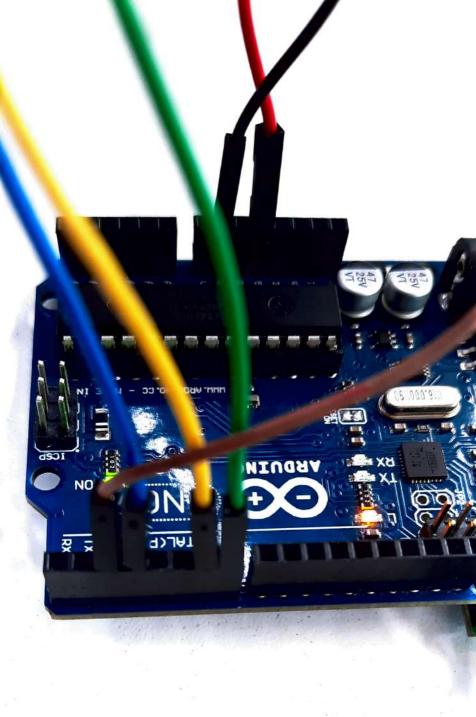
* avor. here * main. here > select * avor. here













Samsung Quad Camera
Shot with my Galaxy
AS1@KAITA





Samsung Quad Camera Shot with my Galaxy AST@KATTA

