Course Outline for ENGR x52

DIGITAL ELECTRONICS

I. CATALOG DESCRIPTION:
ENGR x52 — DIGITAL ELECTRONICS — 3.00 units

Architecture, programming, application and troubleshooting of complex programmable logic device (CPLD) electronic systems. Digital building blocks, number systems, Boolean algebra, combinational and sequential logic, integrated logic families, digital circuit measurement techniques and instrumentation, troubleshooting techniques.

3.00 Units Lecture

Grading Methods:

Discipline:

	MIN
Lecture Hours:	54.00
Expected Outside of Class Hours:	108.00
Total Hours:	162.00

II. NUMBER OF TIMES COURSE MAY BE TAKEN FOR CREDIT:

III. PREREQUISITE AND/OR ADVISORY SKILLS:

IV. MEASURABLE OBJECTIVES:
Upon completion of this course, the student should be able to:

V. CONTENT:

VI. METHODS OF INSTRUCTION:

VII. TYPICAL ASSIGNMENTS:

VIII. EVALUATION:
Methods/Frequency

IX. TYPICAL TEXTS:

X. OTHER MATERIALS REQUIRED OF STUDENTS: