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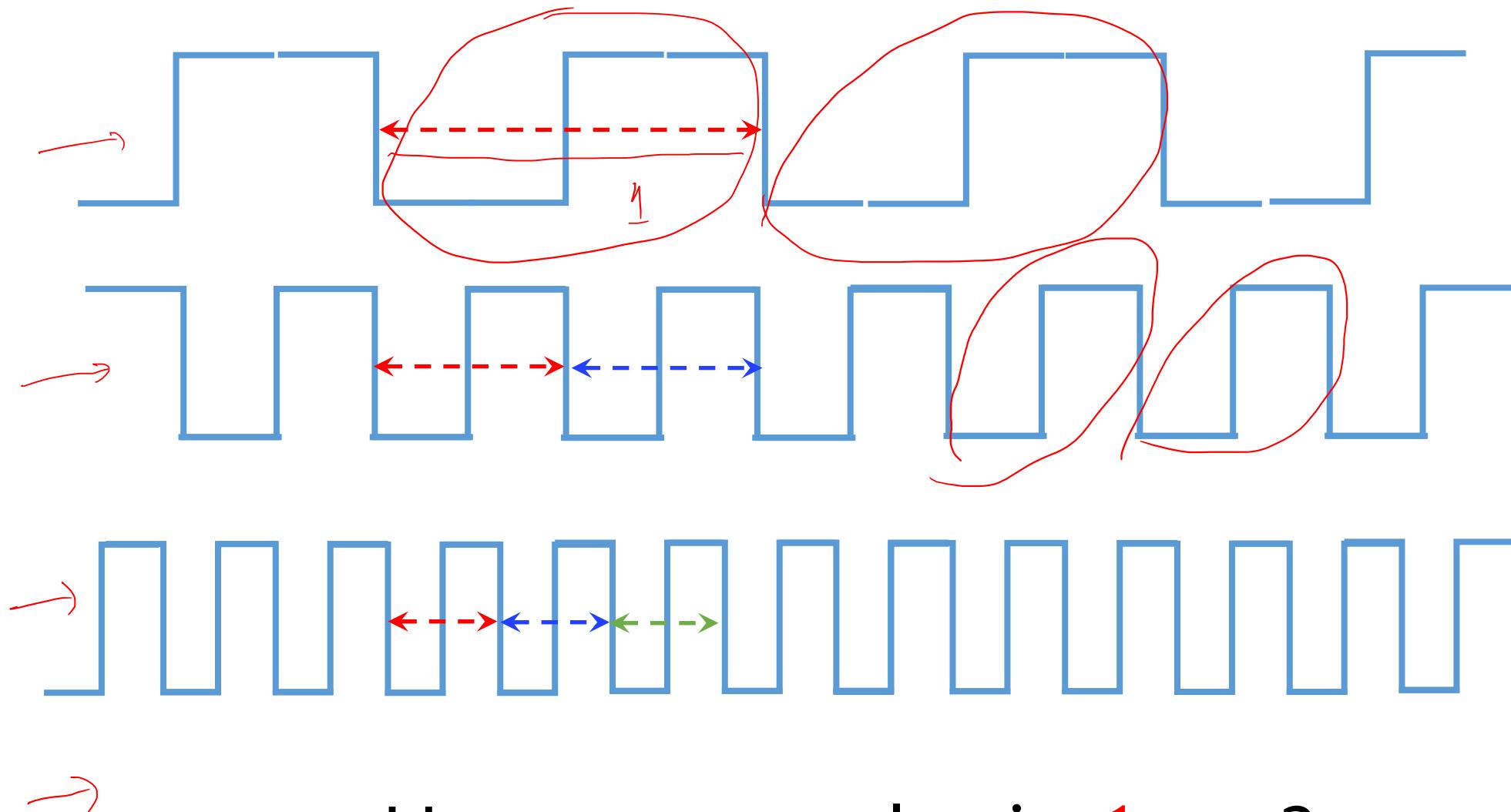
SETs do not apply for Continuing Education courses such as IB, AQ, and ELIP courses.

Instructions for completing SET: http://ask.uwindsor.ca/app/answers/detail/a_id/176

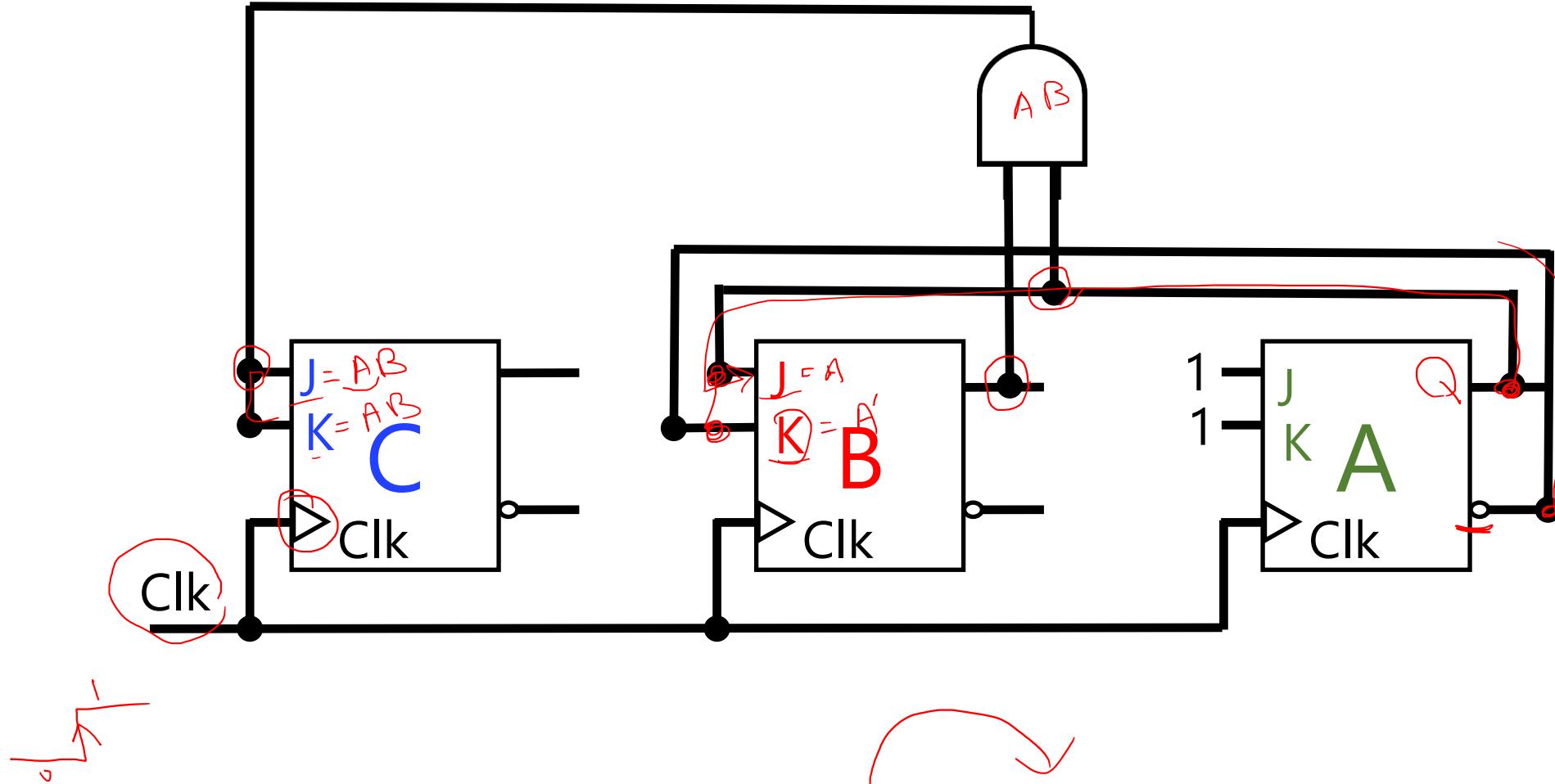
Clock (revisited)

shortened as *clk*

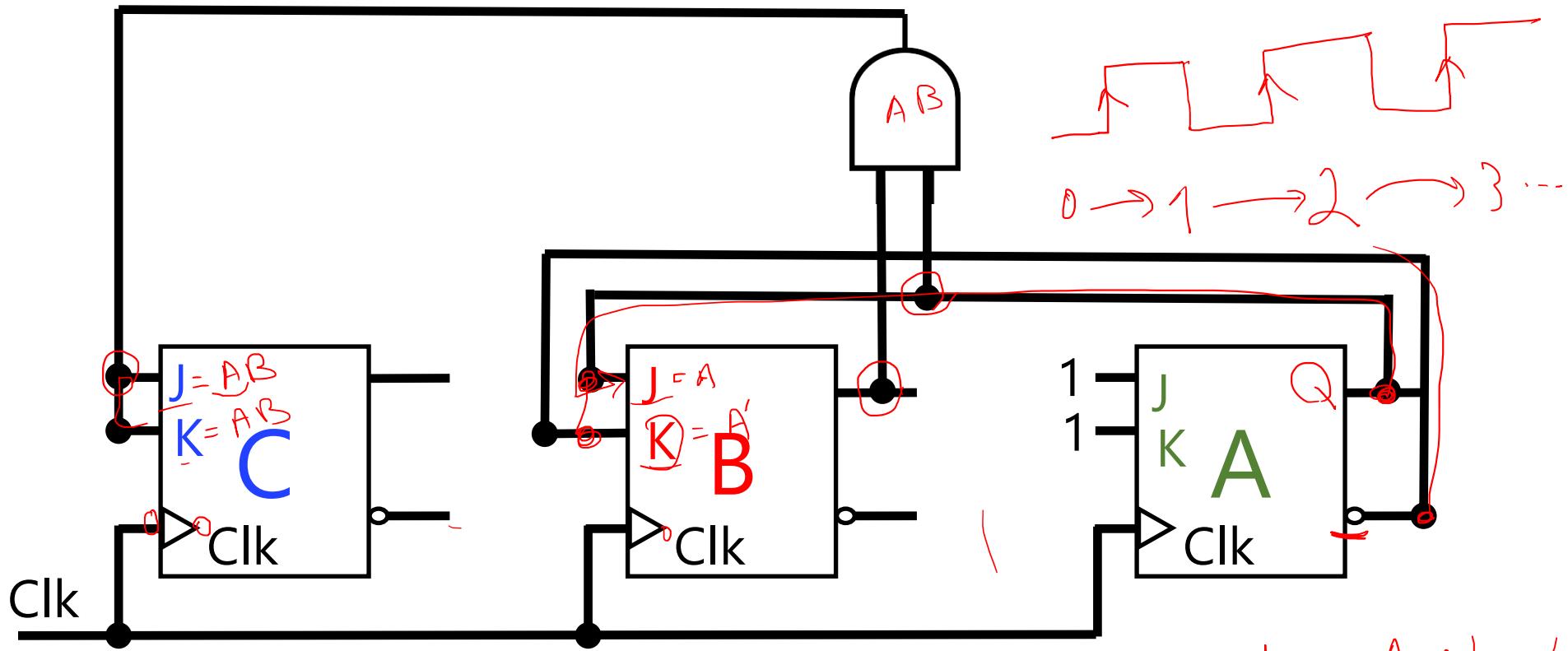
timing device that generates a train of pulses



How many pulse in **1 sec**?



Counter 0 to 7



$\text{Clk} \rightarrow 1 \text{ pulse/sec}$

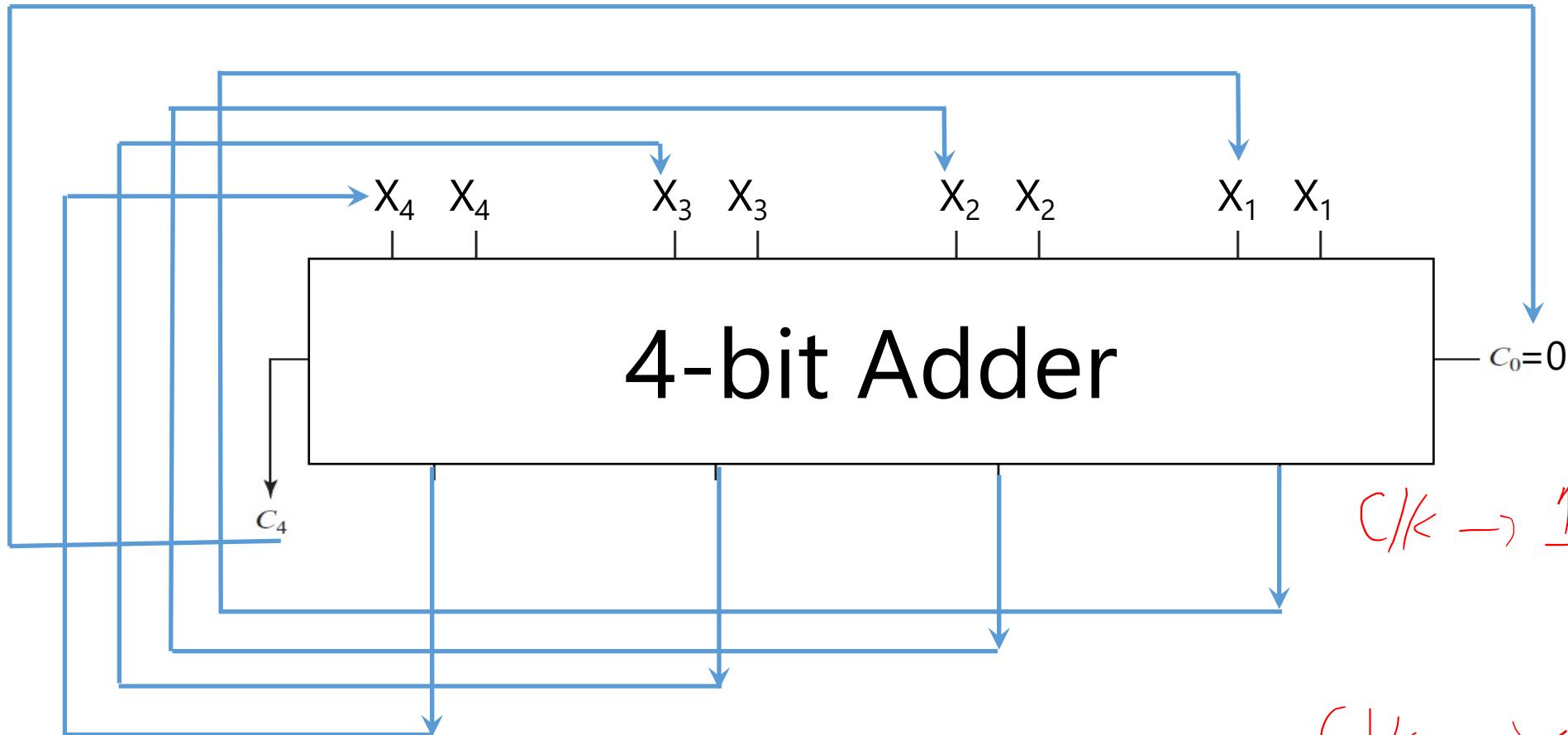
$\rightarrow 1 \text{ Hz}$

$$\Delta t = 7 \text{ sec}$$

Counter 0 to 7

How many pulses? Elapsed time?

$$\begin{aligned} \text{Clk} &\rightarrow 7 \text{ pulses/sec} \\ &\rightarrow 7 \times 2 \end{aligned}$$



$C/K \rightarrow 1/2$

$\leftarrow sec$

$C/I/K \Rightarrow Y + H_2$

$X \times Y = X + \dots + X$ → When to stop?

Feedback → Sequential Logic

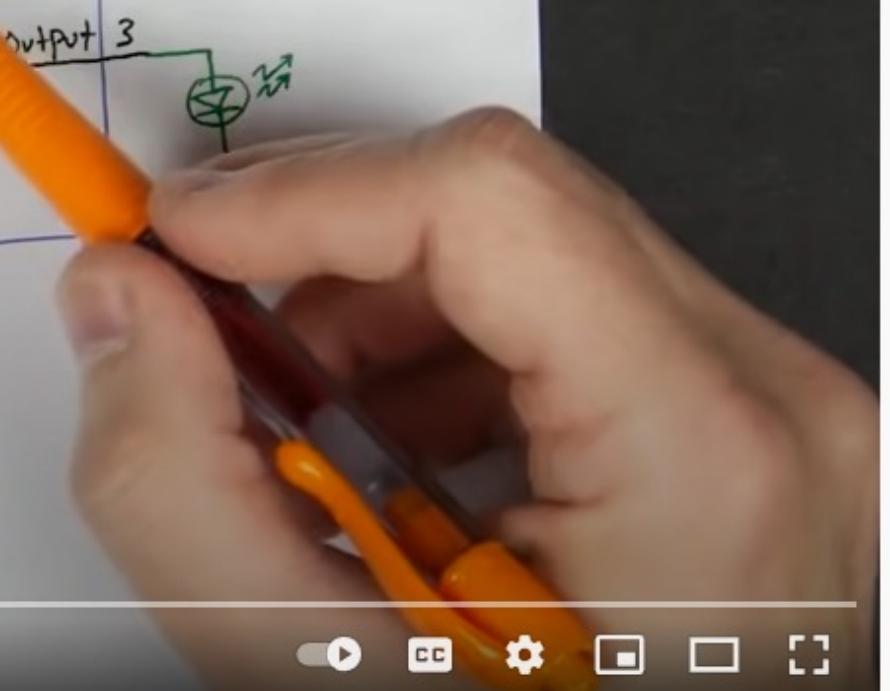
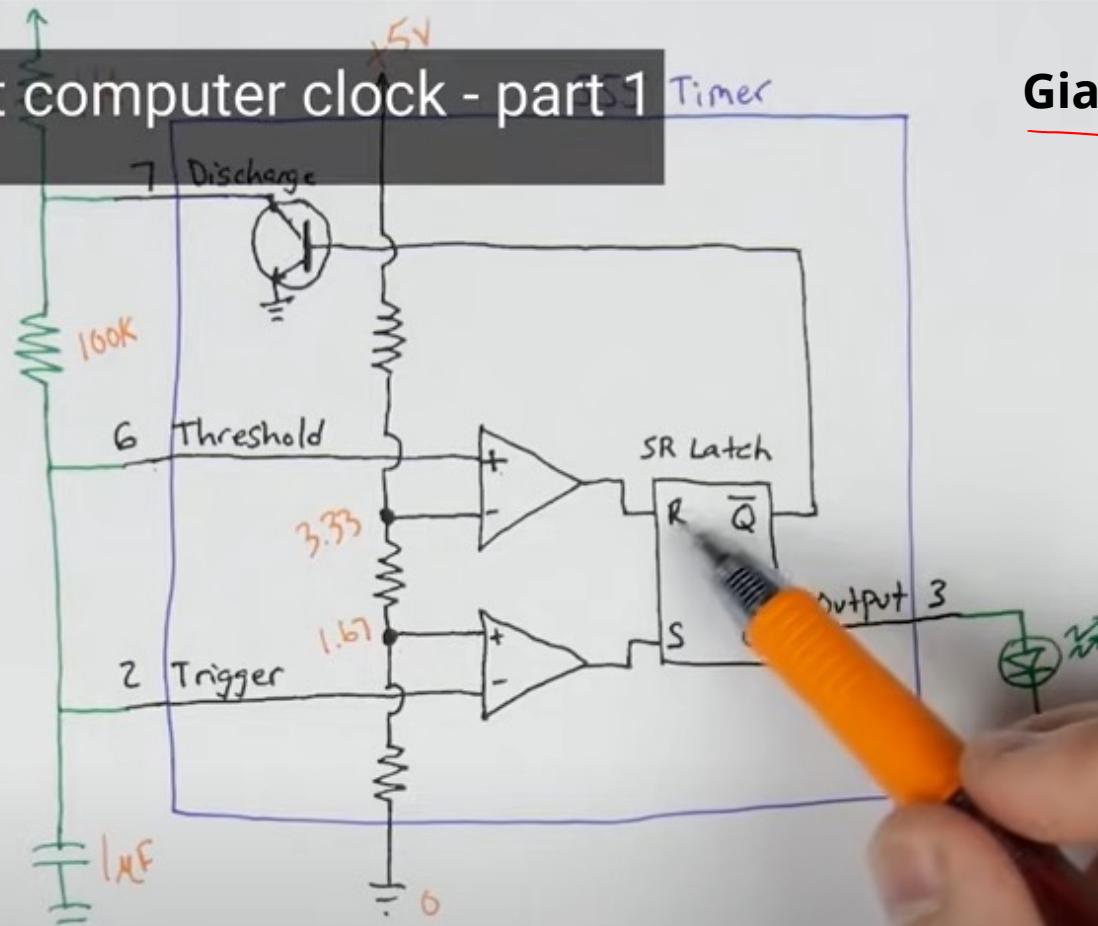


Astable 555 timer - 8-bit computer clock - part 1

Ben Eater

i

Giavi Tran



▶ ▶ 🔍 4:54 / 12:43

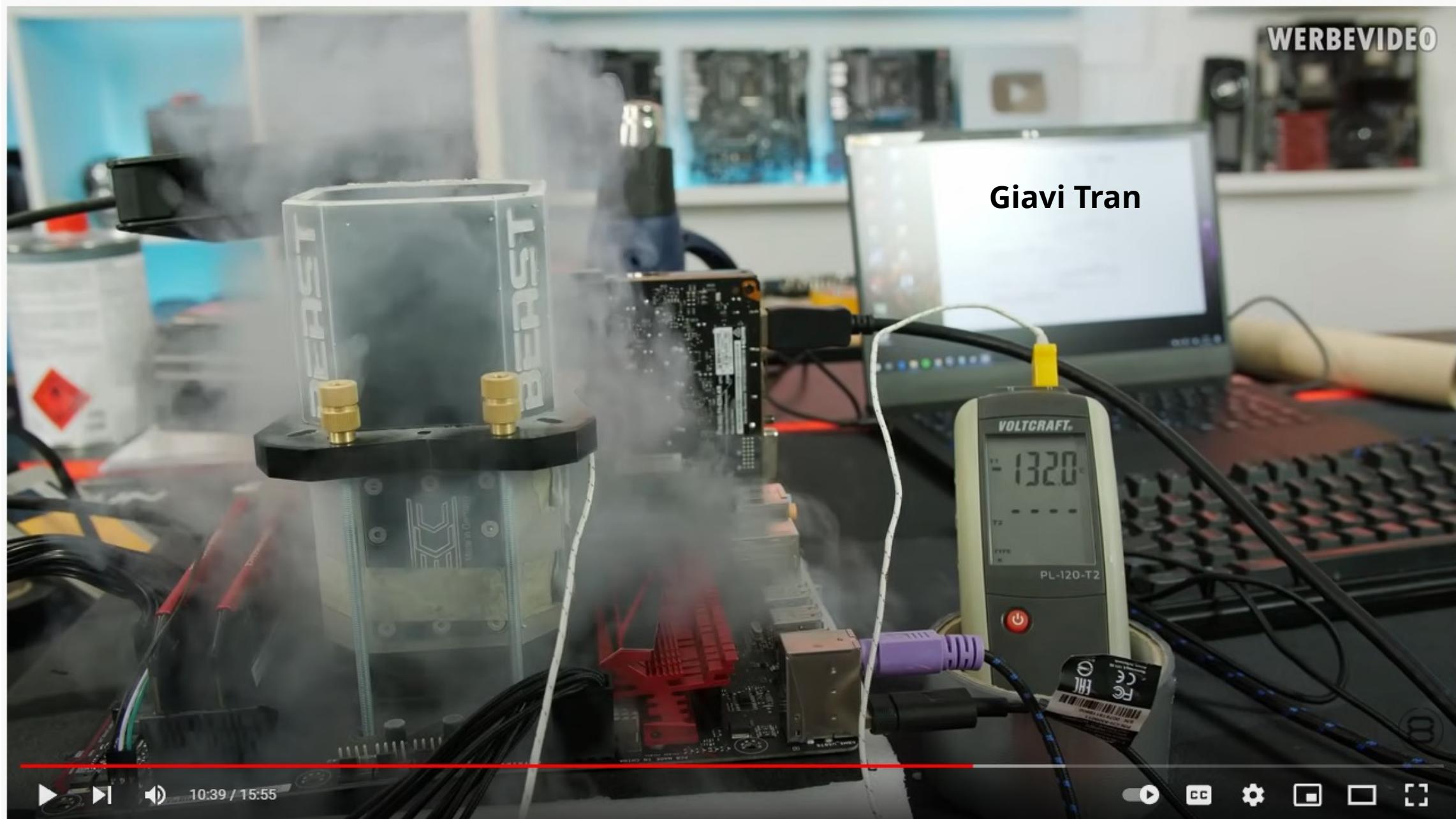
▶ CC ⚙ □ □ □

Water Cooling a TI-84 Graphing Calculator!

3,263,682 views • May 27, 2020

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Giavi Tran



We Overclocked an AMD CPU to over 8.1 GHz !!

3,138,659 views • Apr 22, 2020

71K

DISLIKE

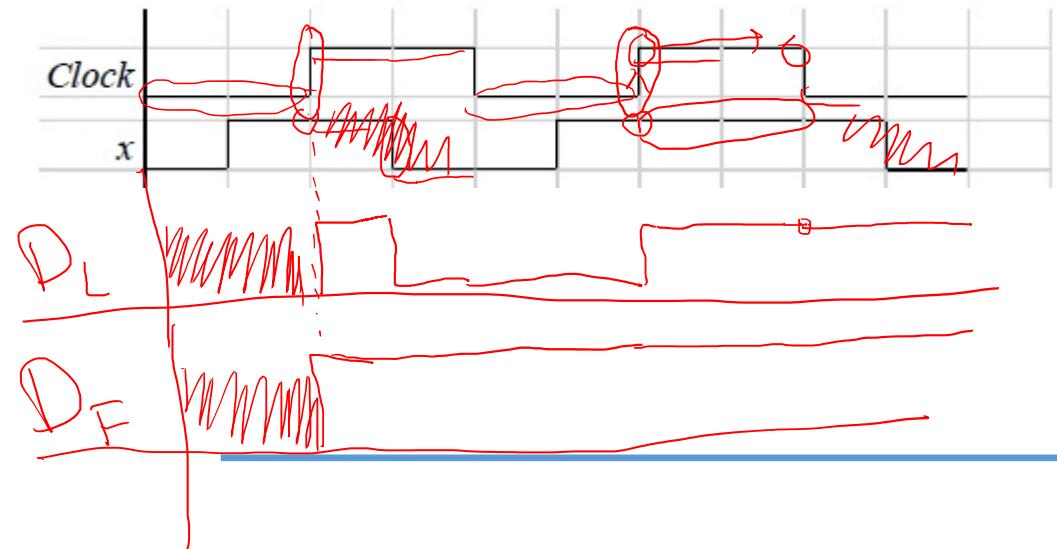
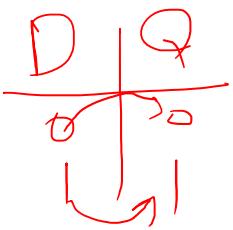
SHARE

THANKS

CLIP

SAVE

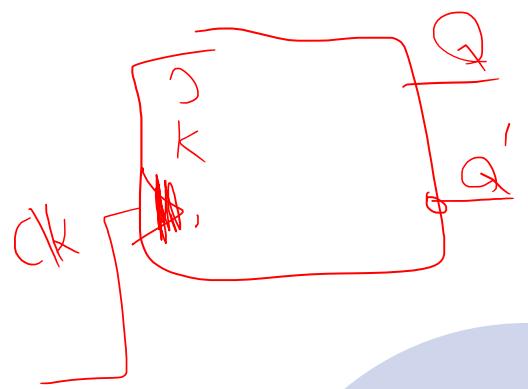
...



Latch Active high (def)
vs. low
FlipFlop Pos edges (delta)

 Practice!

neg edge
 both edges



A hand-drawn truth table for a JK flip-flop. The columns are labeled S, J, K, Q, and Q'. The rows show the state transitions:

S	J	K	Q	Q'
0	0	0	0	1
0	0	1	1	0
1	0	0	1	0
1	1	1	0	1

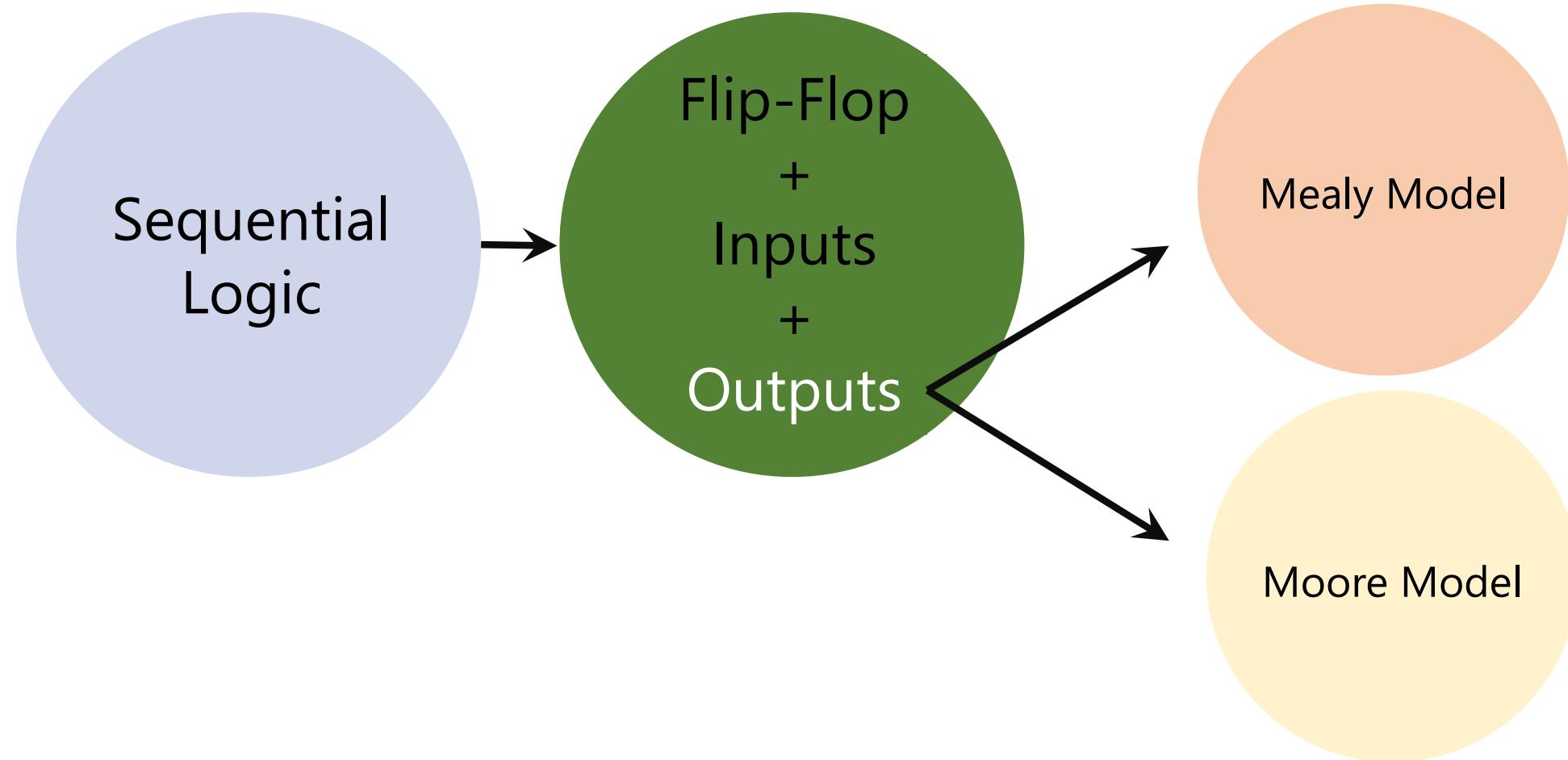
Sequential
Logic

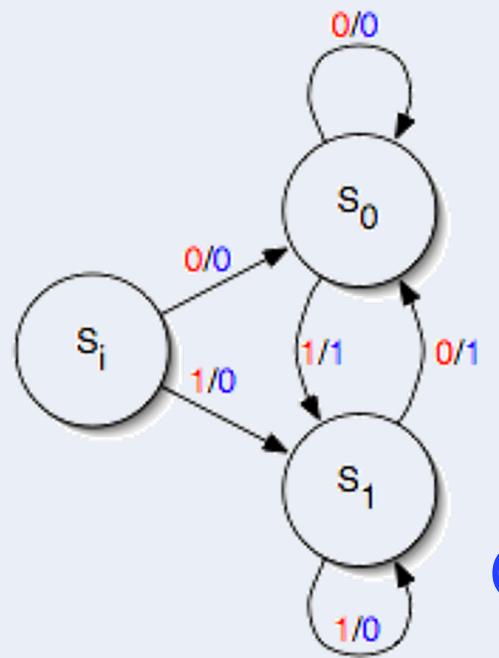


Only
Flip-Flop
+
Inputs
+
Outputs

Flip-Flop
+
Inputs

Analysis
vs.
Design



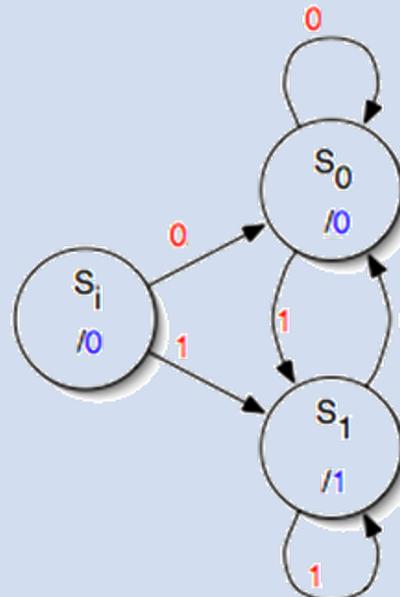


George H. Mealy
(1927 – 2010)
Mathematician and Computer Scientist
Invented Mealy Machine
Also a pioneer of modular programming

Outputs = Function(Current State, Inputs)

Edward Forrest Moore
(1925 – 2003)

Mathematician and Computer Scientist
Inventor of the Moore Machine
Also an early pioneer of artificial life



Outputs = Function(Current State, ~~Inputs~~)

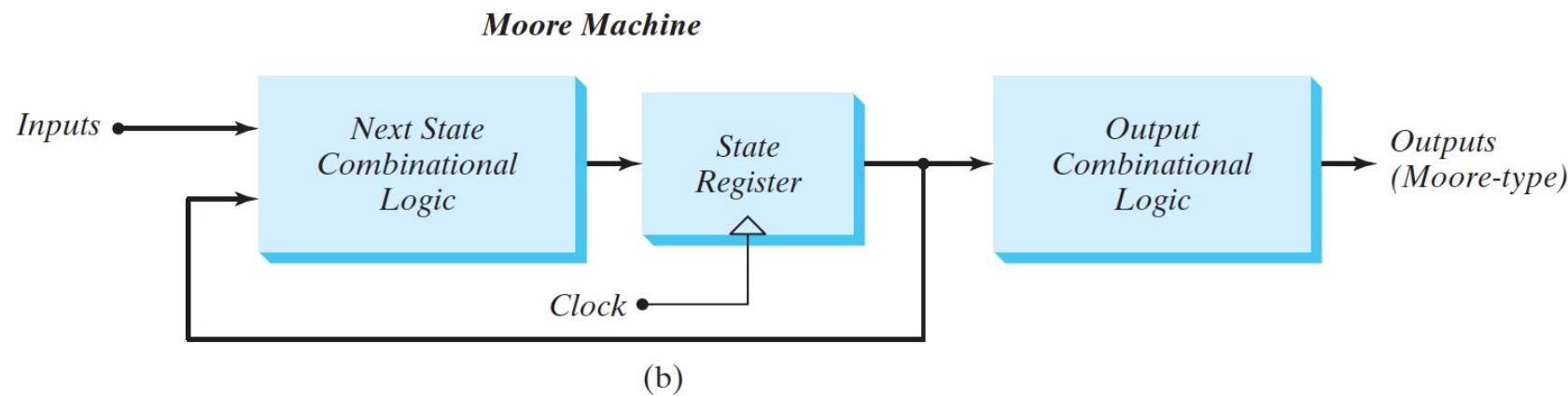
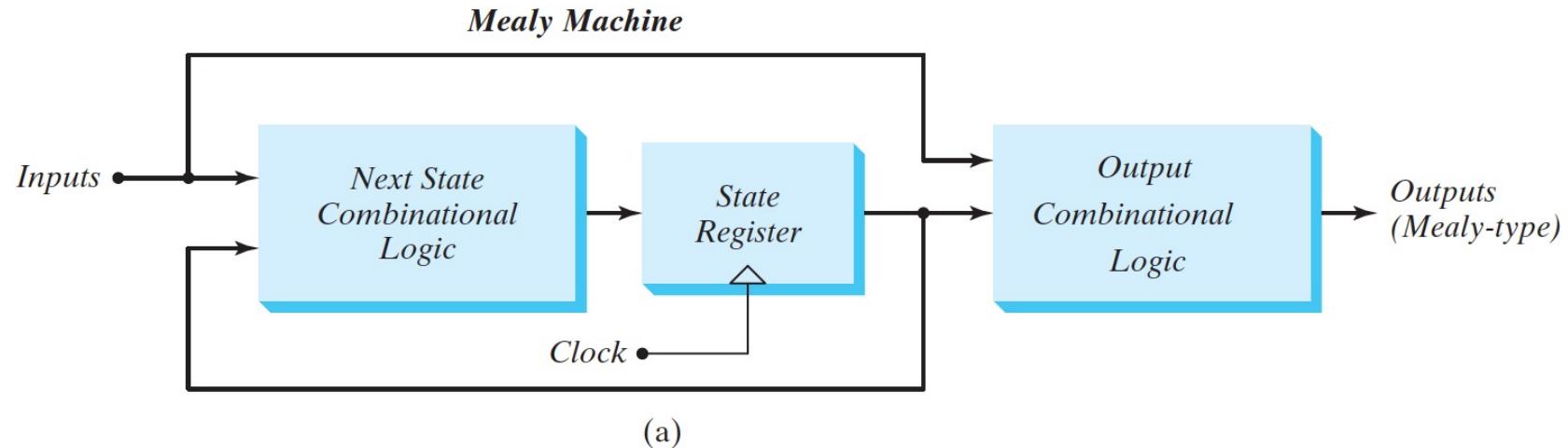


FIGURE 5.21
Block diagrams of Mealy and Moore state machines

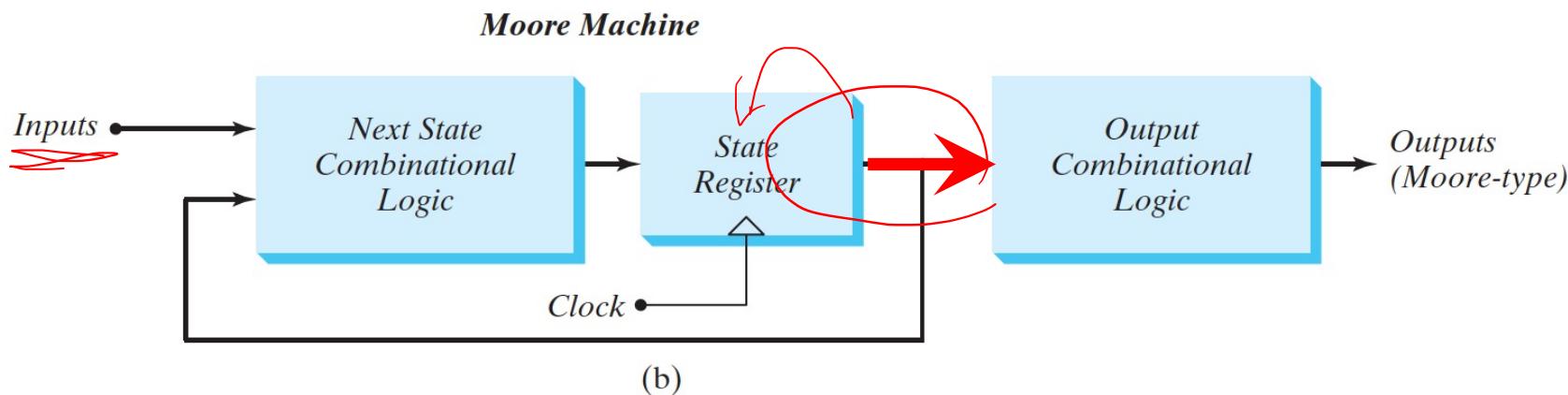
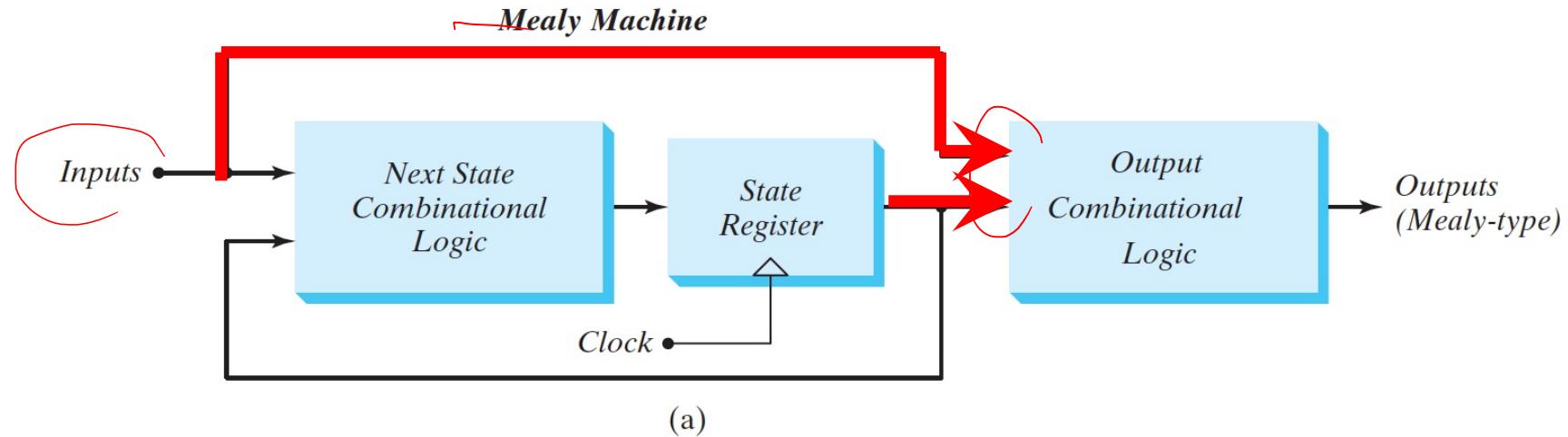
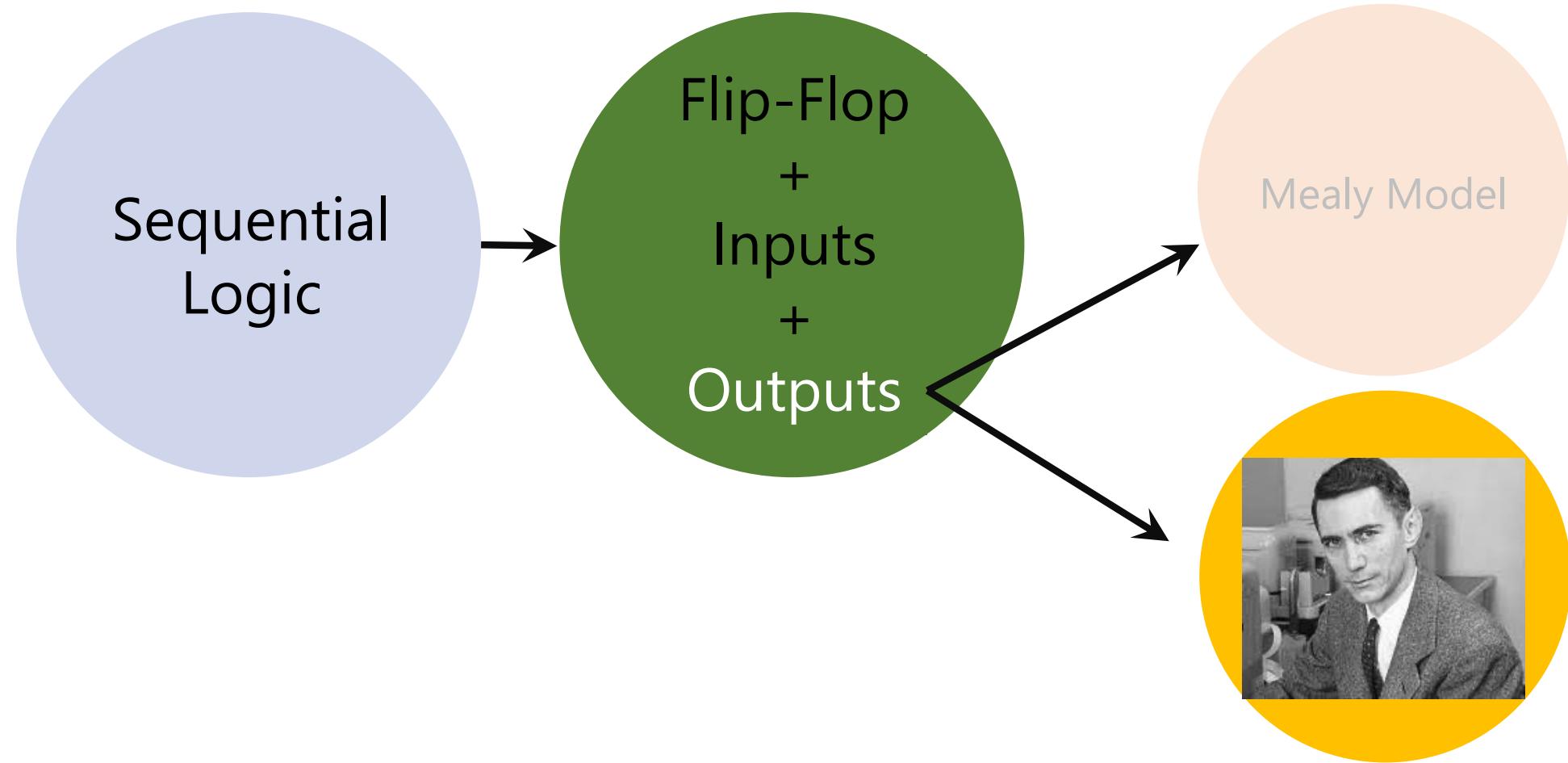


FIGURE 5.21
Block diagrams of Mealy and Moore state machines



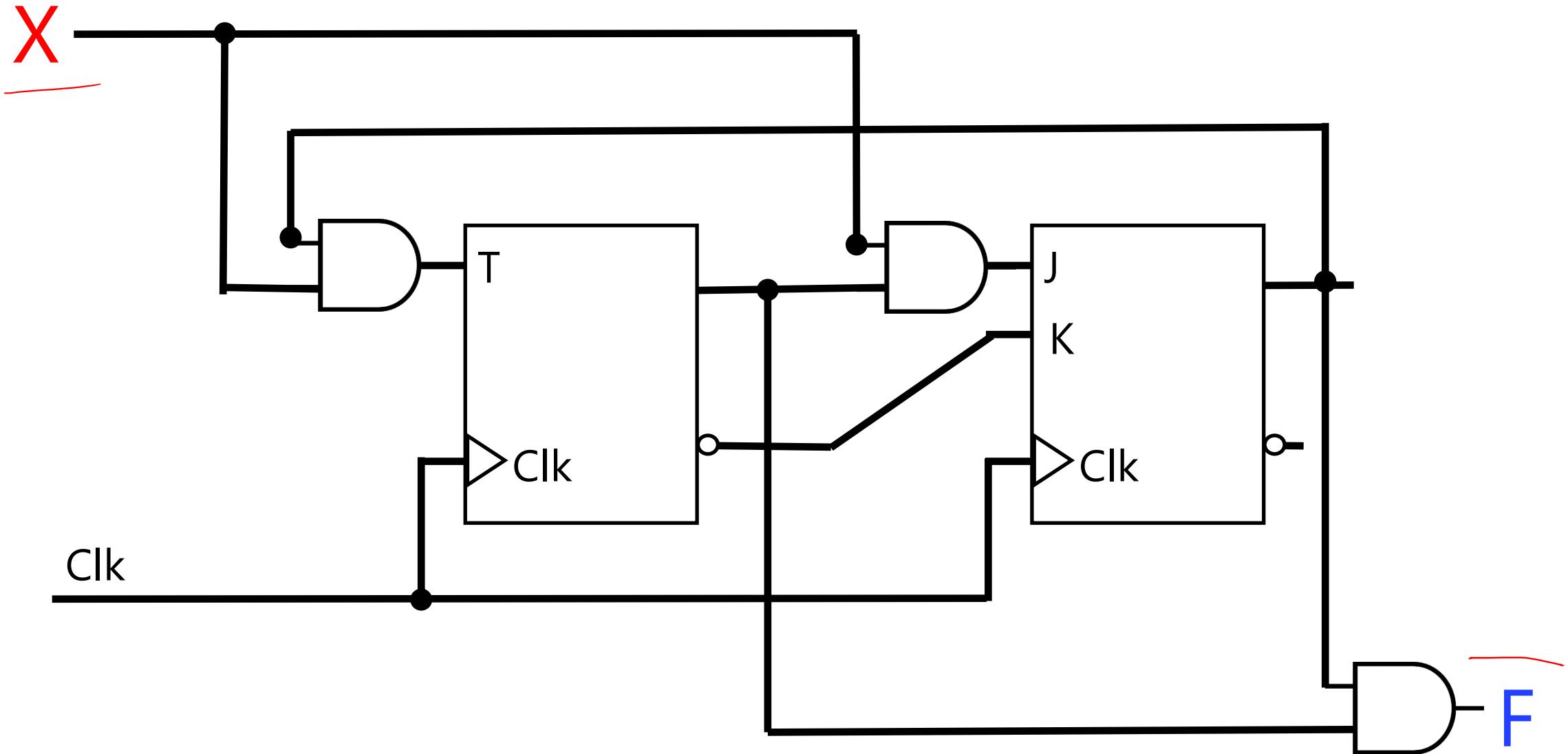
Analysis (Moore model in output) by an example

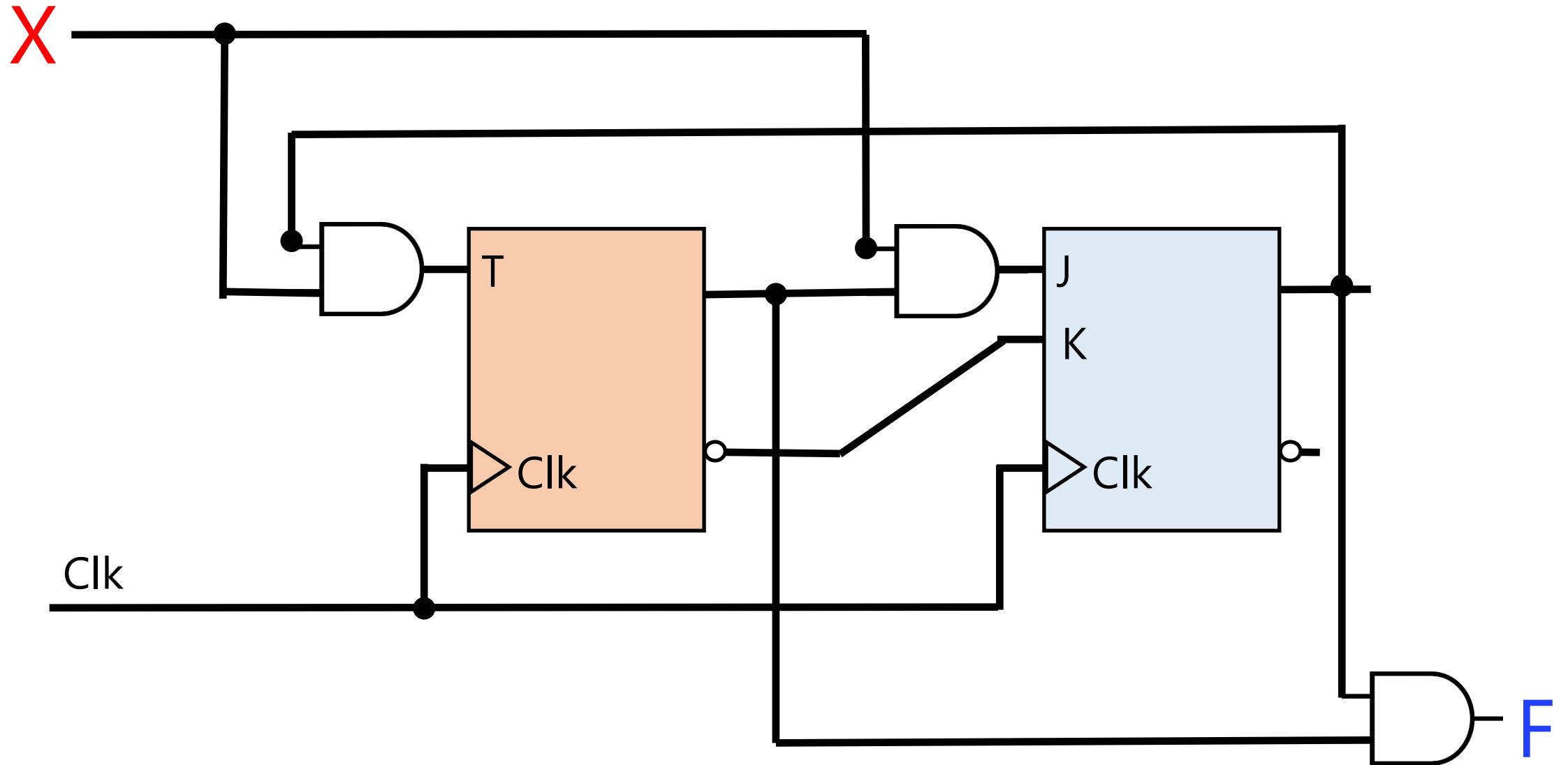
Analysis (Recap)

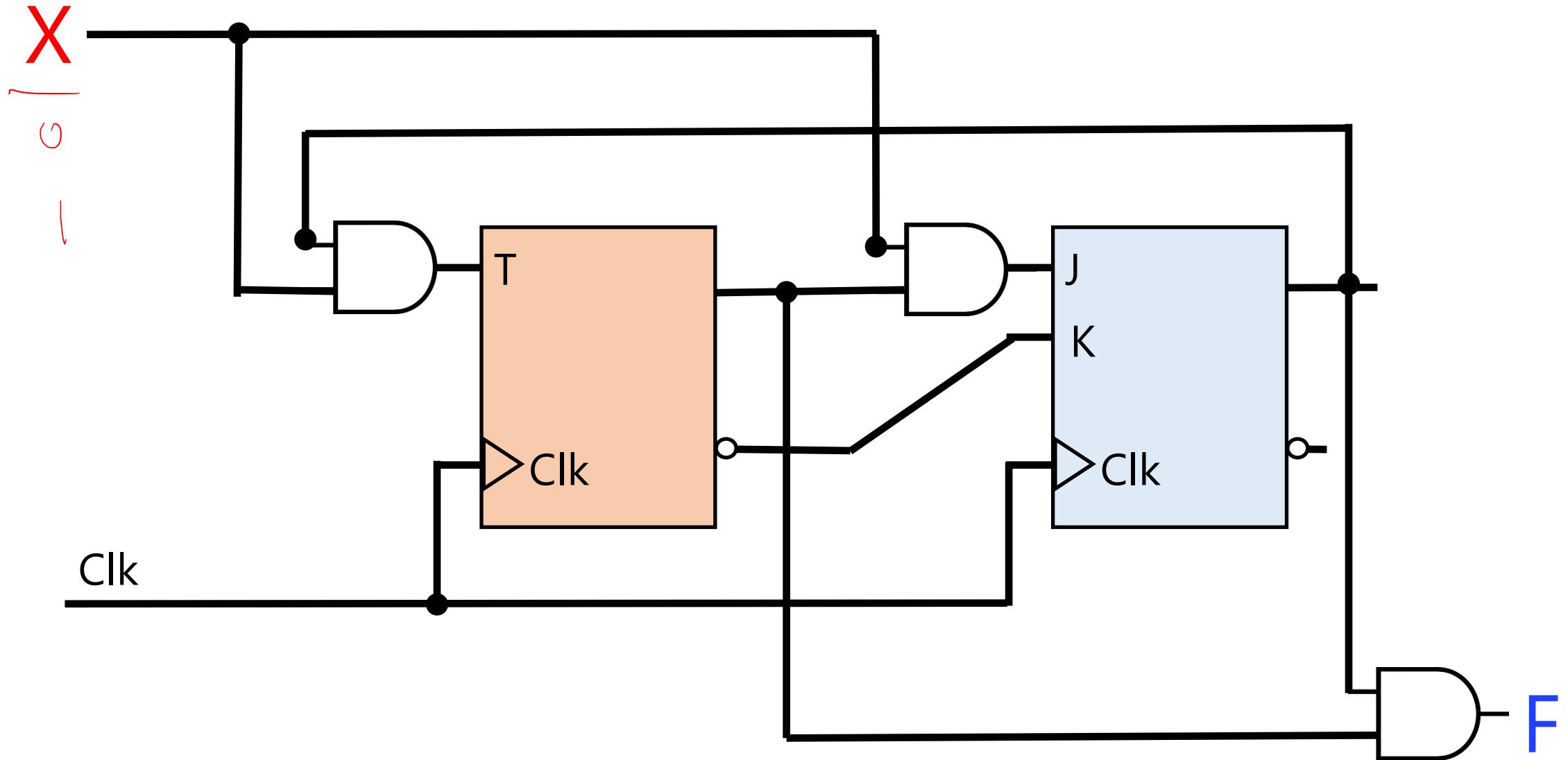
0. Is the circuit sequential or combinational? Any FF or feedback → Sequential
 1. What are the flip-flops? RS, D, T, JK, or mixed (e.g., 2 JK, 1 RS, ...)
 2. What are the state combinations? $2^{\#FF}$
 3. Form "State" table:
 - a) Columns: for each FF, two columns:
 - one for current state,
 - one for next state
 - b) Rows: for each state combination
 - In total: $2^{\#FF}$
 4. Fill the state table for next state columns based on:
 - a) the current state
 - b) the inputs to the FFs
 5. Form State Transition Diagram
 6. (Optional) Analyze paths and states in state transition diagram
-

Analysis (+ Input + Moore Model Output)

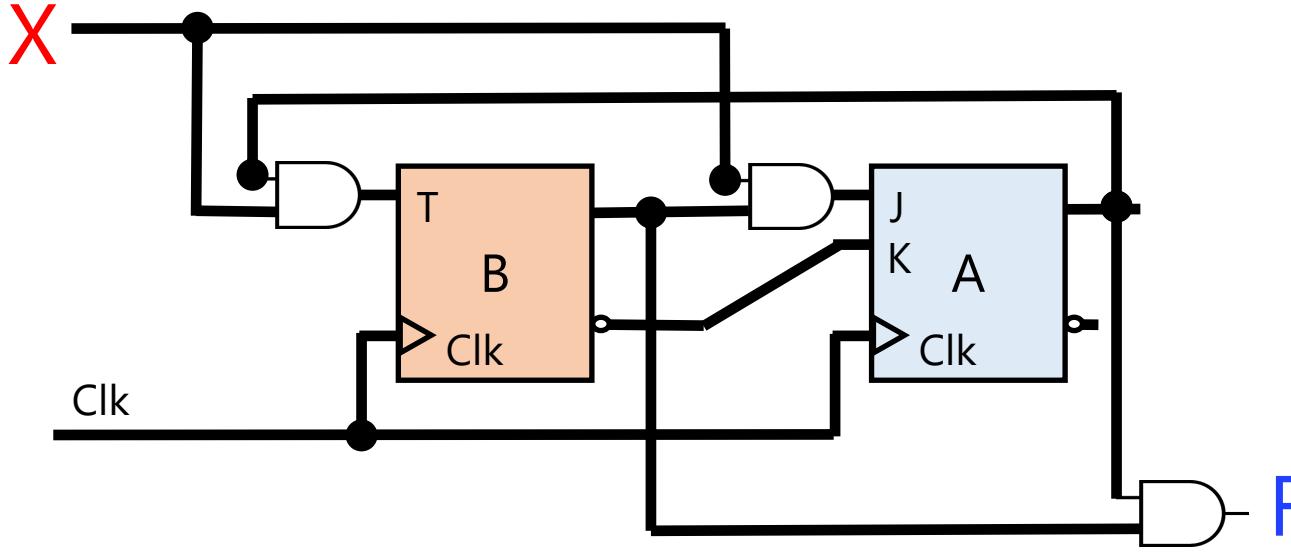
0. Is the circuit sequential or combinational? Any FF or feedback → Sequential
 1. What are the flip-flops? RS, D, T, JK, or mixed (e.g., 2 JK, 1 RS, ...)
 2. What are the state combinations? ~~$2^{\#FF}$~~ + #inputs
 3. Form "State" table:
 - a) Columns: for each FF, two columns:
 - o one for current state,
 - o one for next state
 - b) Rows: for each state combination
 - o In total: ~~$2^{\#FF}$~~
 4. Fill the state table for next state columns based on:
 - a) the current state
 - b) the inputs to the FFs
 5. Form State Transition Diagram
 6. (Optional) Analyze paths and states in state transition diagram
-



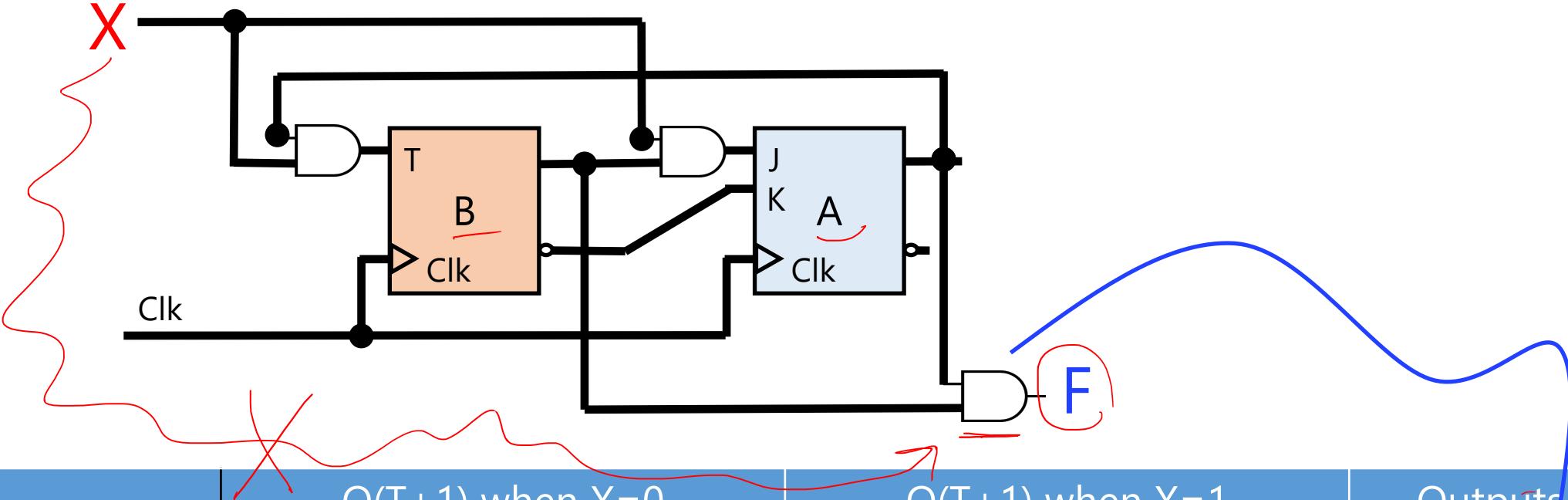




#FFs + #Inputs = 2+1 → $2^3 = \underline{8}$ combinations



Inputs	$Q(T)$	$Q(T+1)$	Outputs
X	B	A	F



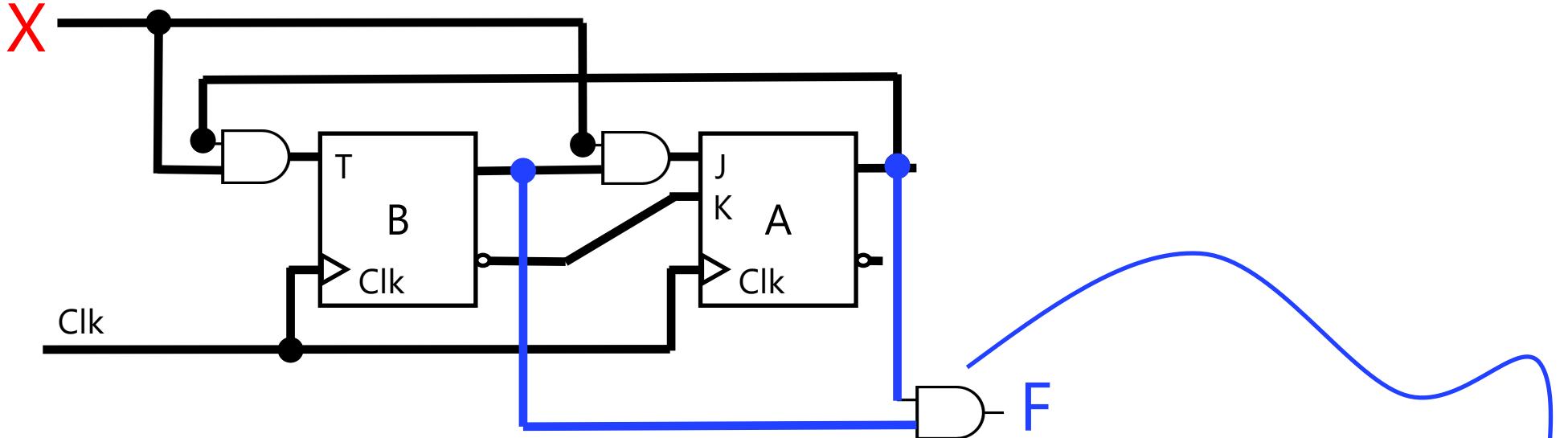
Q(T)		Q(T+1) when X=0		Q(T+1) when X=1		Outputs	
B	A	B	A	B	A	F	

Moore model:
does not depend on X

Alternative State Table

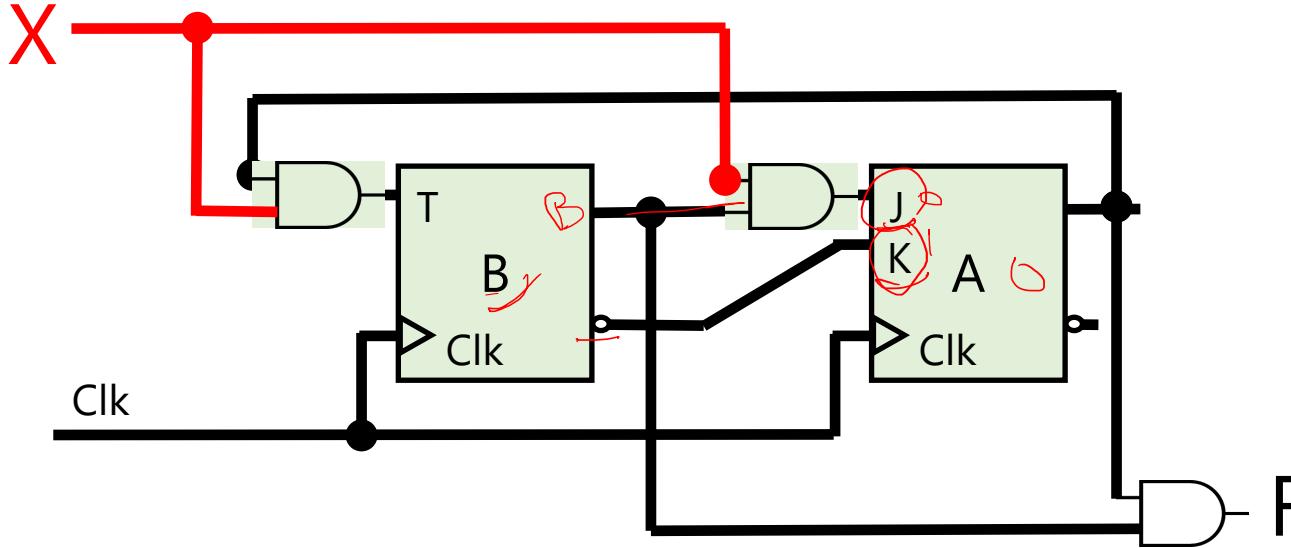
Analysis (Recap)

0. Is the circuit sequential or combinational? **Sequential**
1. What are the flip-flops? **T, JK**
2. What are the state combinations? $2^{\#FF} \times 2^{\#\text{inputs}} = 2^{\#FF+\#\text{inputs}} = 2^3 = 8$
3. Form "State" table:
 - a) Columns:
 - For each FF, two columns: one for current state, one for next state
 - For each input, one column
 - For each output, one column
 - b) Rows: See item 2
4. Fill the state table for
 - a) next state columns
 - b) the output value

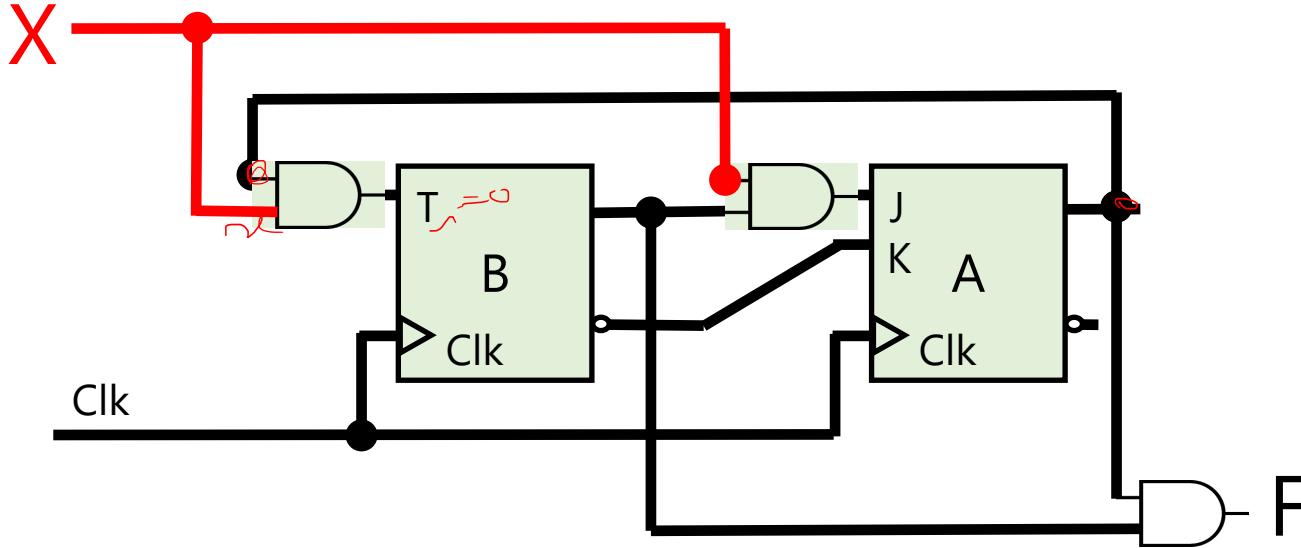


Inputs	$Q(T)$		$Q(T+1)$		Outputs
X	B	A	B	A	$F=BA$
0	0	0			0
0	0	1			0
0	1	0			0
0	1	1			1
1	0	0			0
1	0	1			0
1	1	0			0
1	1	1			1

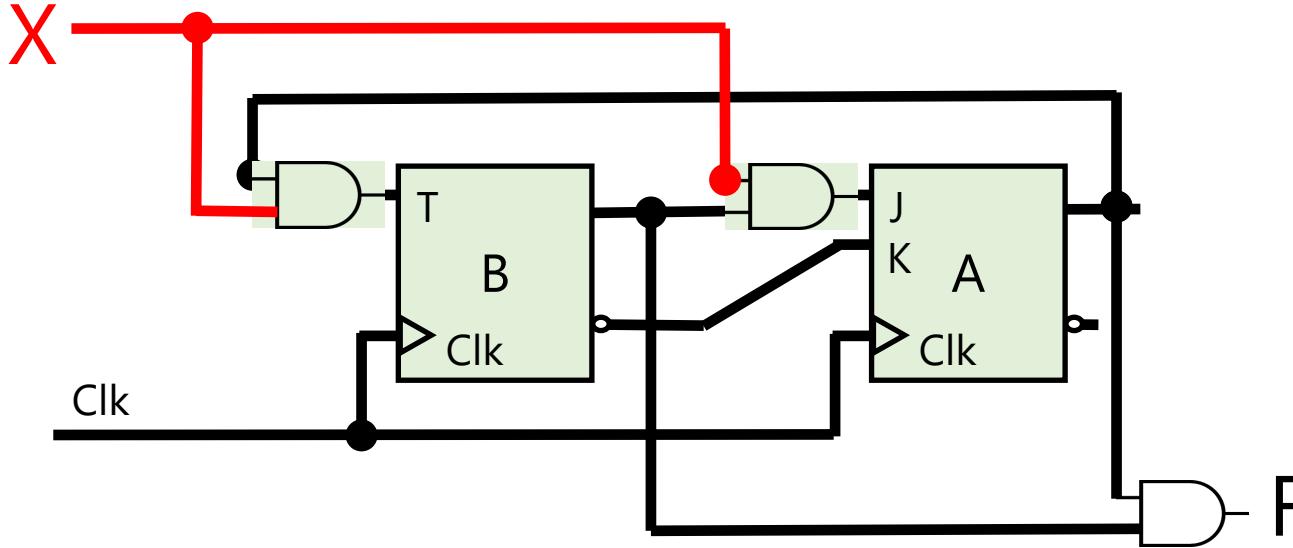
Moore Model
 Only depends on current state
 X is not involved!



Inputs	Q(T)		Q(T+1)		Outputs
X	B	A	B	A	$F=BA$
0	0	0	0	0	0
0	0	1	1	0	0
0	1	0	0	1	1
1	0	0	0	0	0



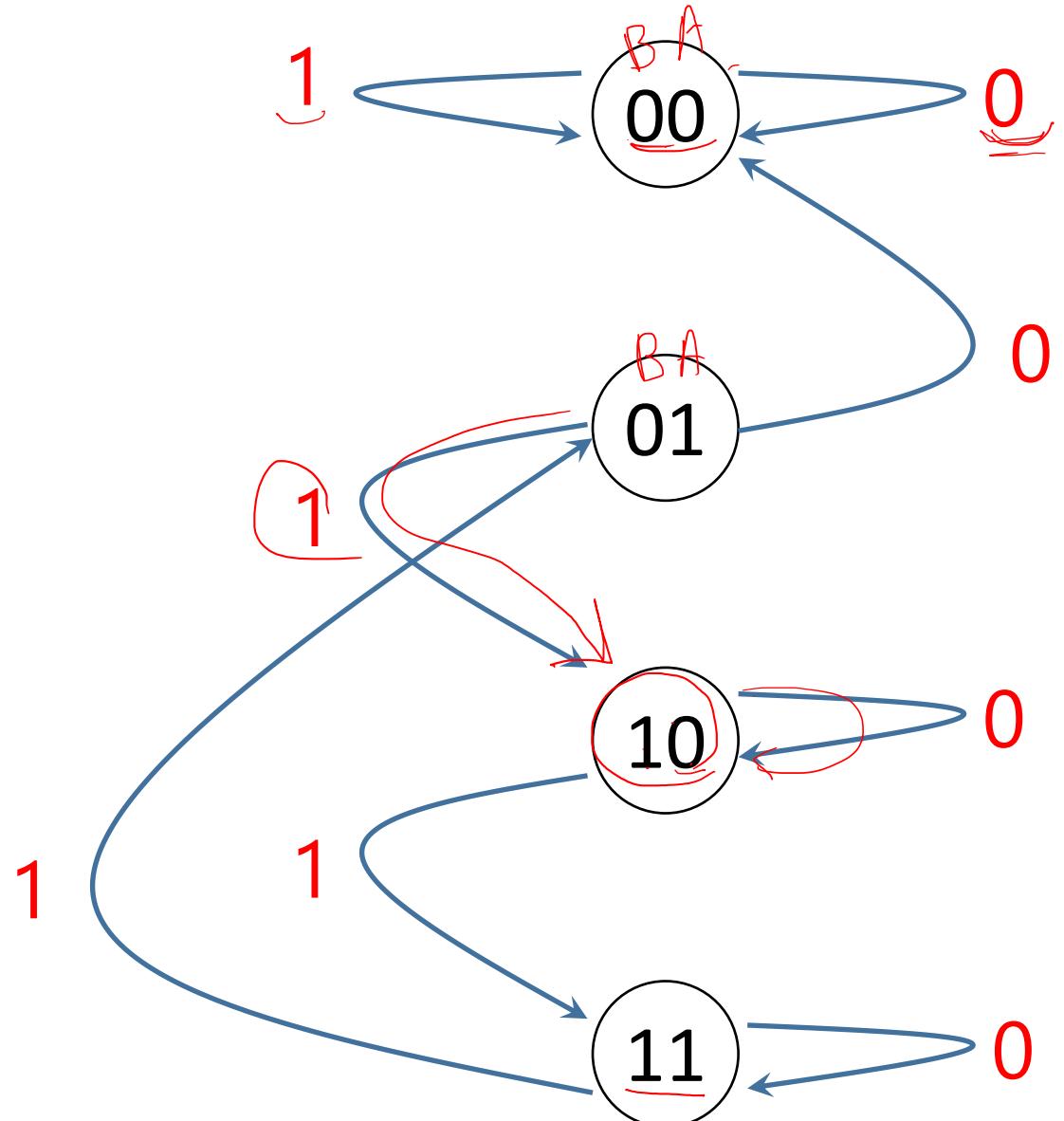
Inputs	Q(T)		Q(T+1)		Outputs	
X	B	A	B	A	F=BA	
0	0	0	$\underbrace{\text{B}=0}_{\text{---}}$ $T_B = \underline{X}A = \underline{0}0 = 0$	0	0	0
0	0	1			0	
0	1	0			0	
0	1	1			1	
1	0	0			0	
1	0	1			0	



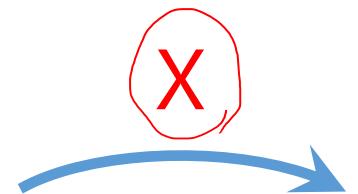
Inputs	Q(T)		Q(T+1)		Outputs
X	B	A	B	A	F=BA
0	0	0	0	0	0
0	0	1	0	0	0
0	1	0	1	0	0
0	1	1	1	1	1
1	0	0	0	0	0
1	0	1	1	0	0
1	1	0	1	1	0
1	1	1	0	1	1

Analysis (Recap)

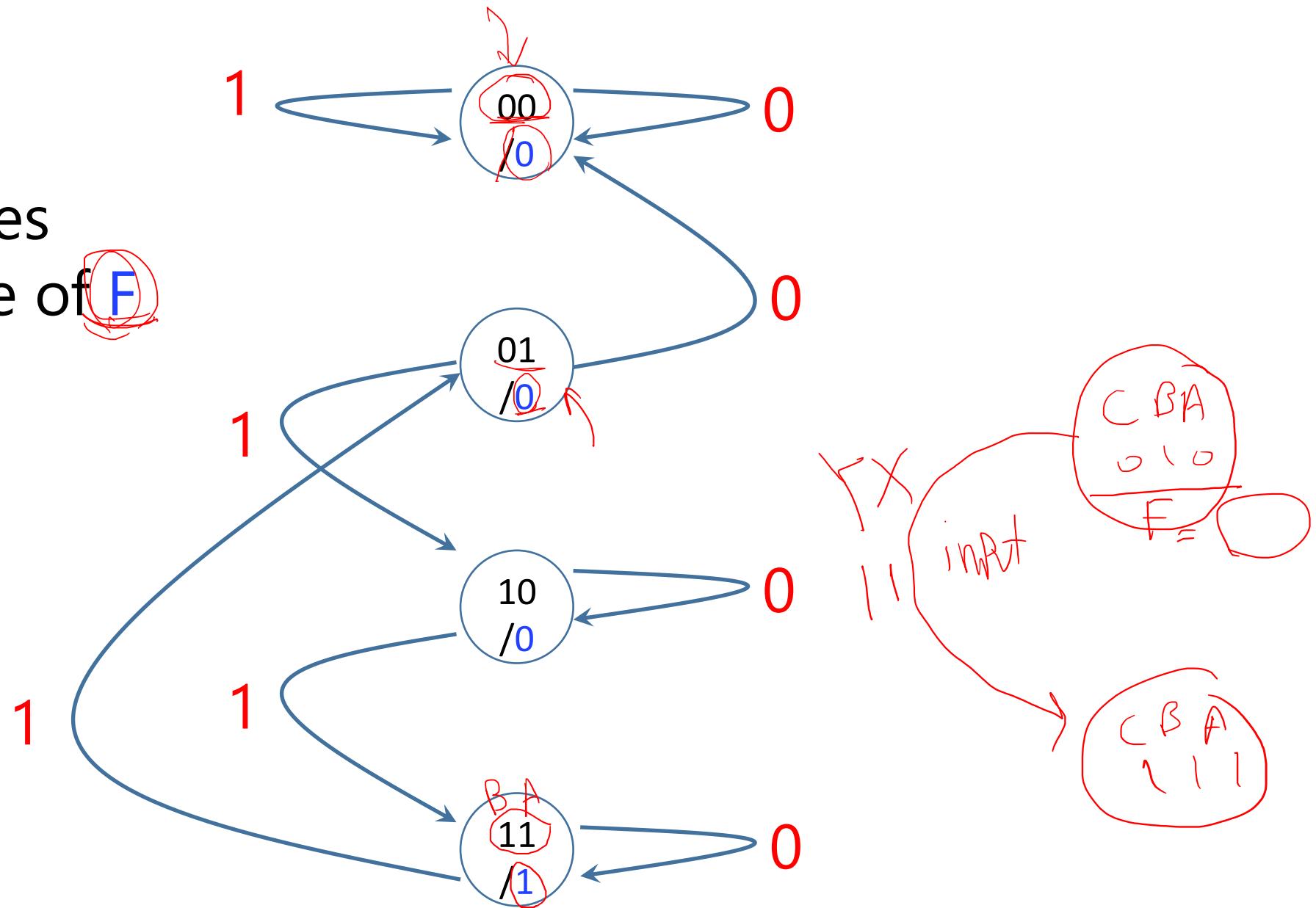
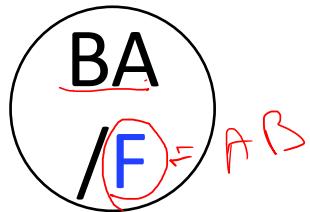
0. Is the circuit sequential or combinational? Sequential
1. What are the flip-flops? T, JK
2. What are the state combinations? $2^{\#FF} \times 2^{\#\text{inputs}} = 2^{\#FF+\#\text{inputs}} = 2^3 = 8$
3. Form "State" table:
 - a) Columns:
 - o For each FF, two columns: one for current state, one for next state
 - o For each input, one column
 - o For each output, one column
 - b) Rows: See item 2
4. Fill the state table for
 - a) next state columns
 - b) the output value
5. Form state (transition) diagram
 - a) nodes for states, directed edges for transitions between states
 - b) labels for edges by the value of input
 - c) labels for nodes by the value of output



Labels on edges
based on value of X



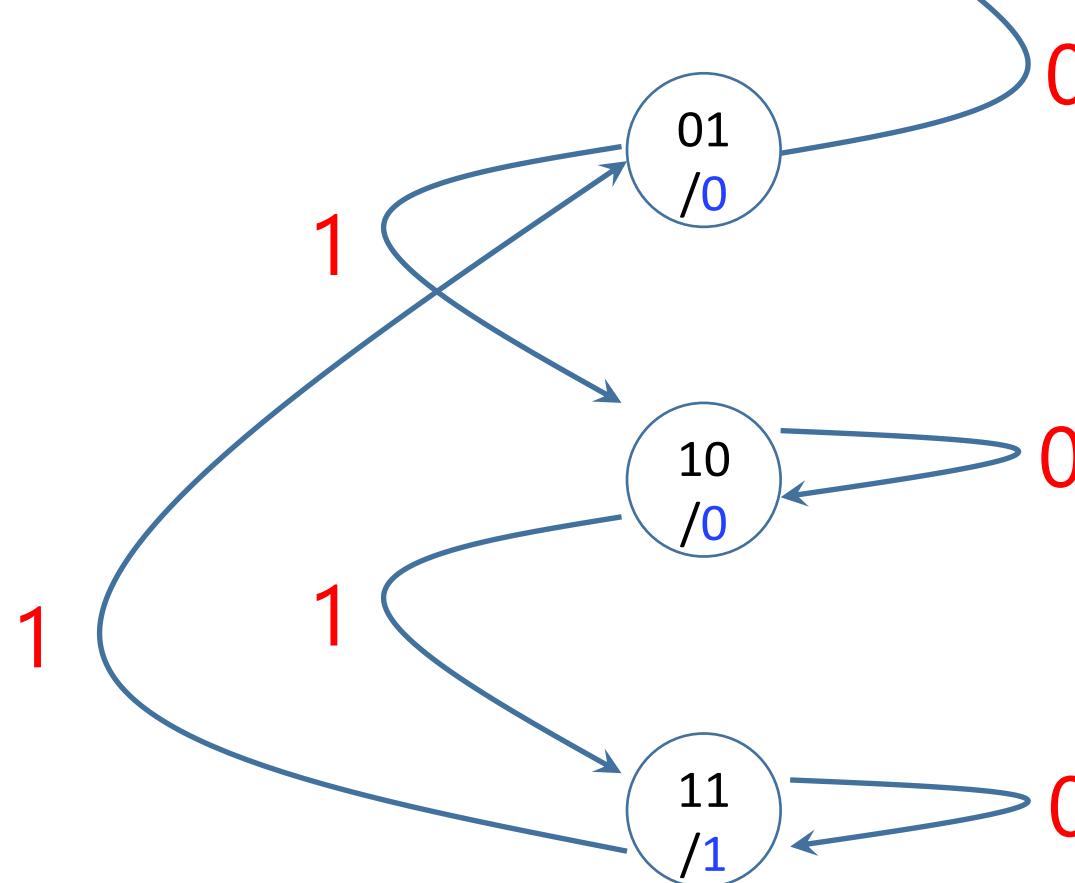
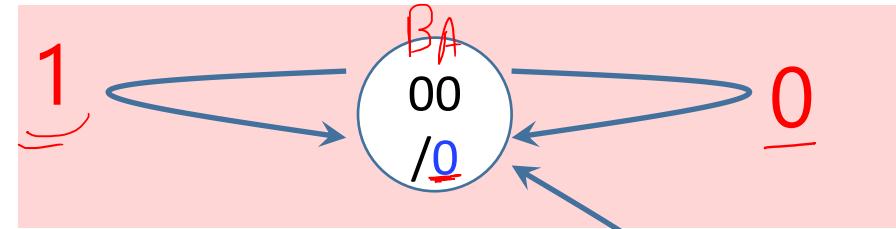
Labels on nodes
based on value of F



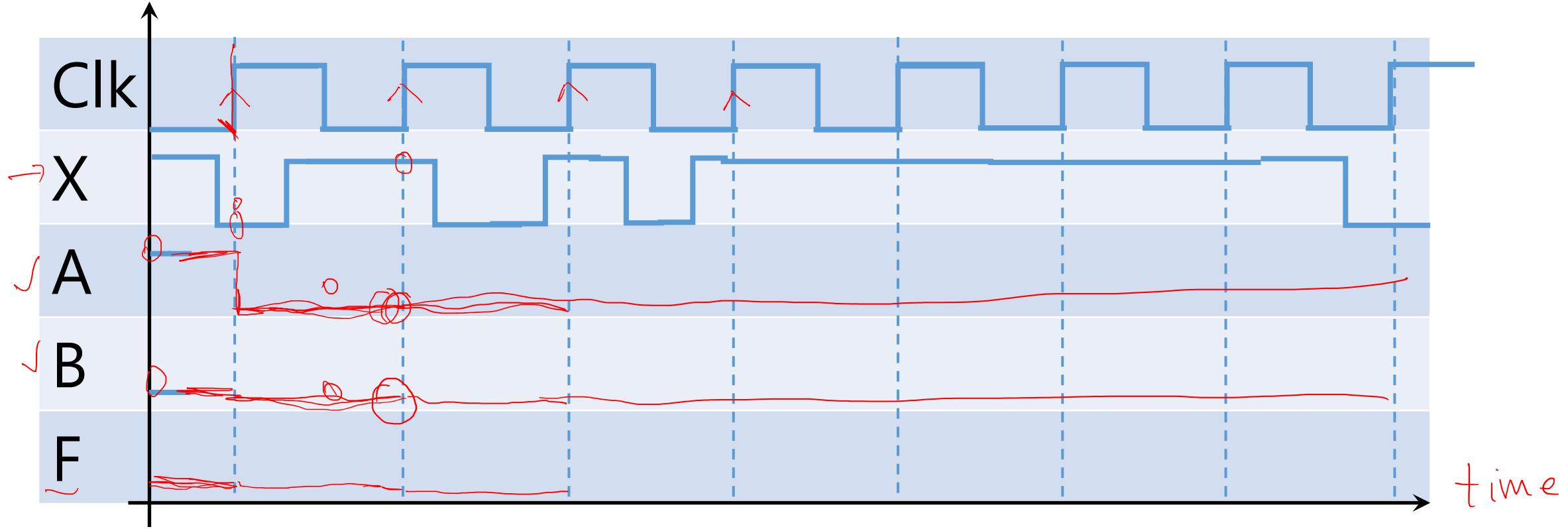
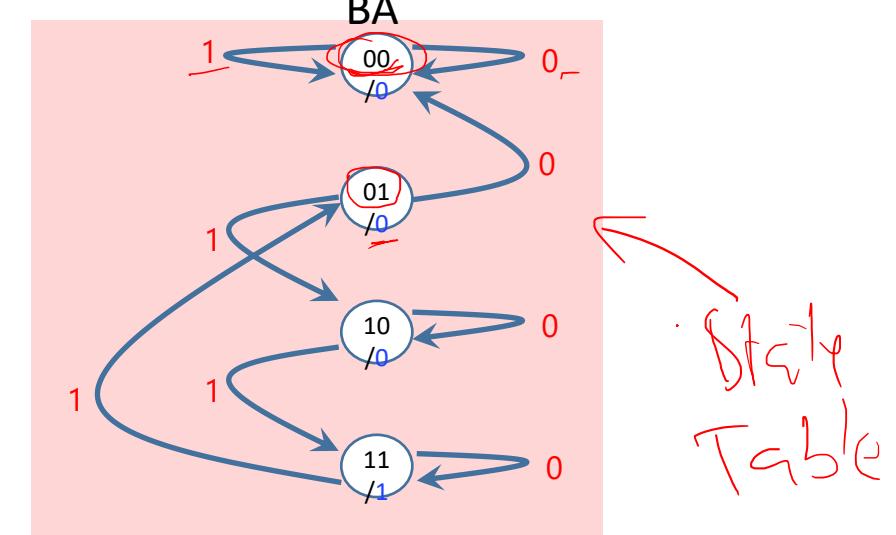
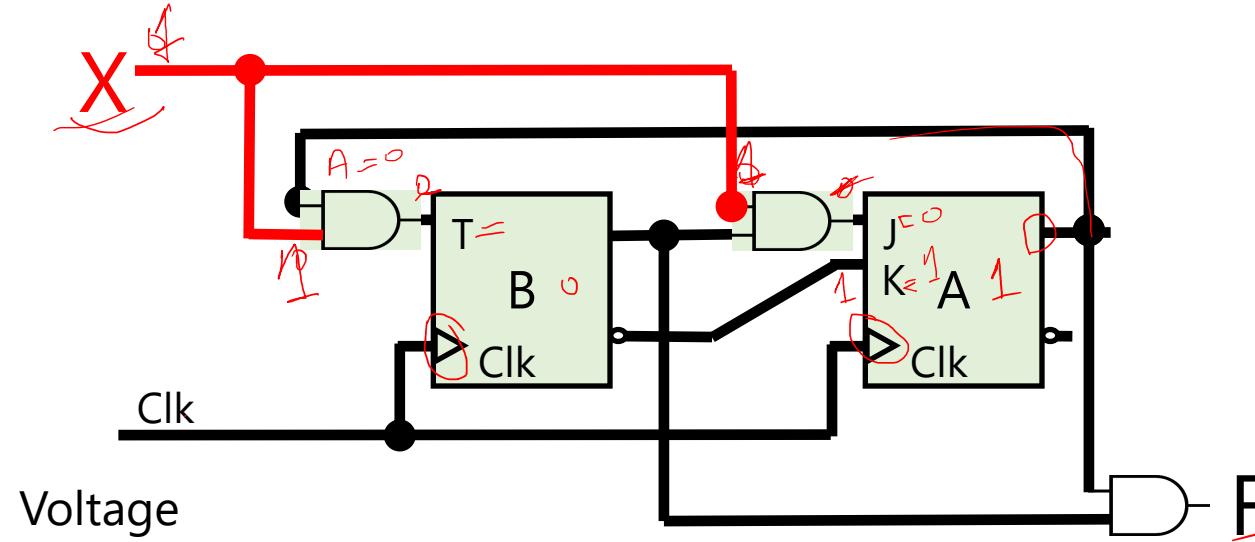
Analysis

6) (*Optional*) Path on State Transitions

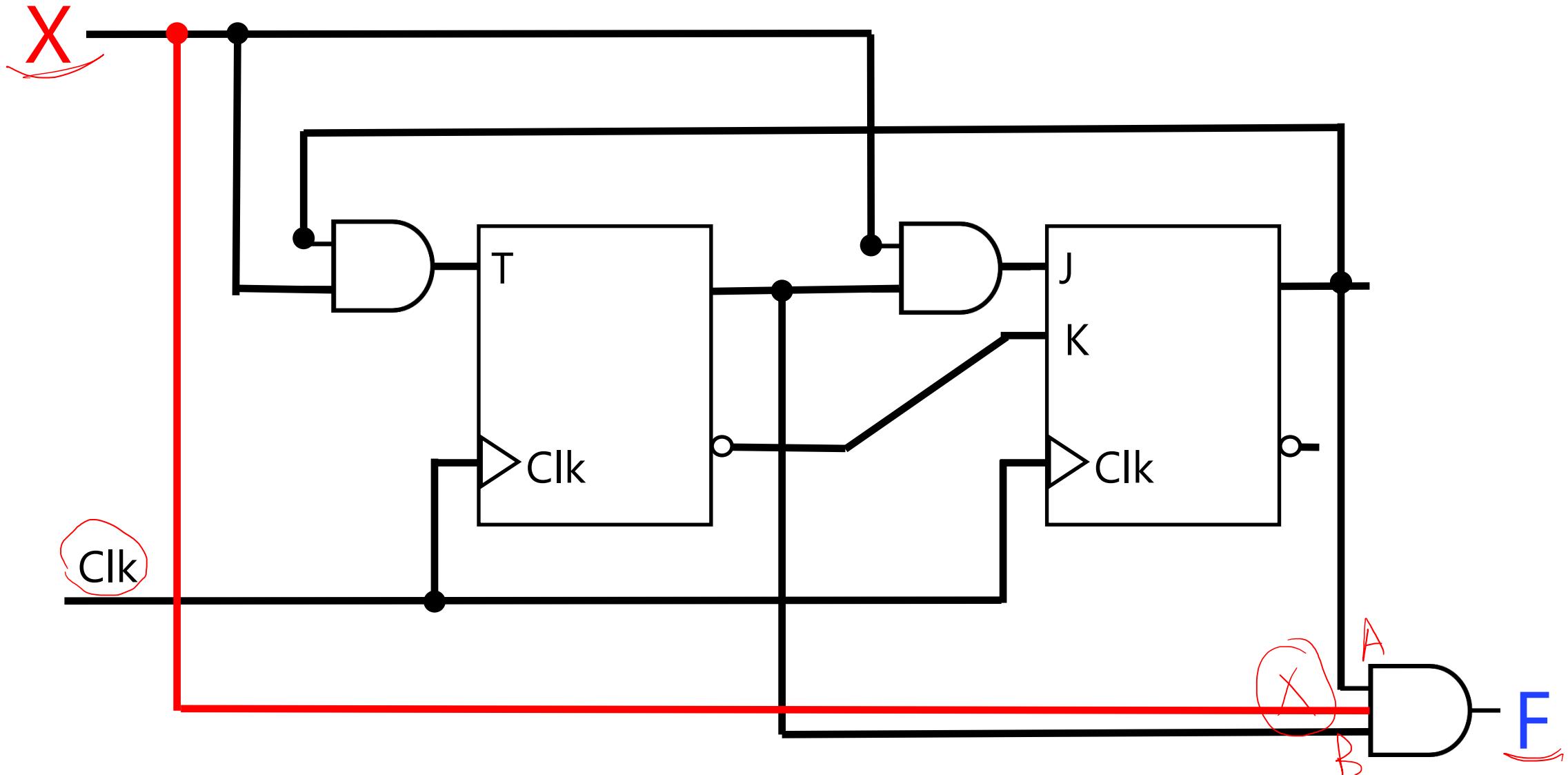
Life lock!



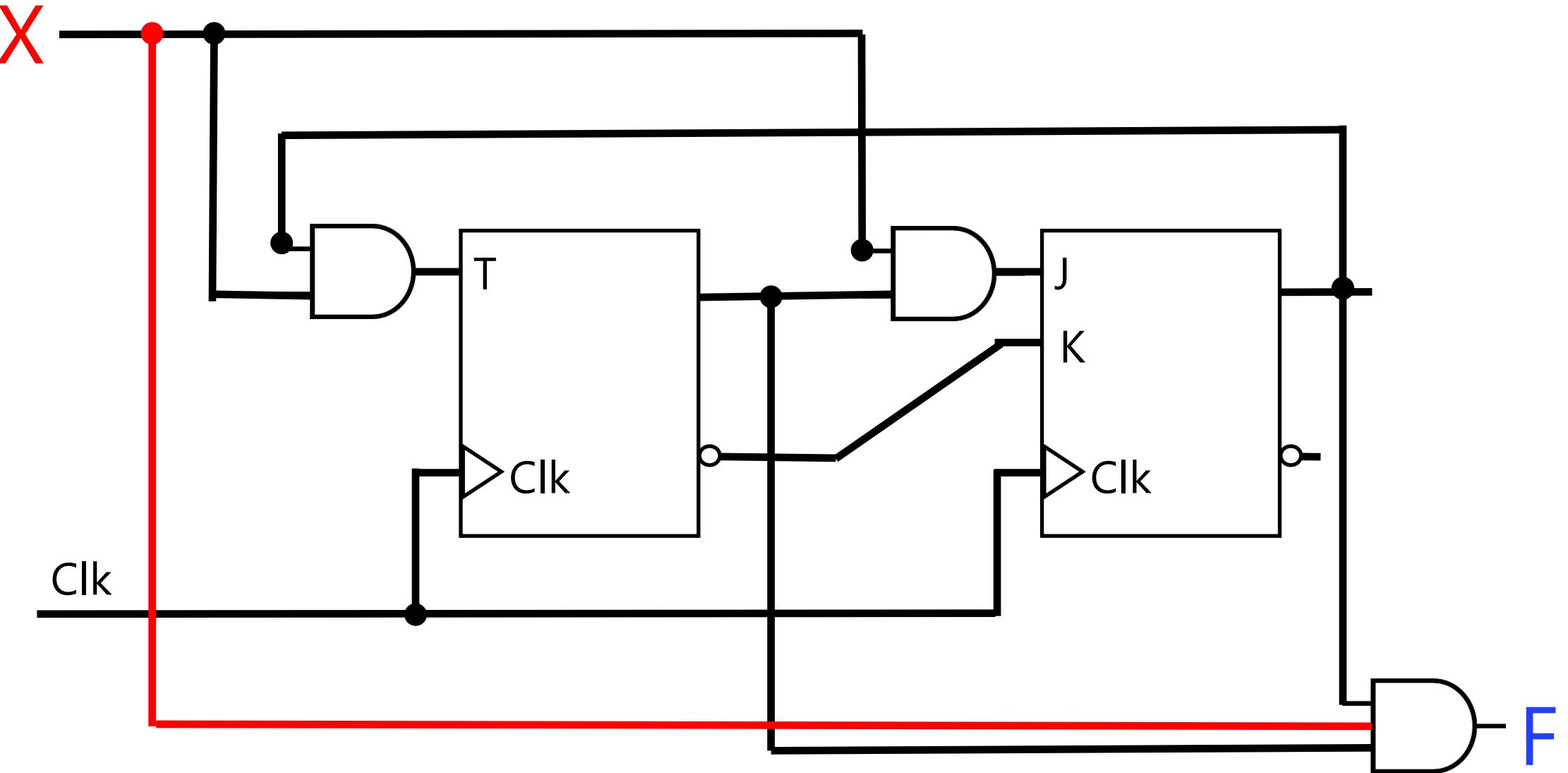
Synchronization



Asynchronized

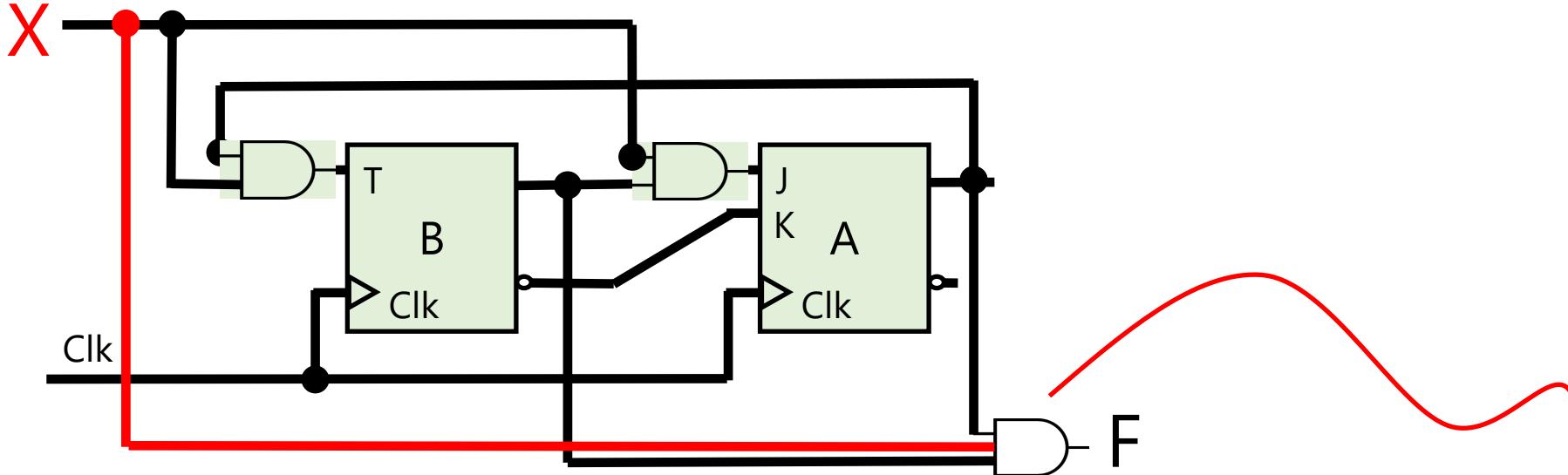


Mealy Model
F depends on X



Mealy Model

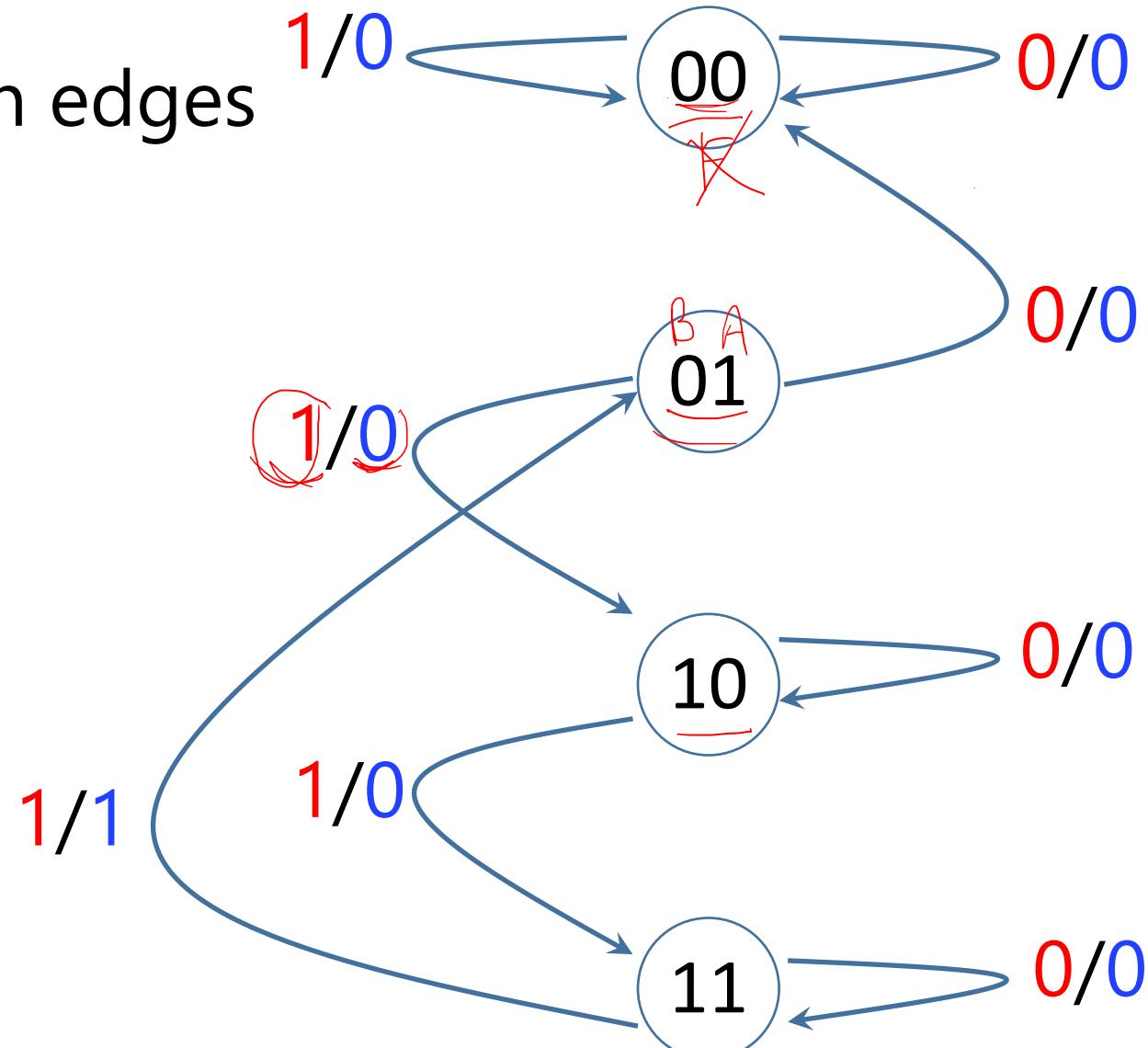
F depends on X, so, F can change out of Clk synchronization!

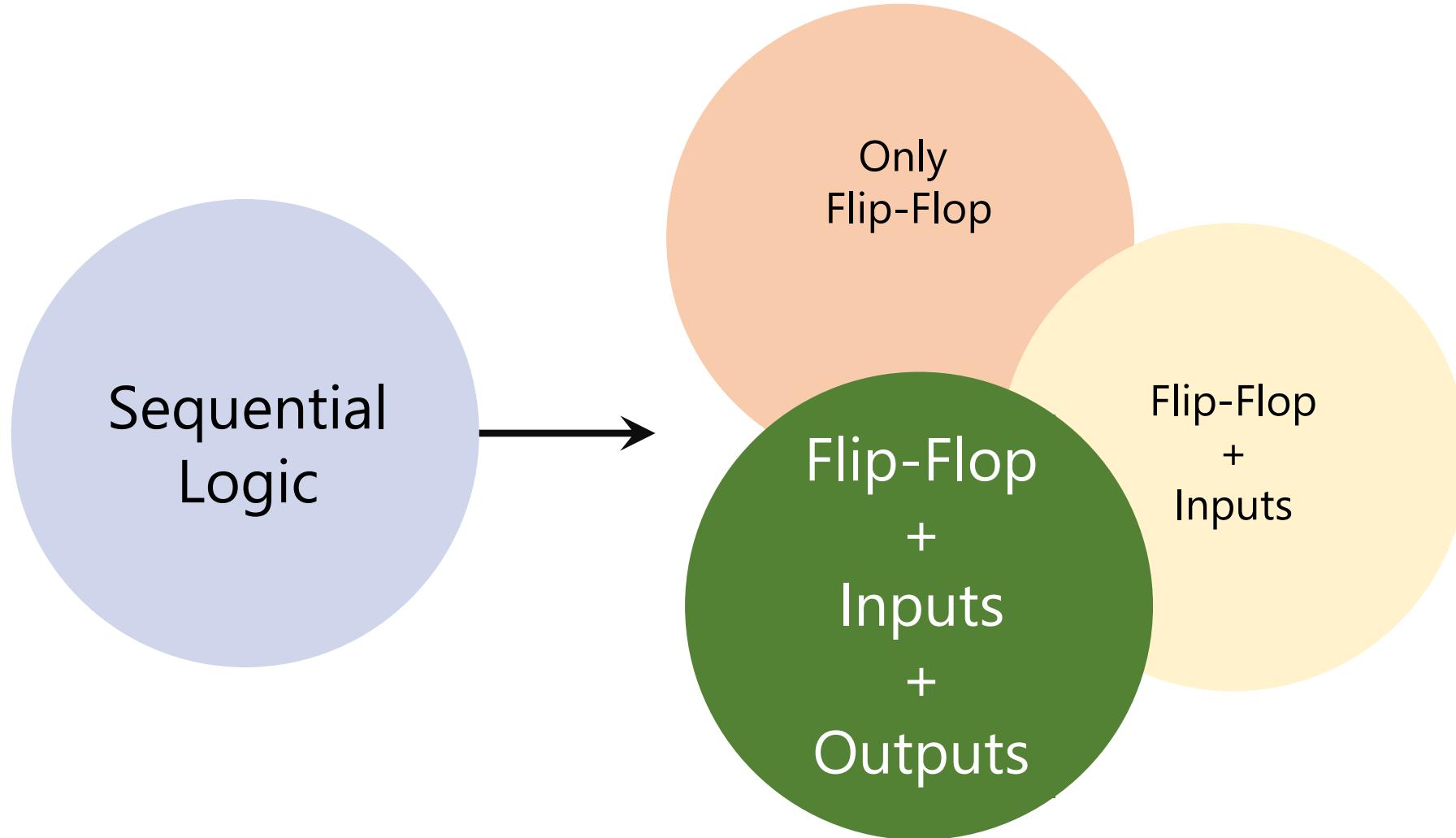


Inputs	Q(T)		Q(T+1)		Outputs
X	B	A	B	A	$F = X \cdot B \cdot A$
0	0	0	0	0	0
0	0	1	1	0	0
0	1	0	1	0	0
0	1	1	1	1	0
1	0	0	0	0	0
1	0	1	1	0	0
1	1	0	1	1	0
1	1	1	0	1	1

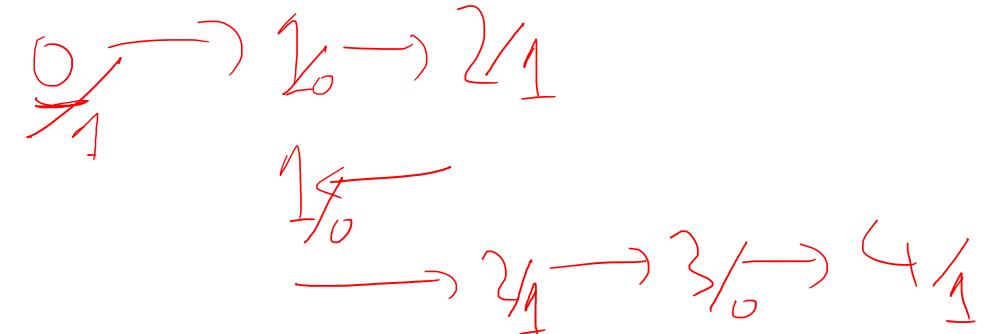
Labels go on edges
for F:

(X/F)





Design by an example



Counter Up-Down

$2 \leftarrow 3 \leftarrow$

Switch to count up from i to $i+1$

Switch to count down from i to $i-1$

Turn on the light if the current number is even

Design (Recap)

0. Do we need combinational logic or sequential logic? Do we need memory?

Counter up/down

$0 \rightarrow 1 \rightarrow 2 \rightarrow 3 \rightarrow \dots \rightarrow N-1 \rightarrow N$

$N \rightarrow N-1 \rightarrow \dots \rightarrow 3 \rightarrow 2 \rightarrow 1 \rightarrow 0$

Design (Recap)

0. Do we need combinational logic or sequential logic? Do we need memory?
1. How many storage (flip-flops)? #FF

Counter up/down

$$N = \underline{7}$$

000 → 001 → 010 → 011 → 100 → 101 → 110 → 111

000 ← 001 ← 010 ← 011 ← 100 ← 101 ← 110 ← 111

For each intermediate state, we need 3 bits → 3 flip-flops

Design (Recap)

0. Do we need combinational logic or sequential logic? Do we need memory?
1. How many storage (flip-flops)? #FF
2. How many input and output?

Counter up/down

$N=7$

$000 \rightarrow 001 \rightarrow 010 \rightarrow 011 \rightarrow 100 \rightarrow 101 \rightarrow 110 \rightarrow 111$

$000 \leftarrow 001 \leftarrow 010 \leftarrow 011 \leftarrow 100 \leftarrow 101 \leftarrow 110 \leftarrow 111$

We need to switch between up and down \rightarrow 1 binary variable

$X=0$ Count Up } ~~Count~~

$X=1$ Count Down }

Counter up/down N=7

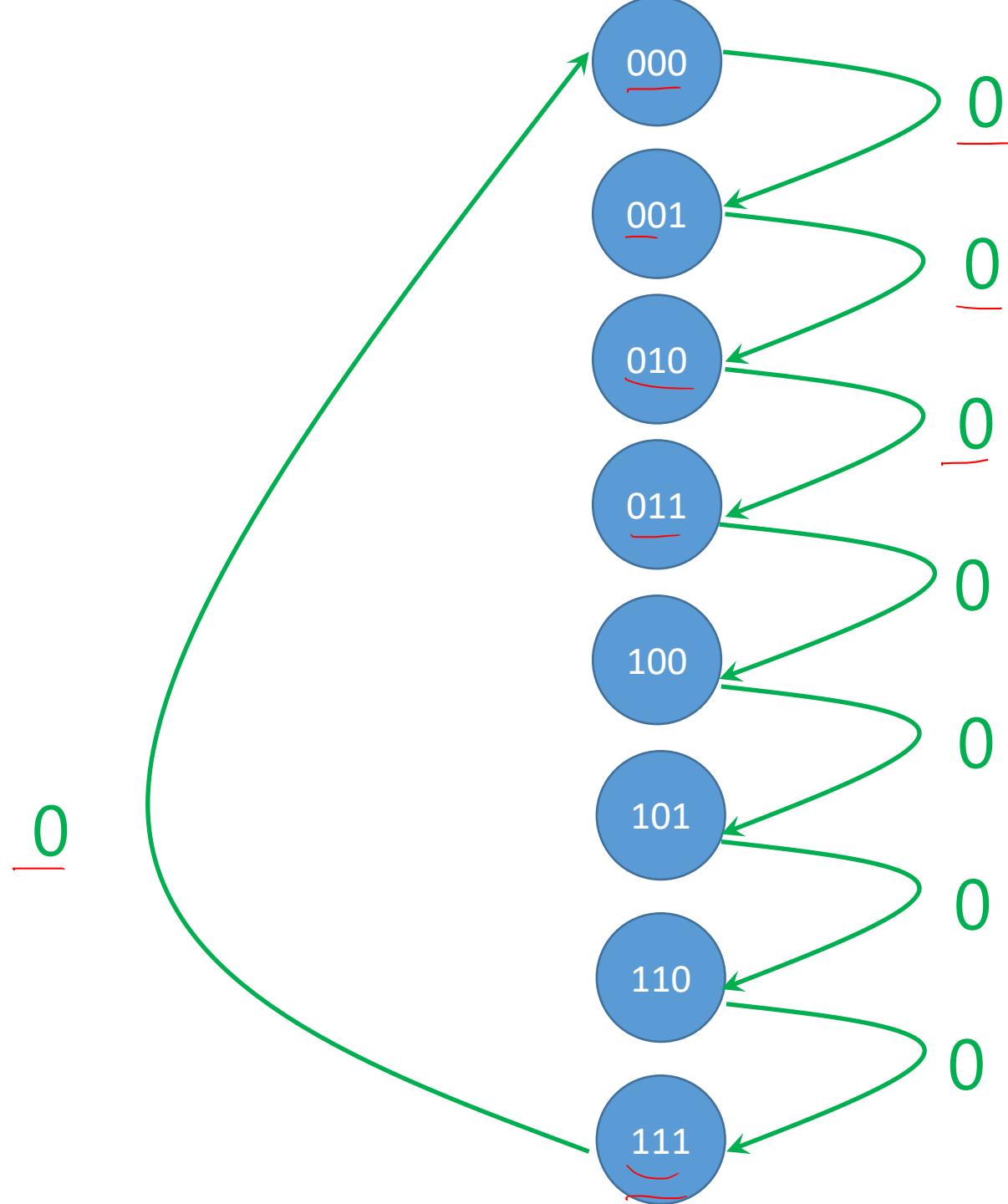
X=0: 000 → 001 → 010 → 011 → 100 → 101 → 110 → 111

X=1: 111 → 110 → 101 → 100 → 011 → 010 → 001 → 000

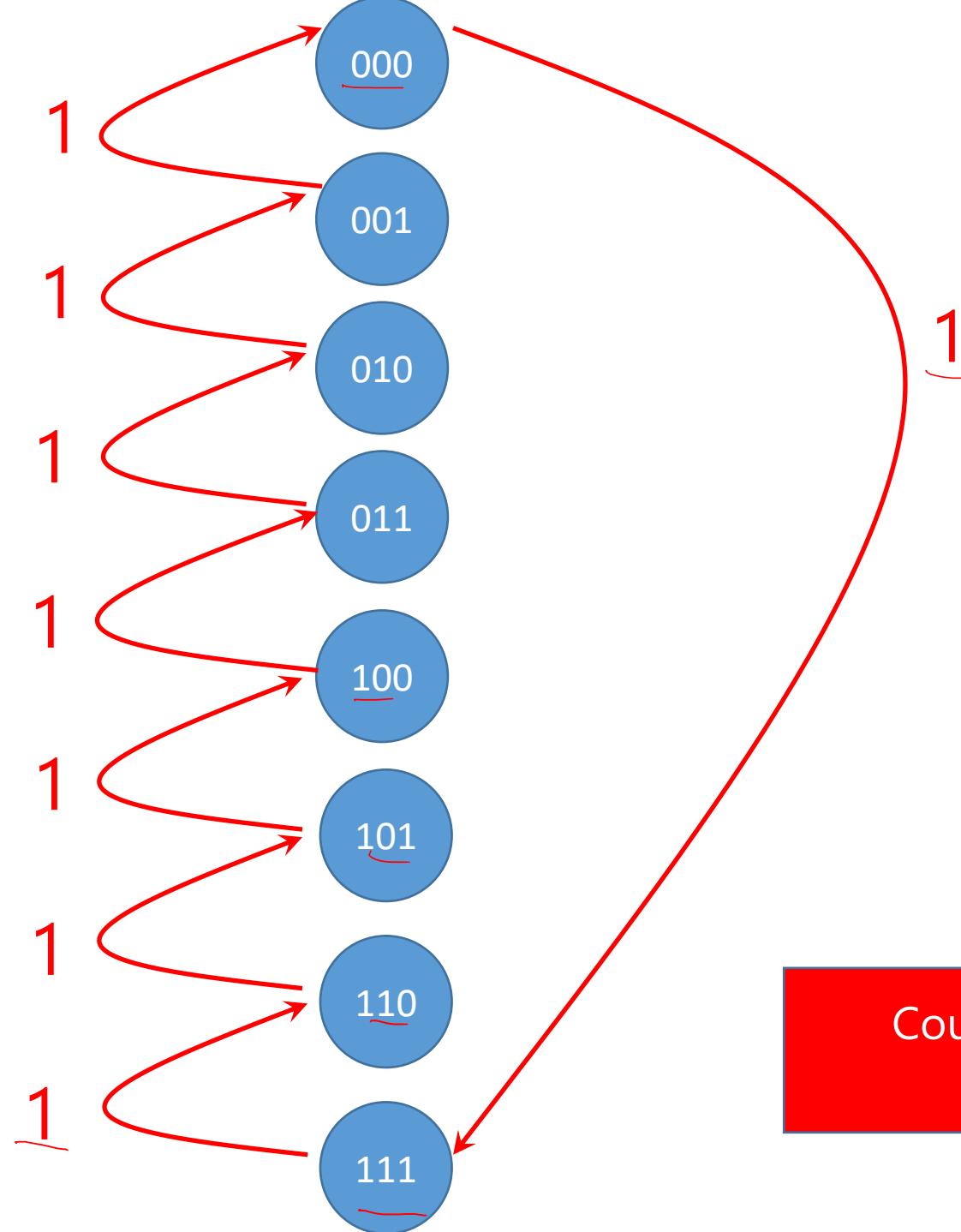
We need to turn on the light when the number is even
→ 1 binary variable → F = 0

Design (Recap)

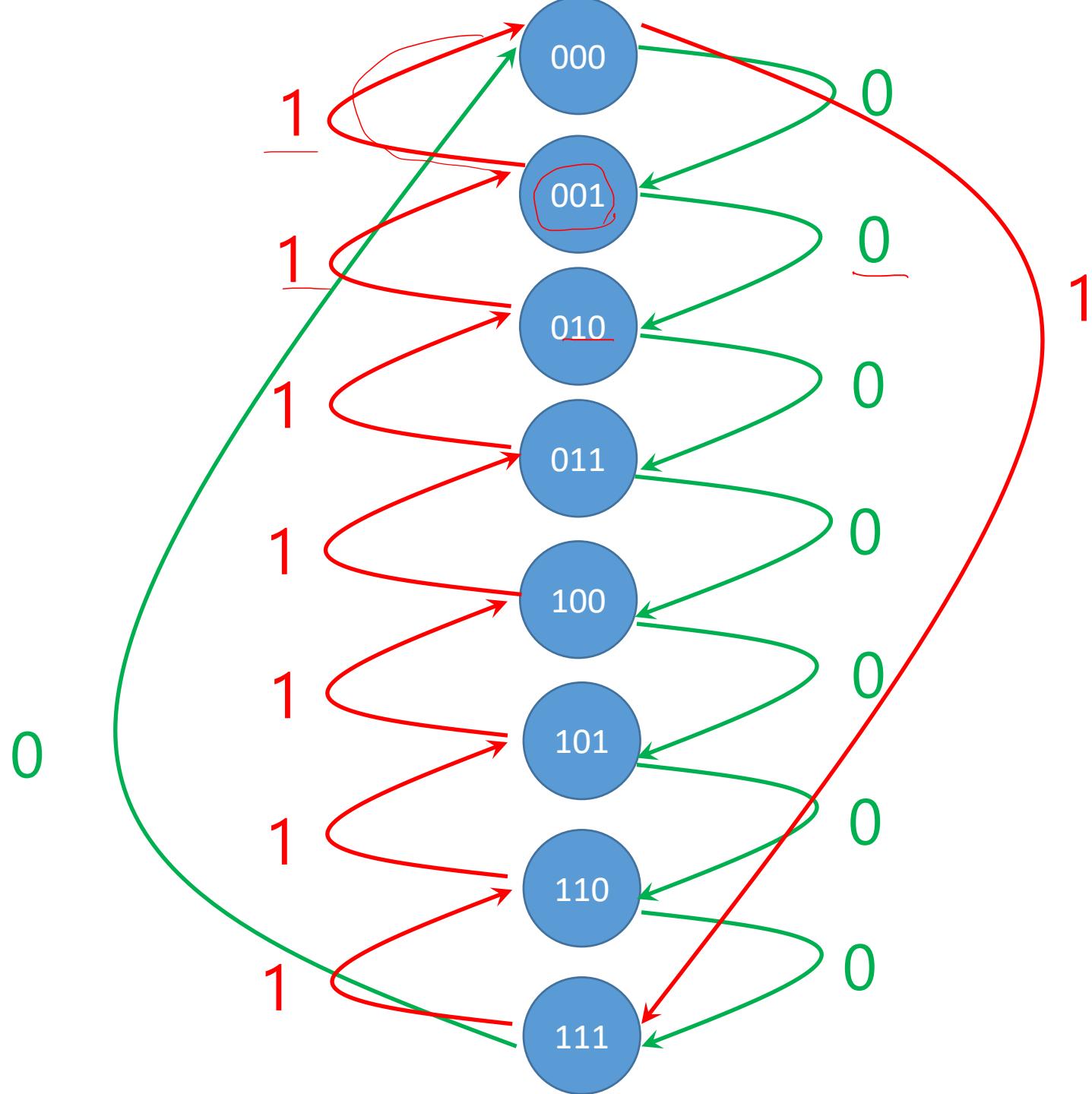
0. Do we need combinational logic or sequential logic? Do we need memory?
1. How many storage (flip-flops)? #FF
2. How many input and output?
3. Form the state (transition) diagram



Count Up
X=0

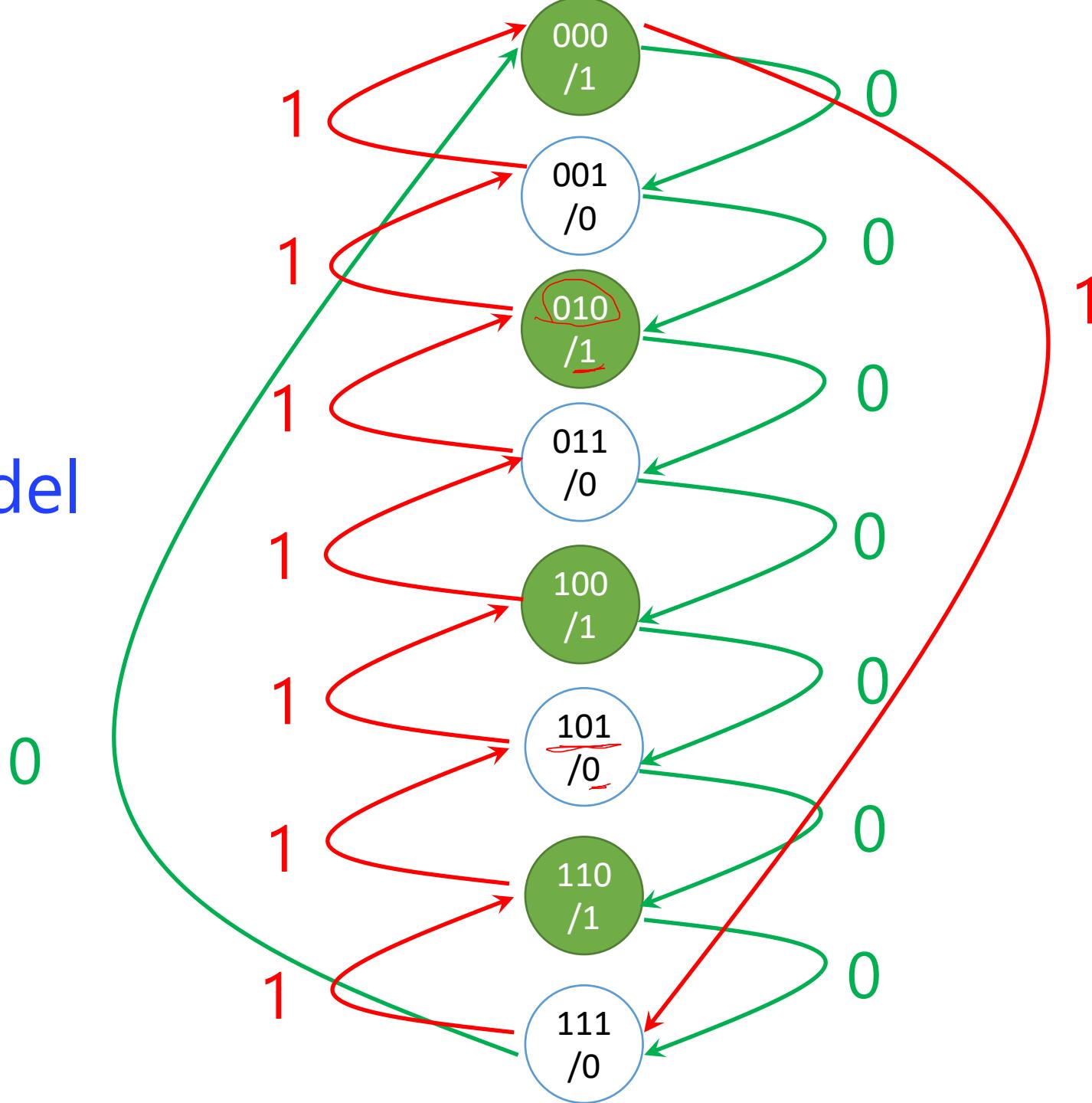


Count Down
X=1



$F=1$: even
 $F=0$: else

Moore model



Design (Recap)

0. Do we need combinational logic or sequential logic? Do we need memory?
1. How many storage (flip-flops)? #FF
2. How many input and output?
3. Form the state (transition) diagram
4. Form the state table
5. Fill the state table

<u>Inputs</u>	Q(T)			Q(T+1)			<u>Outputs</u>
X	C	B	A	C	B	A	F
0	0	0	0	0	0	1	
	0	0	0	1	0	0	
	0	0	1	0	0	1	
	0	0	1	1	1	0	
	0	1	0	0	1	1	
	0	1	0	1	1	1	
	0	1	1	0	1	1	
	1	1	1	1	0	0	
1	0	0	0	0	1	1	
	0	0	0	1	0	0	
	0	0	1	0	0	1	
	0	0	1	1	0	1	
	0	1	0	0	0	1	
	0	1	0	1	0	0	
	0	1	1	0	1	1	
	1	1	1	1	1	0	

Count Up
X=0

Count Down
X=1

Inputs	Q(T)			Q(T+1)			Outputs
X	C	B	A	C	B	A	F
0	0	0	0	0	0	1	1
0	0	0	1	0	1	0	0
0	0	1	0	0	1	1	1
0	0	1	1	1	0	0	0
0	1	0	0	1	0	1	1
0	1	0	1	1	1	0	0
0	1	1	0	0	0	1	1
0	1	1	1	0	0	0	0
1	0	0	0	1	1	1	1
1	0	0	1	0	0	1	0
1	0	1	0	0	1	0	0
1	0	1	1	0	1	1	1
1	1	0	0	0	1	1	1
1	1	0	1	1	0	1	0
1	1	1	0	1	1	0	1
1	1	1	1	1	1	0	0

F depends only on
current state of

CBA

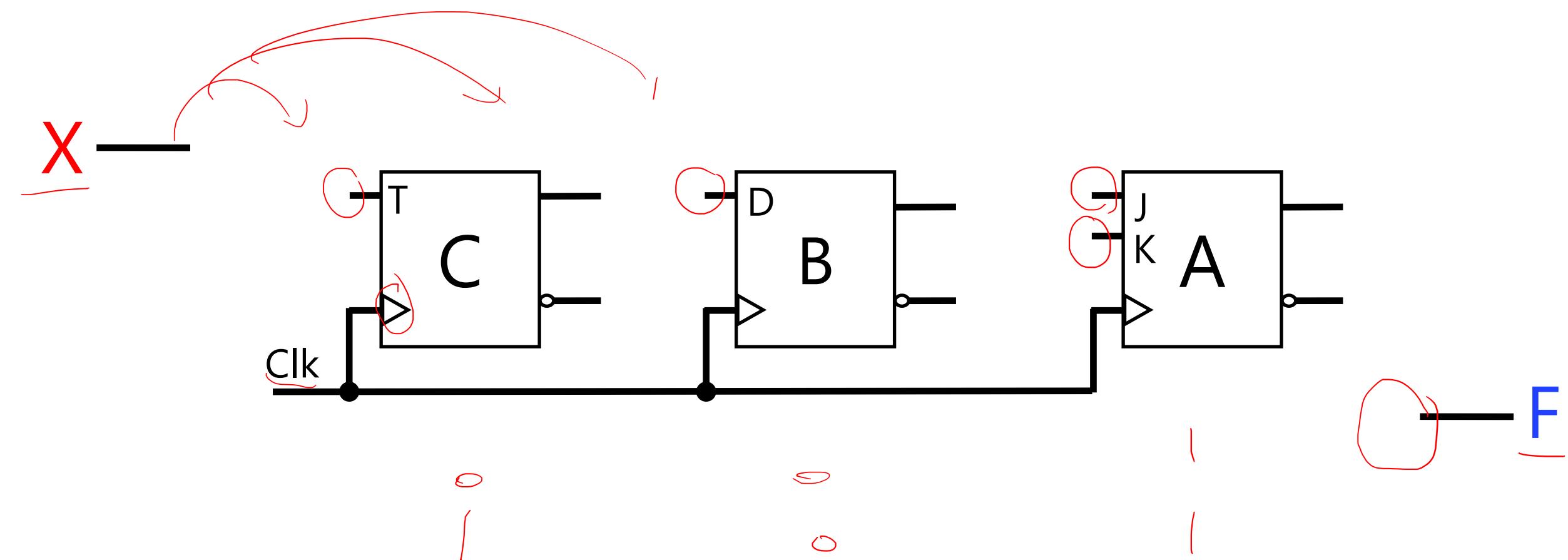
Inputs	Q(T)			Q(T+1)			Outputs	
	X	C	B	A	C	B	A	
0	0	0	0	0	0	0	1	1
0	0	0	0	1	0	1	0	0
0	0	0	1	0	0	1	1	1
0	0	0	1	1	1	0	0	0
0	1	0	0	0	1	0	1	1
0	1	0	0	1	1	1	0	0
0	1	1	0	0	1	1	1	1
0	1	1	1	0	1	1	0	0
1	0	0	0	0	1	1	1	1
1	0	0	0	1	0	0	0	0
1	0	0	1	0	0	0	1	1
1	0	0	1	1	0	1	0	0
1	1	0	0	0	0	1	1	1
1	1	0	0	1	1	0	0	0
1	1	1	0	0	1	0	1	1
1	1	1	1	1	1	1	0	0

Design (Recap)

0. Do we need combinational logic or sequential logic? Do we need memory?
1. How many storage (flip-flops)? #FF
2. How many input and output?
3. Form the state (transition) diagram
4. Form the state table
5. Fill the state table
6. What type of storage (flip-flop)? RS, D, T, JK, or Mixed

Counter Up/Down

Let's pick mix FFs: $1 \times T\text{-FF}$, $1 \times D\text{-FF}$, $1 \times JK\text{-FF}$



Design (Recap)

0. Do we need combinational logic or sequential logic? Do we need memory?
 1. How many storage (flip-flops)? #FF
 2. How many input and output?
 3. Form the state (transition) diagram
 4. Form the state table
 5. Fill the state table
 6. What type of storage (flip-flop)? RS, D, T, JK, or Mixed
 7. Input (*excitation*) equations for each FF
 8. Minimization of input (*excitation*) equations
- 

Excitation Equations

A	$J_A = F(X, C, B, A)$	$K_A = F(X, C, B, A)$
B	$D_B = F(X, C, B, A)$	
C	$T_C = F(X, C, B, A)$	

Input

Flip-Flops

Excitation Equations

A	$J_A = F(X, C, B, A)$	$K_A = F(X, C, B, A)$
B	$D_B =$	
C	$T_C =$	

Inputs	Q(T)				Q(T+1)		Outputs	Excitation for A			
X	C	B	A		C	B	A	F	Action	J_A	K_A
0	0	0	0		0	0	1	1	Set/Cmp	1	x
0	0	0	1		0	1	0	0	Rst/Cmp	x	1
0	0	1	0		0	1	1	1	Set/Cmp	1	x
0	0	1	1		1	0	0	0	Rst/Cmp	x	1
0	1	0	0		0	1	1	1	Set/Cmp	1	x
0	1	0	1		1	1	0	0	Rst/Cmp	x	1
0	1	1	0		1	1	1	0	Set/Cmp	1	x
0	1	1	1		0	1	1	1	Rst/Cmp	x	1
0	1	1	1		1	0	0	0	Set/Cmp	1	x
1	0	0	0		0	1	1	1	Rst/Cmp	x	1
1	0	0	1		1	0	0	0	Set/Cmp	1	x
1	0	1	0		0	0	1	1	Rst/Cmp	x	1
1	0	1	1		1	0	0	0	Set/Cmp	1	x
1	1	0	0		0	0	1	1	Rst/Cmp	x	1
1	1	0	1		1	1	0	0	Set/Cmp	1	x
1	1	1	0		0	1	1	1	Rst/Cmp	x	1
1	1	1	1		1	1	0	0	Set/Cmp	1	x
1	1	1	1		0	1	1	0	Rst/Cmp	x	1

Excitation Equations

A	$J_A = \sum(0, 2, 4, 6, 8, 10, 12, 14) + d(\underline{1, 3, 5, 7, 9, 11, 13, 15})$	$K_A = \sum(1, 3, 5, 7, 9, 11, 13, 15) + d(0, 2, 4, 6, 8, 10, 12, 14)$
B	$D_B =$	
C	$T_C =$	

Inputs	Q(T)			Q(T+1)		Outputs		Excitation for B	
X	C	B	A	C	B	A	F	Action	D_B
0	0	0	0	0	0	1	1	Rst	0
0	0	0	1	0	1	0	0	Set	1
0	0	1	0	0	0	1	1	Set	1
0	0	1	1	1	0	0	0	Rst	0
0	1	0	0	0	0	1	1	Rst	0
0	1	0	1	1	1	0	0	Set	1
0	1	1	0	1	1	1	1	Set	1
0	1	1	1	0	1	1	1	Rst	0
1	0	0	0	0	1	1	1	Set	1
1	0	0	1	0	0	0	0	Rst	0
1	0	1	0	0	0	1	1	Rst	0
1	0	1	1	1	0	0	0	Set	1
1	1	0	0	0	0	1	1	Rst	0
1	1	0	1	1	1	0	0	Rst	0
1	1	1	0	1	1	1	0	Set	1
1	1	1	1	1	1	1	0	Set	1

Excitation Equations

A	$J_A = \sum(0,2,4,6,8,10,12,14) + d(1,3,5,7,9,11,13,15)$	$K_A = \sum(1,3,5,7,9,11,13,15) + d(0,2,4,6,8,10,12,14)$
B	$D_B = \sum(1,2,5,6,8,11,12,15)$	Never "don't care condition" happens in D-FF
C	$T_C =$	

Inputs	Q(T)			Q(T+1)			Outputs	Excitation for C	
X	C	B	A	B	A	F	Action	T _d	
0	0	0	0	0	1	1	Store	0	
0	0	0	1	1	0	0	Store	0	
0	0	1	0	1	1	1	Store	0	
0	0	1	1	0	0	0	Comp	1	
0	1	0	0	1	1	1	Store	0	
0	1	0	1	1	0	0	Store	0	
0	1	1	0	1	1	1	Store	0	
0	1	1	1	0	0	0	Comp	1	
1	0	0	0	1	1	1	Comp	1	
1	0	0	1	0	0	0	Store	0	
1	0	1	0	0	1	1	Store	0	
1	0	1	1	1	0	0	Store	0	
1	1	0	0	0	1	1	Comp	1	
1	1	0	1	1	0	0	Store	0	
1	1	1	0	1	1	1	Store	0	
1	1	1	1	0	0	0	Store	0	

Excitation Equations

A	$J_A = \sum(0,2,4,6,8,10,12,14) + d(1,3,5,7,9,11,13,15)$	$K_A = \sum(1,3,5,7,9,11,13,15) + d(0,2,4,6,8,10,12,14)$
B	$D_B = \sum(1,2,5,6,8,11,12,15)$	Never “don’t care condition” happens in D-FF
C	$T_C = \sum(3,7,8,12)$	Never “don’t care condition” happens in T-FF

Minimization for excitation equations

?-Variable K-Map

Excitation Equations

A	$J_A = \sum(0,2,4,6,8,10,12,14) + d(1,3,5,7,9,11,13,15)$	$K_A = \sum(1,3,5,7,9,11,13,15) + d(0,2,4,6,8,10,12,14)$
B	$D_B = \sum(1,2,5,6,8,11,12,15)$ <i>Never “don’t care condition” happens in D-FF</i>	
C	$T_C = \sum(3,7,8,12)$ <i>Never “don’t care condition” happens in T-FF</i>	

Minimization
4-Variable K-Map
F(X, C, B, A)

		BA			
		00	01	11	10
XC	00	0 m ₀	0 m ₁	1 m ₃	0 m ₂
	01	0 m ₄	0 m ₅	1 m ₇	0 m ₆
	11	1 m ₁₂	0 m ₁₃	0 m ₁₅	0 m ₁₄
	10	1 m ₈	0 m ₉	0 m ₁₁	0 m ₁₀

$$T_C = \sum(3, 7, 8, 12) \\ = X'BA + XB'A'$$

		BA			
		00	01	11	10
XC	00	0 m ₀	1 m ₁	0 m ₃	1 m ₂
	01	0 m ₄	1 m ₅	0 m ₇	1 m ₆
	11	1 m ₁₂	0 m ₁₃	1 m ₁₅	0 m ₁₄
	10	1 m ₈	0 m ₉	1 m ₁₁	0 m ₁₀

$$D_B = \sum(1, 2, 5, 6, 8, 11, 12, 15) \\ = X'B'A + X'BA' + XB'A' + XBA \\ = X'(B'A + BA') + X(B'A' + BA) \\ = X'(B \oplus A) + X(B \odot A) \\ = X \oplus B \oplus A$$

		BA			
		00	01	11	10
XC	00	1 m ₀	X m ₁	X m ₃	1 m ₂
	01	1 m ₄	X m ₅	X m ₇	1 m ₆
	11	1 m ₁₂	0 m ₁₃	X m ₁₅	1 m ₁₄
	10	1 m ₈	0 m ₉	1 m ₁₁	X m ₁₀

		BA			
		00	01	11	10
XC	00	X m ₀	1 m ₁	1 m ₃	X m ₂
	01	X m ₄	1 m ₅	1 m ₇	X m ₆
	11	X m ₁₂	1 m ₁₃	1 m ₁₅	X m ₁₄
	10	X m ₈	1 m ₉	1 m ₁₁	X m ₁₀

$$K_A = \sum(1, 3, 5, 7, 9, 11, 13, 15) + \\ d(0, 2, 4, 6, 8, 10, 12, 14) = 1$$

Excitation Equations

A	$J_A = 1$	$K_A = 1$
B	$D_B = F(X, C, B, A) = X \oplus B \oplus A$	
C	$T_C = F(X, C, B, A) = X'BA + XB'A'$	

Design (Recap)

0. Do we need combinational logic or sequential logic? Do we need memory?
1. How many storage (flip-flops)? #FF
2. How many input and output?
3. Form the state (transition) diagram
4. Form the state table
5. Fill the state table
6. What type of storage (flip-flop)? RS, D, T, JK, or Mixed
7. Input (*excitation*) equations for each FF
8. Minimization of input (*excitation*) equations
9. Boolean function for the output }
10. Minimization of output variable }

Inputs	Q(T)			Q(T+1)			Outputs
X	C	B	A	C	B	A	F
0	0	0	0	0	0	1	1
0	0	0	1	0	1	0	0
0	0	1	0	0	1	1	1
0	0	1	1	1	0	0	0
0	1	0	0	1	0	1	1
0	1	0	1	1	1	0	0
0	1	1	0	1	1	1	1
0	1	1	1	0	0	0	0
1	0	0	0	1	1	1	1
1	0	0	1	0	0	0	0
1	0	1	0	0	0	1	1
1	0	1	1	0	1	0	0
1	1	0	0	0	1	1	1
1	1	0	1	1	0	0	0
1	1	1	0	1	0	1	1
1	1	1	1	1	1	0	0

Moore $F(C, B, A)$ *Moore*

Outputs

*F**SP*

1

0

1

0

1

0

0

1

0

1

0

0

1

1

0

1

0

0

1

0

1

0

1

0

1

0

Inputs	Q(T)			Q(T+1)			Outputs
X	C	B	A	C	B	A	Y
0	0	0	0	0	0	1	1
0	0	0	1	0	1	0	0
0	0	1	0	0	1	1	1
0	0	1	1	1	0	0	0
0	1	0	0	1	0	1	1
0	1	0	1	1	1	0	0
0	1	1	0	1	1	1	1
0	1	1	1	0	0	0	0
1	0	0	0	1	1	1	1
1	0	0	1	0	0	0	0
1	0	1	0	0	1	0	1
1	0	1	1	0	1	0	0
1	1	0	0	0	1	1	1
1	1	0	1	1	0	0	0
1	1	1	0	1	0	1	1
1	1	1	1	1	1	0	0

Incorrect but if we make
a mistake that X is
involved:

$$F = F(X, C, B, A) =$$

$$\sum(0, 2, 4, 6, \cancel{8}, \cancel{10}, \cancel{12}, \cancel{14})$$

Minimization for output Y

3-Variable K-Map

Incorrectly ~~4~~-Variable K-Map

		BA			
		00	01	11	10
C	0	1 m ₀	0 m ₁	0 m ₃	1 m ₂
	1	1 m ₄	0 m ₅	0 m ₇	1 m ₆

$$\begin{aligned}
 F &= F(C, B, A) = \sum(0, 2, 4, 6) \\
 &= \textcircled{A'}
 \end{aligned}$$

Even if we consider the input, because Y does not depend on X in the state table, it will disappear in simplification!

		BA			
		00	01	11	10
X _C	00	1 m ₀	0 m ₁	0 m ₃	1 m ₂
	01	1 m ₄	0 m ₅	0 m ₇	1 m ₆

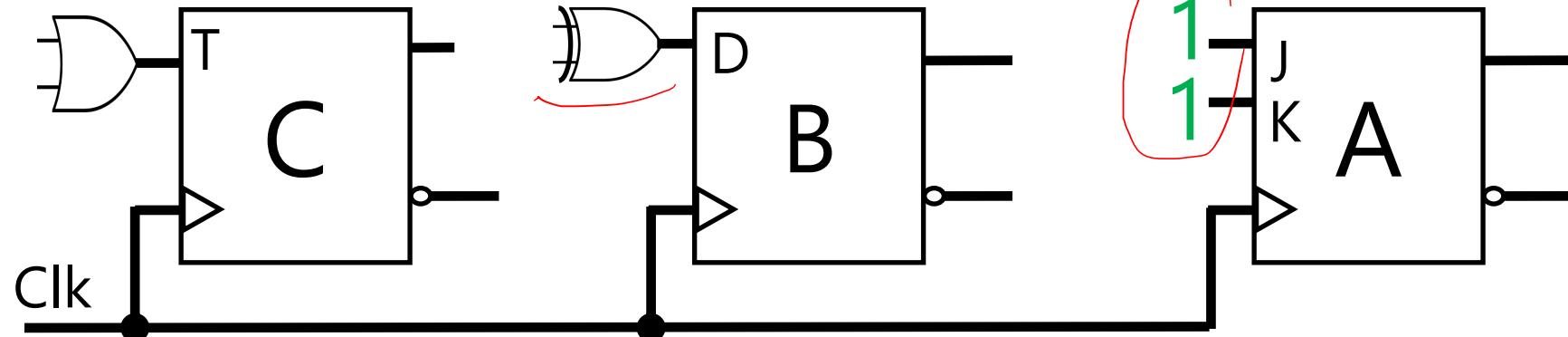
		BA			
		00	01	11	10
X _C	11	1 m ₁₂	0 m ₁₃	0 m ₁₅	1 m ₁₄
	10	1 m ₈	0 m ₉	0 m ₁₁	1 m ₁₀

$$\begin{aligned}
 F &= F(X, C, B, A) = \sum(0, 2, 4, 6, \textcircled{8}, 10, 12, 14) \\
 &= \textcircled{A'}
 \end{aligned}$$

Design (Recap)

0. Do we need combinational logic or sequential logic? Do we need memory?
 1. How many storage (flip-flops)? #FF
 2. How many input and output?
 3. Form the state (transition) diagram
 4. Form the state table
 5. Fill the state table
 6. What type of storage (flip-flop)? RS, D, T, JK, or Mixed
 7. Input (*excitation*) equations for each FF
 8. Minimization of input (*excitation*) equations
 9. Boolean function for the output
 10. Minimization of output variable
 11. Draw/Sketch Logic Circuit
 12. (Optional) Test
-

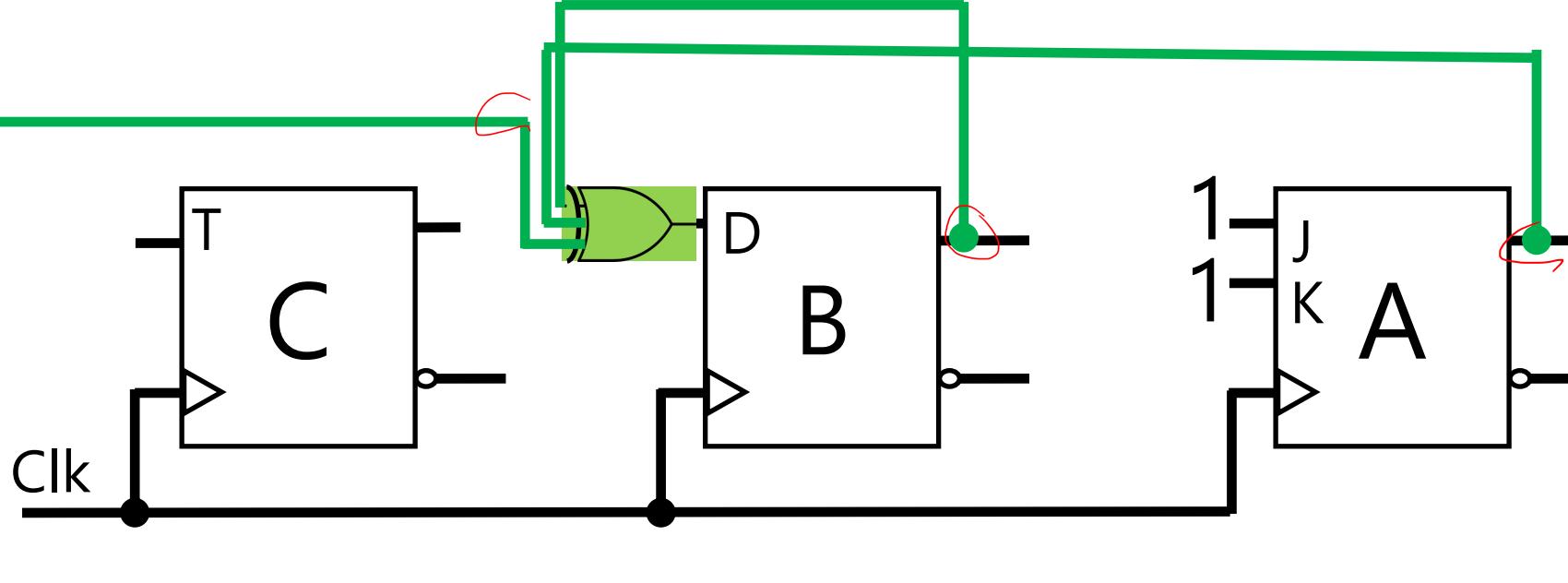
X—



— F

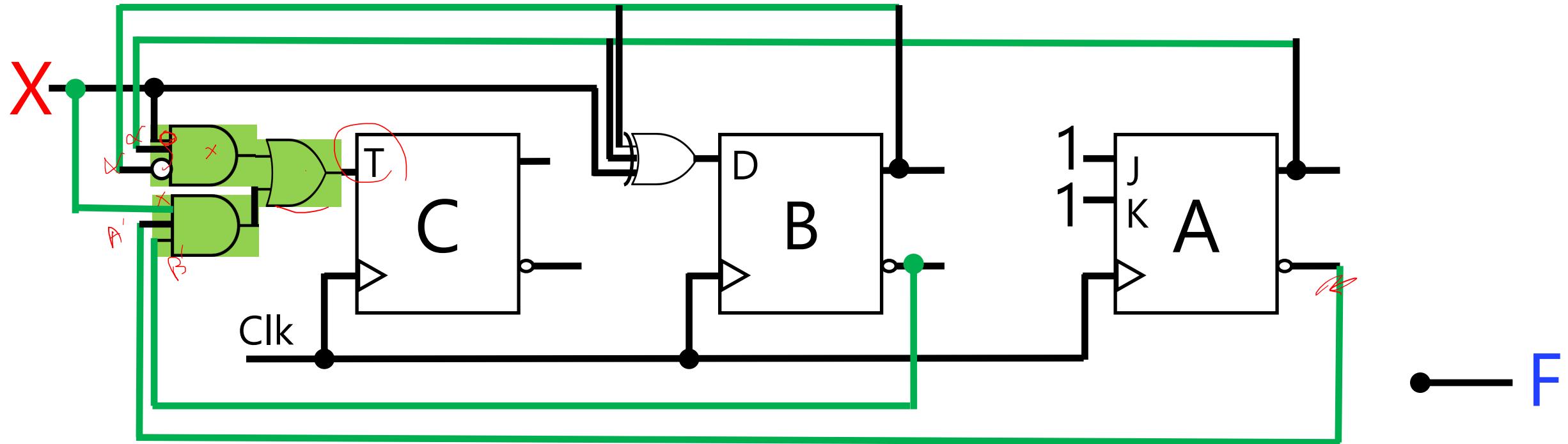
A	$J_A = 1$	$K_A = 1$
B	$D_B = F(X, C, B, A) = X \oplus B \oplus A$	
C	$T_C = F(X, C, B, A) = X'BA + XB'A'$	
F	$F(C, B, A) = A'$	

X

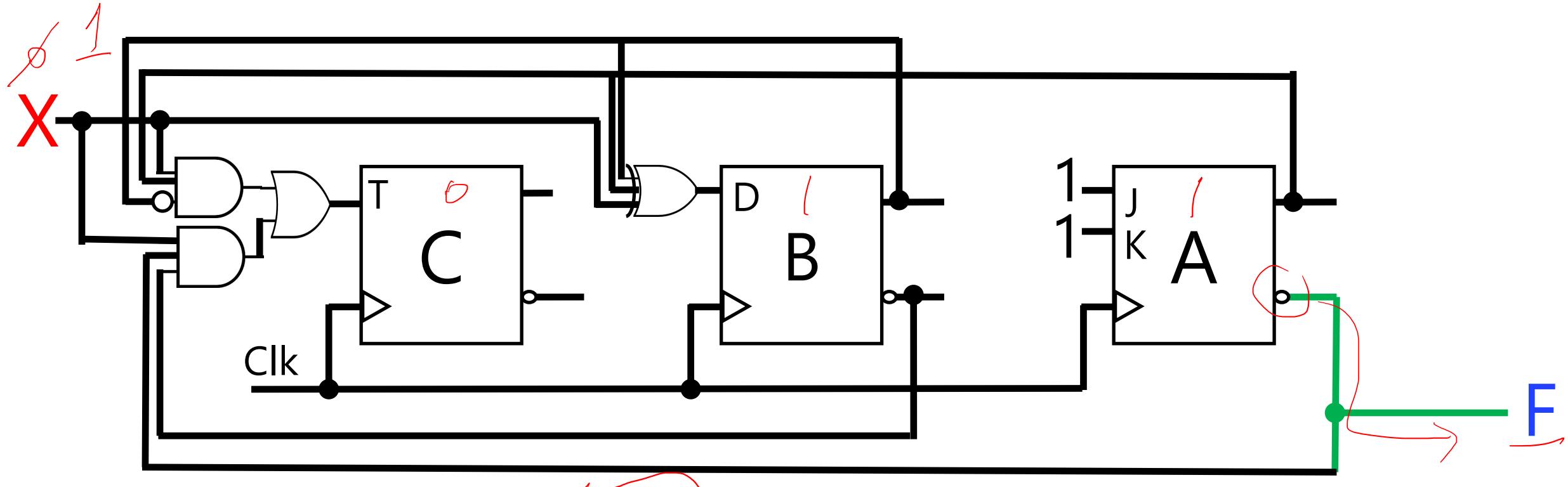


F

A	$J_A = 1$	$K_A = 1$
B	$D_B = F(X, C, B, A) = X \oplus B \oplus A$	
C	$T_C = F(X, C, B, A) = X'BA + XB'A'$	
F	$F(C, B, A) = A'$	



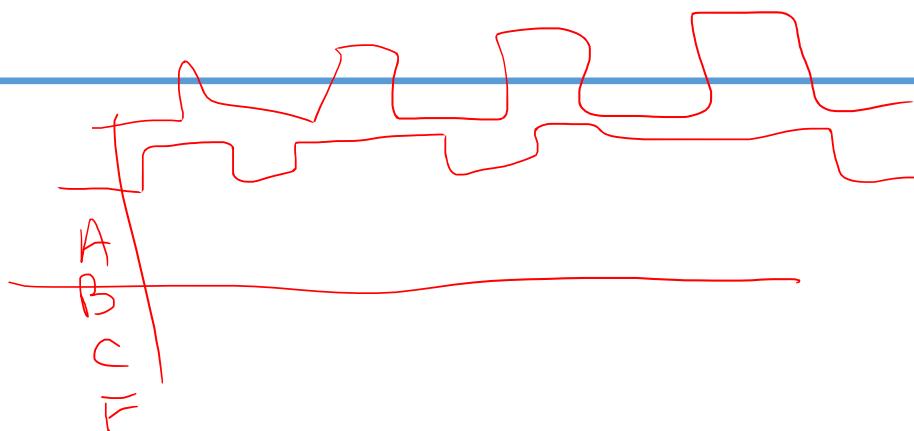
A	$J_A = 1$	$K_A = 1$
B	$D_B = F(X, C, B, A) = X \oplus B \oplus A$	
C	$T_C = F(X, C, B, A) = \cancel{X'BA} + \cancel{XB'A}$	
F	$F(C, B, A) = A'$	



A	$J_A = 1$	$K_A = 1$	2 1	3	4 1
B	$D_B = F(X, C, B, A) = X \oplus B \oplus A$				
C	$T_C = F(X, C, B, A) = X'BA + XB'A'$				
F	$F(C, B, A) = \textcircled{A'}$				

Synchronization

Practice Timing Diagram



Counter Up-Down Example Mealy Model

Counting up and number is even

Counting up and number is odd

Counting down and number is odd

Counting down and number is even

Counter Up-Down

X=0: Count up from i to i+1

X=1: Count down from N i to i -1

F = 1 when counting up (X=0) and number is even

F = 0 when counting up (X=0) and number is odd

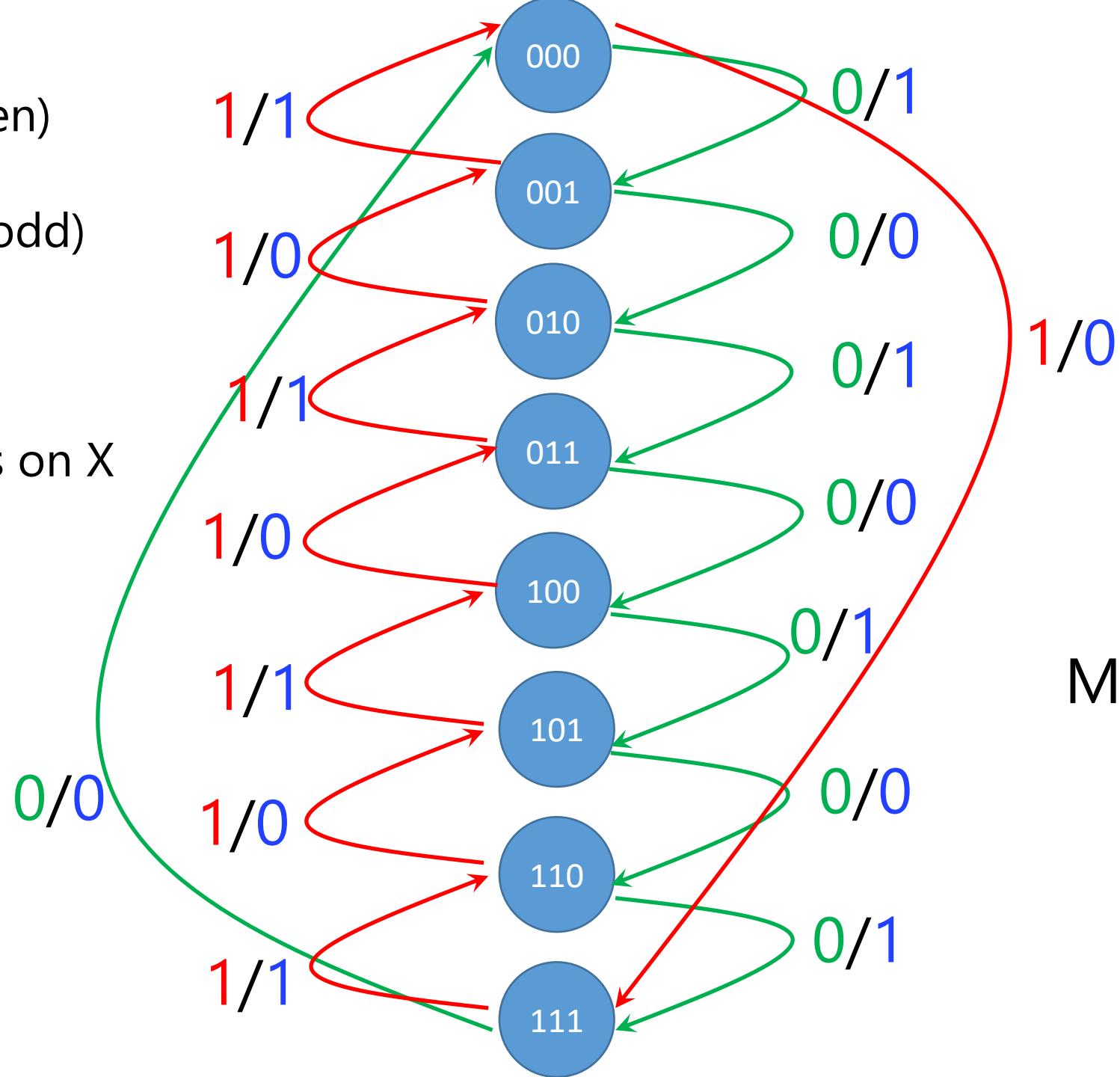
F = 1 when counting down (X=1) and number is odd

F = 0 when counting down (X=1) and number is even

$F=1$: (up AND even)
OR
(down AND odd)

$F=0$: else

Y clearly depends on X



Mealy Model
 X/F

Inputs	Q(T)			Q(T+1)			Outputs
X	C	B	A	C	B	A	F
0	0	0	0	0	0	1	1
0	0	0	1	0	1	0	0
0	0	1	0	0	1	1	1
0	0	1	1	1	0	0	0
0	1	0	0	1	0	1	1
0	1	0	1	1	1	0	0
0	1	1	0	1	1	1	1
0	1	1	1	0	0	0	0
1	0	0	0	0	0	0	0
1	0	0	1	0	0	1	1
1	0	1	0	0	1	0	1
1	1	0	0	0	1	1	0
1	1	0	1	1	0	0	1
1	1	1	0	1	0	1	0
1	1	1	1	1	1	0	1

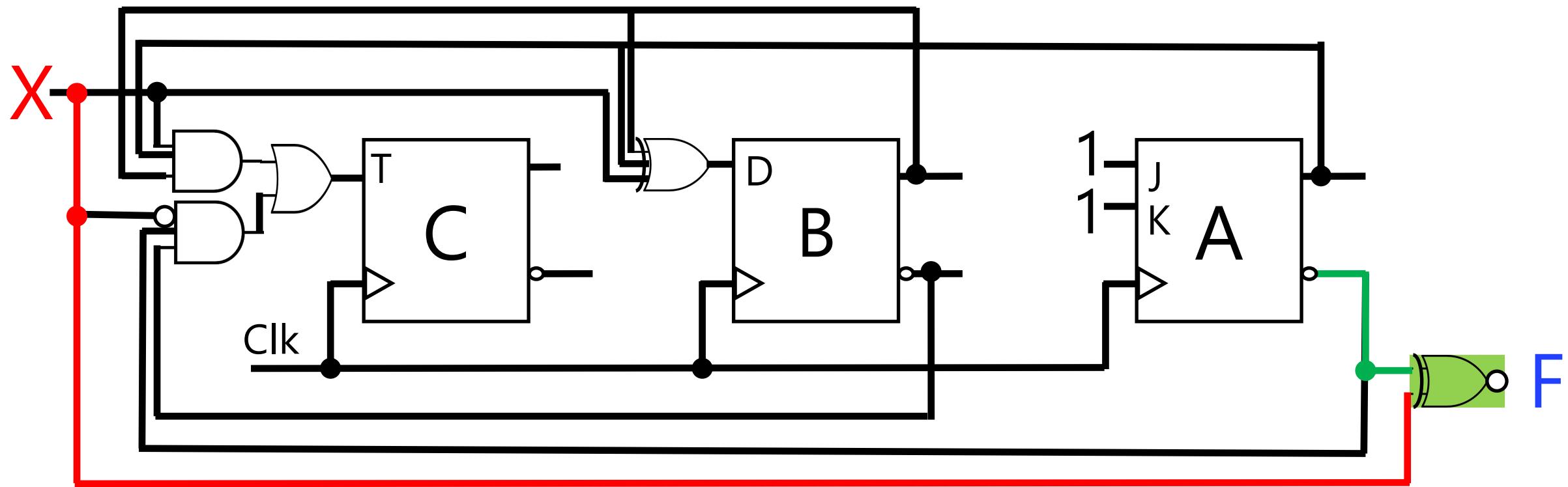
$$Y = \sum(X, C, B, A) = \\ \sum(0, 2, 4, 6, 9, 11, 13, 15)$$

Minimization for output F

4-Variable K-Map

		BA			
		00	01	11	10
XC	00	1 m ₀	0 m ₁	0 m ₃	1 m ₂
	01	1 m ₄	0 m ₅	0 m ₇	1 m ₆
	11	0 m ₁₂	1 m ₁₃	1 m ₁₅	0 m ₁₄
	10	0 m ₈	1 m ₉	1 m ₁₁	0 m ₁₀

$$\begin{aligned}
 F &= F(X, C, B, A) = \sum(0, 2, 4, 6, 9, 11, 13, 15) \\
 &= X'A' + XA \\
 &= X \odot A
 \end{aligned}$$



A	$J_A = 1$	$K_A = 1$
B	$D_B = F(X, C, B, A) = X \oplus B \oplus A$	
C	$T_C = F(X, C, B, A) = X'BA + XB'A'$	
F	$F(C, B, A) = X \odot A$	

Asynchronization Practice Timing Diagram
