

## OCXO Part No.: OS400-4915-002

Issue 1; 2nd December 2022

### Features

- Temperature stability  $\pm 5$ ppb
- Low phase noise
- Frequency 49.1520 MHz
- Industry standard package
- The flexible nature of the design means that variations to suit almost any application can be developed to meet individual customer requirements

### Option (custom)

- Temperature stability:  $\pm 5$ ppb over (0 to  $+50$ ) $^{\circ}$ C
- Output: CMOS 15pF, 45% 50%
- Voltage: 5.0V
- Warm up current: 390mA
- Quiescent current: 180mA

### Phase Noise (typical)

- $F_0 + 10$ Hz -110 dBc/Hz
- $F_0 + 100$ Hz -135 dBc/Hz
- $F_0 + 1$ KHz -153 dBc/Hz
- $F_0 + 10$ KHz -162 dBc/Hz
- $F_0 + 100$ KHz -165 dBc/Hz

### Voltage /Load change

- $\pm 5\%$  supply voltage change:  $\pm 2$ ppb
- $\pm 10\%$  load change:  $\pm 10$ ppb

### Ageing

Based on 10MHz unit after 30 days continuous operation:

- Per day:  $\pm 0.1$ ppb max.
- Per year:  $\pm 50$ ppb max.
- Warm up time: 5 minutes to within 0.1ppm

### Voltage Trim

- No Connect

### Reference Options

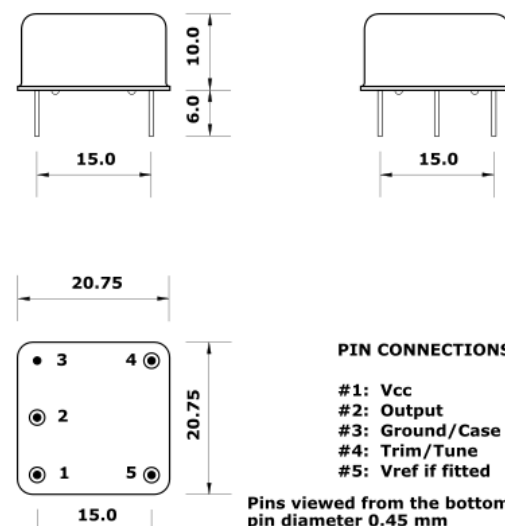
- +3.0V for +3.3V supply

### Environmental

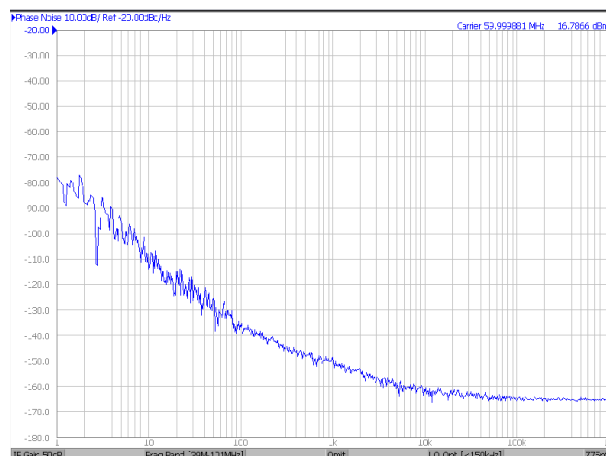
- Electrostatic-Sensitive Device (ESD)
- Storage Temperature Range: ( $-40$  to  $+125$ ) $^{\circ}$ C
- Mechanical shock: MIL standard 202F, method 213, condition J
- Thermal shock: MIL standard 202F, method 107, condition A
- Vibration: MIL standard 202F, method 204, condition B



### Dimensions (mm)



### Phase Noise Plot



- Solderability: 5 seconds maximum at 230°C
- 3 seconds maximum at 350°C

#### Compliance

- RoHS Status (2011/65/EU) - Compliant
- REACH Status - Compliant

#### Packaging

- Pack Style: Bulk

#### Ordering Information

- OCXO Part No.: OS400-4915-002
- Frequency: 49.1520 MHz

#### Test Circuit - CMOS Load

