

DATA LEVEL PARALLELISM

in vector SIMD, and GPU architectures.



Siemens Cray1, Cray2, Xcray3,

a set of applications ~~of~~ have significant DLP, not only matrix oriented applications in scientific computing.

media oriented image and sound processing, machine learning algorithms

SIMD is potentially more energy efficient compared to MIMD. biggest advantage of SIMD vs MIMD is that the programmer continues to think sequentially and achieve parallel speedup.

SIMD systems is attractive for μ PDs, also servers.

ECS 1330 B variations of SIMD in μ PD for web server.

- multimedia SIMD inst. set extensions,
- graphics processing unit. CUDA

120/100 PF

vector architectures are easier to understand and to compile it to, than other SIMD variations.

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who since now have (S) μ PDs

MMX is

SSE1 and SSE2.

SSE