3 LOGICAL TIME

- * scalar time logical time used to capture causality of events in distributed
- " vector time systems, as physical time is not accurate enough.
- " metroc time.

necessity for knowledge of causality

- * distributed algorithm design
 - leveness & featuress in motual exclusion algorithms
 - · Meintain consistently in raplicated olatobases
 - " hill design correct develock objection algorithms
 - " avoid phanton and undetected dualecks
- · tracking of dependent events
 - "histoming reexecution, in failure receiving-
 - help build a checkpoint in replicated dotainses
 - and in detection of file inconsistencies in once of methodisk plantitioning.
- · knowledge about progress
- discarding obsolets information
- garbage collection
- termination eletection
- · concurrency measure

clock consistency condition

ei - ej i coei) (coei)

strongly consistent

e: - e; (-) c(e;) < c(eg)

datastructures

(to represent logical time)

- · local clock own progress
- · global clock · lecal view of others progress

protocol

Cupdate plata to ensure consistency condn.)

- · Ri updating local clock.
- * R2 updating global clock

SCALAR TIME



Lines

a: local clock & local view of global time (integer)

RI Ci = Ci+d

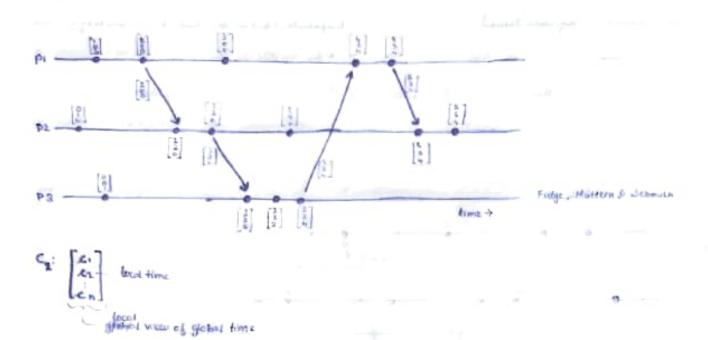
fon minternal, send &

R2, C1 = max (C1, Cmg)

- 2 execute RI
- a deliver mag
- Eon recieve &
- · totally ordering ements possible
 - imesiamps: 2= (t,i) q=(v,j)
 - 2 kg & stev or tousing
 - => x -> y v xlly

events with same scalar time are independent tie-breaking mechanism used if some 1.

- · event counting, among processes
- · no strong consistency



$$C_i = C_i$$
 \longleftrightarrow $\forall n$ $C_i \in C_i \in C_i$ \longleftrightarrow $\forall n$ $C_i \in C_i \in C_i$ \longleftrightarrow $C_i \in C_i$ and $\exists n$ $C_i \in C_i \in C_i$ \longleftrightarrow $C_i \in C_i$

- · isomorphism between events & sometationings
 - x 11 y 10 Cx 11 Cy

- · strong consistency we are stone of missing related
- · event counting

applications distributed delegging a could be manufaction a sound county memory, establish glashil breakfronts, consurrange of characterists.

EFFICIENT IMPLEMENTATIONS OF VECTOR CLOCKS

SINGHAL KSHEMKALYANI'S DIFFERENTIAL TECHNIQUE

observation: between successive send to same process, only few entires of vector clock out sender process charge (likely).

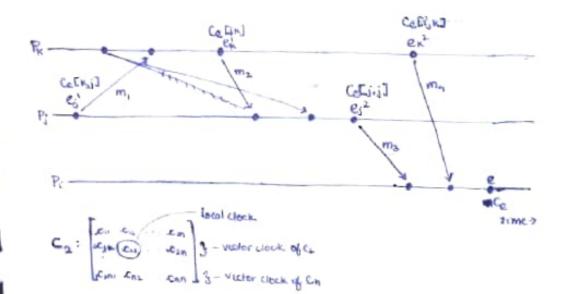
LS: Last sent

LU: Lost update

value of local clock when po last sent value of local clock when Pi conducted message to P; respective entry of its clock.

& CPx, ex) 3 data can be sent in form of tuples instead of full vector.

MATRIX TIME



Eon internal, send }

RI CICI, (1 = CEC; i) + d Re CICI; +] = max (CICI, +], Comp [i, +] Ci = max (Ci, Cmg)

2. execute R

3. deliver msg

· disrard obsolete information m. (C, [a, e]) > every process knows that Pe time has progressed the to processes will no longer venuite from the certain information.

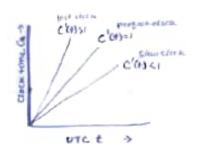
for var ins fact to charact passeste information

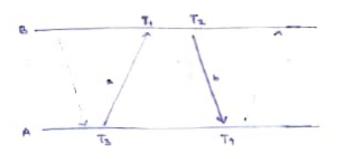
PHYSICAL CLOCK SYNCHRONIZATION: MTP.

notion of time, interval, and relative ordering of events. due to different clock rates, periodic clock synchronization is necessary. (with UTG)

- time: Ca. (+)
- · Offset: (a.(t) t time diff. cort real time
- * drift: Ca"(t)
 acceptoration of clock progress
- · brequency: Ca'(1)

 rate at which clock progresso.
- * skew: Cal(e) -1
 rate diff. com perfect clock





clock offset o (wit a)

= a+b = mand (Court a) Teats a (Tax Ta)

= a = worththatinet;

extension time protocol (NTP) on internet uses the offset delay estimation method:

NTP involves a hierarchian tree of time servers. Here, peers A, & orn inclesendently colacide delay & affset using a single bidirectional message stream.

* most recent pairs of 0, \$8 taken. The pair with min. & is chosen.