Processes to a shared resource (or data) a socialized.

process holding unique token can enter Cs.

algos differ by how they search for token.

non-token based

to determine who can enter CS next. a process enters as when an assertion becomes true

(and balse for everyone else).

subsets of processes (querums) are formed

in such a way that when 2 processes

request CS, at least one process recieves

both requests and is responsible to make (arms).

Aure only one process gets CS.

algorithm performance in load:

low load . seldom more than one request for CS.

heavy load - alwegs a pending request for cs.

Ca process is deldom in delestate in heavy load)

algorithm best and worst case performance:

most algorithm best response time: 27+E rand trying deby

often, best and worst cases raincide with less and high loads.

process Pi in one of 3 states:

- requesting as count)

- executing CS

neither (idle)

algorithm requirements:

- Safety: only i process in CS

Liveness: no deadlack or starvation

· fairness: (S executed in order of arrival

performance metrics:

message complexity

no of mags per CS execution

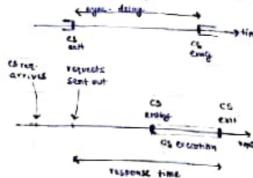
time gap between csexit & entry.

- response time

time gap blu Cs request & exit.

- system throughput

charaction on time



## LAMPORT'S ALGORITHM

processes use scalar clocks and request-queue to perform mutual exclusion. request-queue is ordered by timestamps. process at the top of request-queue enters CS. requires fifo channels for fairness, and mut. ex-

Proof: motual exclusion achieved. (by contradiction)

assume P., Fe in CS — O

tassume P. Request & Reservest. (cs) — O

O + P2 and REPLY to P1

PZ has PI REDUEST before Pirety

Dor PI REQUEST at top of P2 request queue.

contradiction > mutual exculturion achieved.

assume Pa in CS (hypere PI) ... D

proof: fairness achieved (by matradiction)

0 + P2 recieved P.'s REPLY

+ P2 received Pt REQUEST (FIFO channel)

(a) of REQUEST at top of P2 request queue.

contradiction of farness achieved.

optimization:
of Pi, Pi REQUEST & P. REQUEST
> no need to REPLY P.

> 0 (2(n-1) +> 0 (3(n-1))

CS request :

· Pe broadous Request Ct, i) . one ison)

· B on recreasing REQUEST (t, i) returns timestamped mREPLY to P: and adds

REQUEST to request queue;

execute CS!

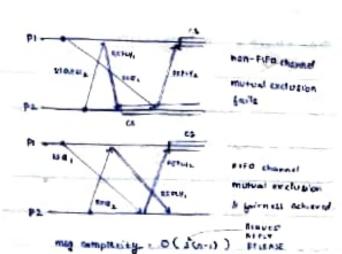
11: Prrecieved REPLY with larger timestamp from all.

12: Pris REQUEST at top of request queue,

release CS:

Pe on as exit broadcasts timestamped RELEASE to all (inc. essel).

Ps on recieving RELEASE, removes Pris
REQUEST from request-queue;



# RICART - AGARWALA ALGORITHM

rautual exclusion achieved.

Processes with seator cheeks and request deferred -	request CS:
terroit to perform mutual exclusion without a	· Pr. broadouts timestamped REQUEST.
the channel.	· Pron recieving REQUEST from Pr sends REPLY
the second secon	only of Pais not executing as and PAREQUEST
proof: motural exclusion achieved (by contradiction)	timestamp is larger than Pr REQUEST. else,
assume Pi, Pa in Cs _ 0	reply is deferred and P sets RP, [1]=1.
salome PI REQUEST (PL REQUEST (N) - 0	
On P2 has PI REPLY	exercise CS:
PI not th CS & PI REMY PAREM	· Pi received REPLY from all.
contradiction 3 mutual exclusion achieved	
Pi cs	release CS:
REG.	· P: conds all deferred REPLY messages:
br vegr	YRDEGIT-1, Pe sends REPLY to P; and sets
mag complexity . OC ICH-1) ) ALPLY	RP(G]-0.
	-
MAERAWA'S ALGORITHM	
Corde	itons, for request sets repost set (quarum)
first quorum based algorithm, a process only.	MI: Rin Rit & vets
requests a subset of processes (quorum). two	M2: Pi e Re +6
simultaneously requesting process as withing thete	M3:  R1  = K
quorum will have atleast I common process, white	M4: Prekno of Ris Vij
REPLYS to only one of the two.	N = K(K-1)+1 .  Ril = VN

Cheory of projective planes)

no of Processes

proof: mutual exclusion achieved (by amunicion) request cs:

assume Pi, Pi in C5 \_ 0

Assume R: n R; · EPij J \_ 0

O + Pi get REPLY from all in Ri

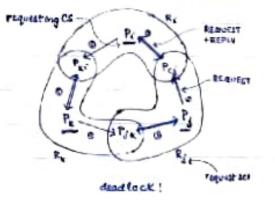
O + Pi get REPLY from all in Ri

O + Pi get REPLY from all in Ri

Contractiction + mutual exclusion achieved

mag complexity: O( 2778) release

markawa's algorithm an deadlock because a process is locked by other processes, and requests are not prioritized by timestamps.



in response to smoothe Cy) from Pij, Pi

sends vield () to Pij of R has recieved

FAILED from a process in Ri, and of it

sent vield to any one but not recieved

new REPLY from it.

mag complexity: O(str)

- . Pi sends REQUEST (1) to all processes in Ri.
- P; on recreving REQUEST (1), sends REPLY (2) only if it hashit dent REPLY to a process since last RELEASE mag. else, it queues REQUEST (1) for later consideration.

#### execute cs:

· Pe enters CS after it received REPLY from every process in Ri.

### release CS:

- · Pr sends RELEASERS to every site in Ri.
- By on recieving RELEASE (1), sends REPLY to
  ment process in it queue after deleting Pi.
   if queue is empty, it updates its state to
  no REPLY sent since lost RELEASE.

# handling deadlacus:

- " when REQUEST (+, j) brom P; blocks at Pij because Pij sent REPLY to Pi, Pej sends FAILED (ij) to Pj if its request has lower priority, else Pij Sende Impuire (j) to Pi.
- of Pi at appropriate location in queue, and sends a REPLY Gj) to process at top of queue.