

30.7.2020

MARCH

Monday

WK 12 • 076-289

17

|    |    |    |    |    |    |    |
|----|----|----|----|----|----|----|
|    | 1  | 2  | 3  | 4  | 5  | 6  |
| 7  | 8  | 9  | 10 | 11 | 12 | 13 |
| 14 | 15 | 16 | 17 | 18 | 19 | 20 |
| 21 | 22 | 23 | 24 | 25 | 26 | 27 |
| 28 | 29 | 30 |    |    |    |    |
| M  | T  | W  | T  | F  | S  | S  |

ECE 468

ANALOG IC DESIGN

9 TAUGHT BY

10 prof. abhishek srivastava

11 prof. zia abbas

12

1 COURSE TOPICS

2 basics of analog design

3 • MOS model for analog circuits

4 • large signal modeling

5 • incremental modeling

6 • MOS parasitics

• mismatches

• speed ( $C_{BT}$ )• passive components for IC design ( $R, C, L$ )

• biasing

• negative feedback for biasing

2014

MARCH

18

Tuesday

077-288 • WK 12

GUITAR CONCERT

QUOTES FROM AMP USERS

30.7.2020

2

|   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |
|---|---|---|---|---|---|---|---|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|
| 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 | 29 | 30 | 31 |
| 5 | M | T | W | T | F | S |   |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |    |

- introduction to layout
- gain - BW - swing - power - noise - area trade-offs

## 2. amplifier design

- review of single stage amplifiers
- Single-ended and differential amplifier designs
- $g_m / I_{DQ}$  design technique
- sub-threshold design technique for low power consumption.
- techniques to increase gain of amplifiers
- active loads
- cas code
- differential amplifier with current mirror load.
- mirror pole
- stability issues and utility of negative gain feedback in high gain amplifiers

|    |    |    |    |    |    |    |
|----|----|----|----|----|----|----|
| 7  | 1  | 2  | 3  | 4  | 5  | 6  |
| 14 | 8  | 9  | 10 | 11 | 12 | 13 |
| 21 | 15 | 16 | 17 | 18 | 19 | 20 |
| 28 | 22 | 23 | 24 | 25 | 26 | 27 |
| M  | T  | W  | T  | F  | S  | S  |

30.7.2020

3

MARCH

Wednesday

WK 12 • 078-287

19

### 3. operational amplifier design

- review of op-amp characteristics

- CMRR

- Offset

- single stage op-amp

- high gain op-amps - telescopic, two stage

- stability and frequency compensation

- fully differential amplifier (FDA)

- common-mode feedback

- review of low noise

- low voltage op-amp design techniques.

### 4. other topics

- noise

- layout techniques

- effect of off chip components and packaging on IC design.

2014

30.7.2020

MARCH

20

Thursday

079-286 • WK 12

|    |    |    |    |    |    |    |
|----|----|----|----|----|----|----|
| 2  | 3  | 4  | 5  | 6  | 7  | 8  |
| 9  | 10 | 11 | 12 | 13 | 14 | 15 |
| 16 | 17 | 18 | 19 | 20 | 21 | 22 |
| 23 | 24 | 25 | 26 | 27 | 28 | 29 |
| S  | M  | T  | W  | T  | F  | S  |

- phase noise

- PLLs.

## TEXT BOOKS

1. design of analog CMOS integrated circuits.

2. CMOS analog circuit design.

3. analysis and design of analog integrated circuits.