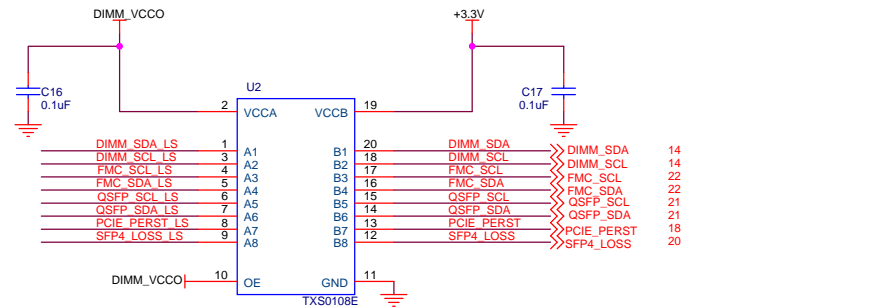
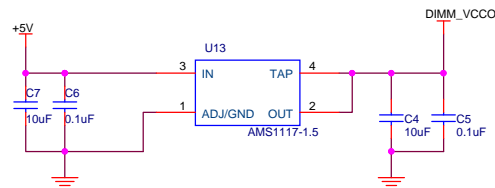
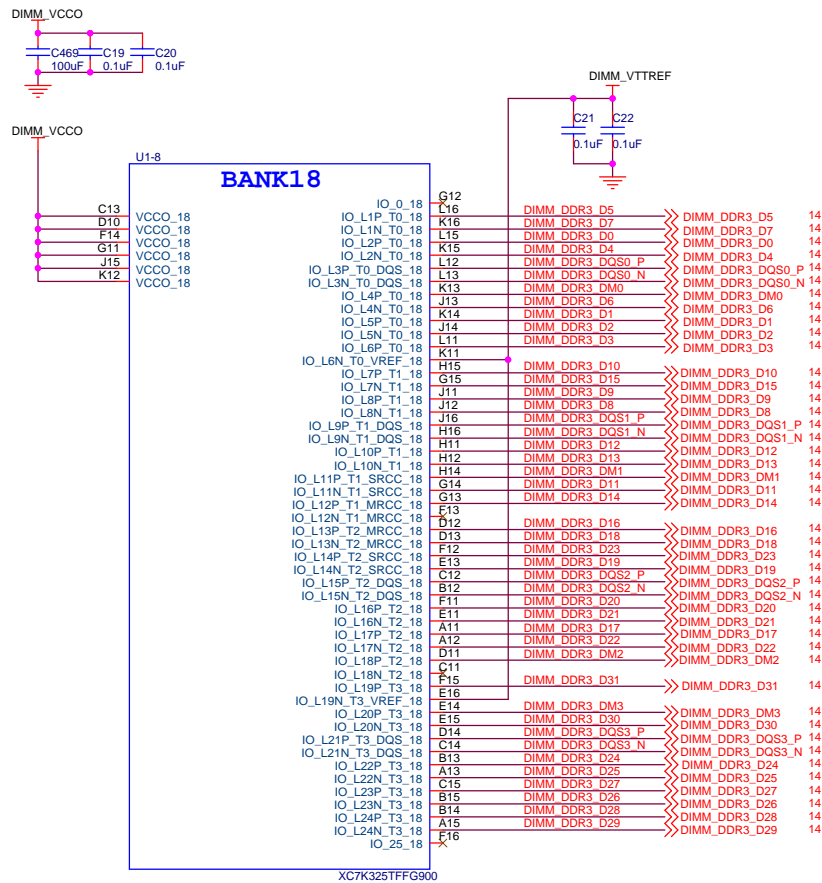
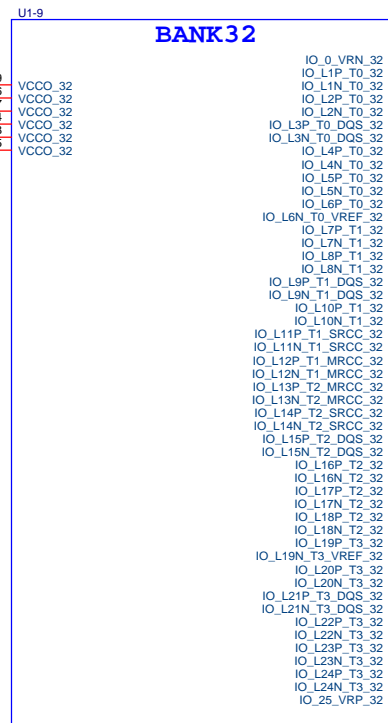
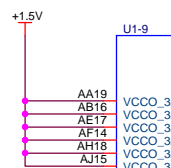
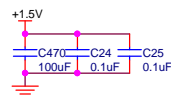


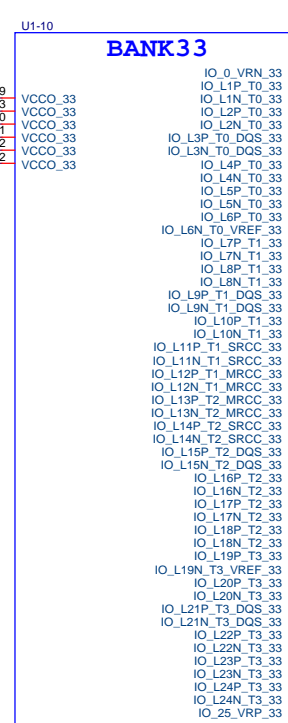
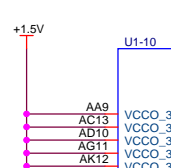
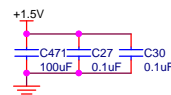
LDO POWER (Default 1.5V)



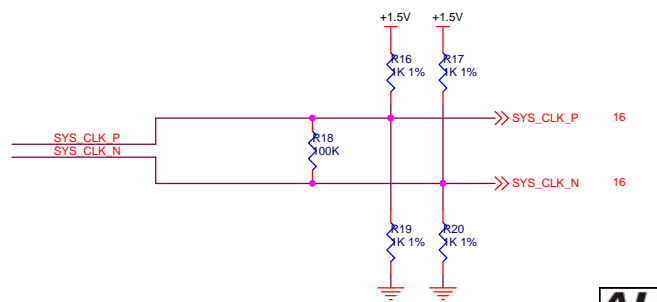


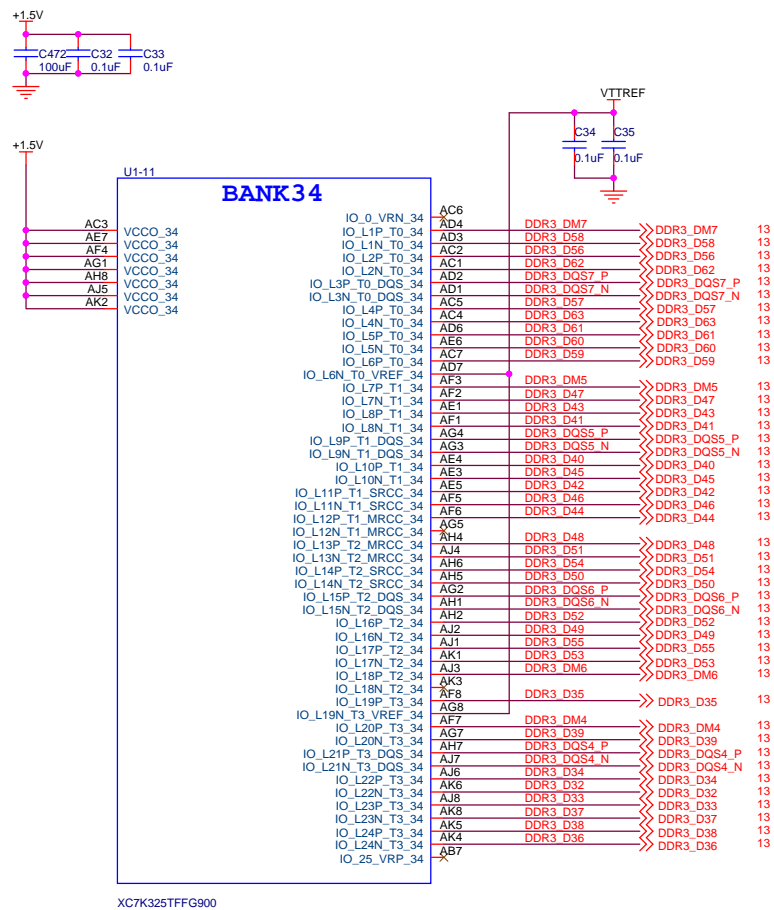


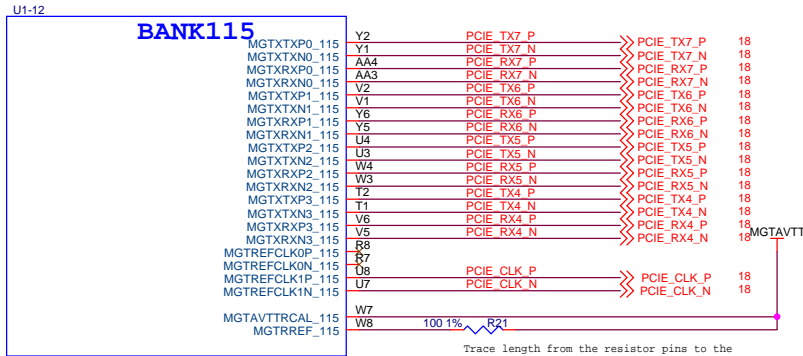
XC7K325TFFG900



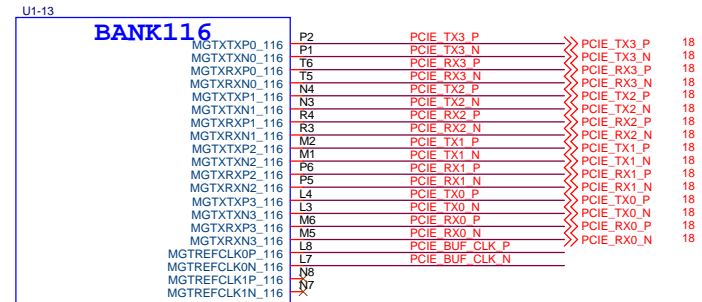
XC7K325TFFG900



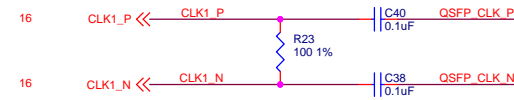
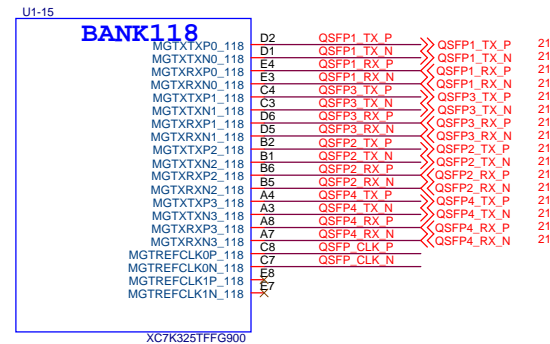
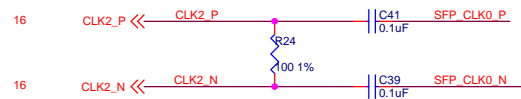
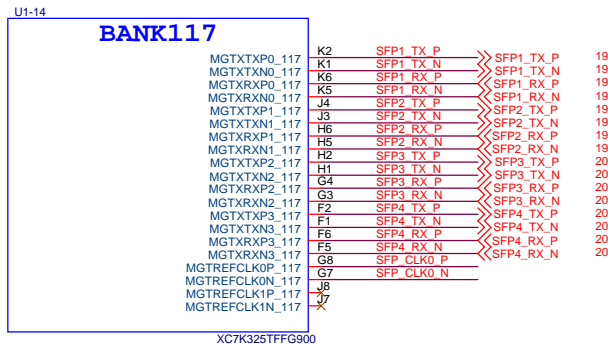
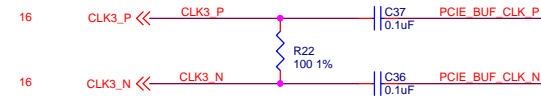


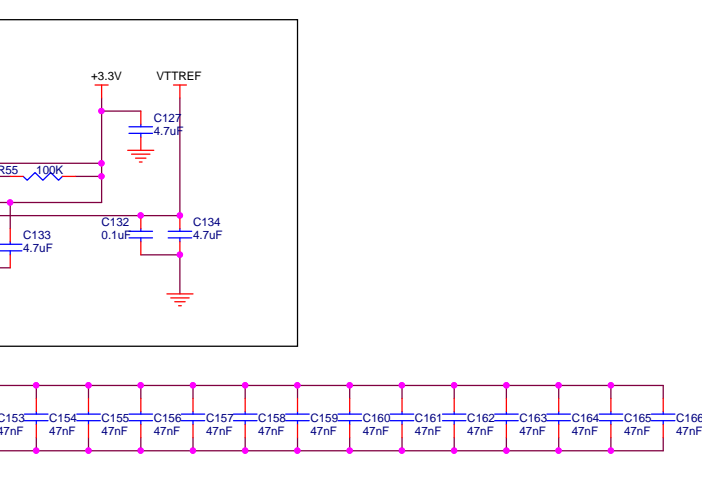
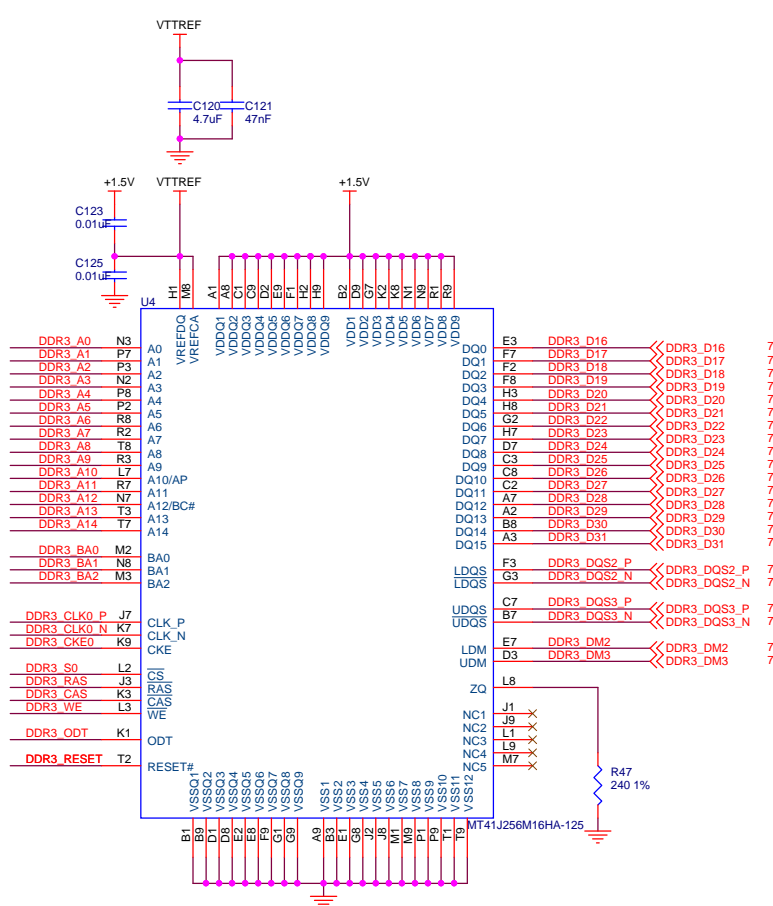
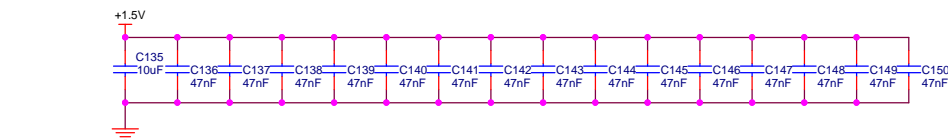
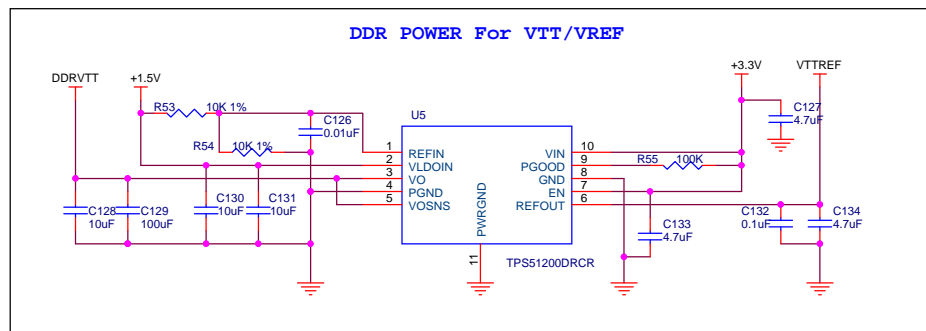
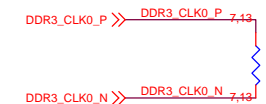
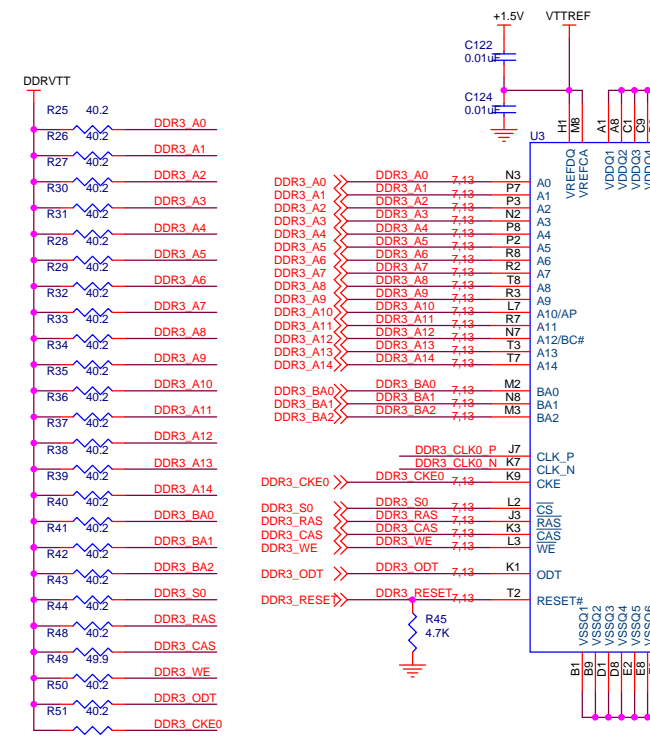
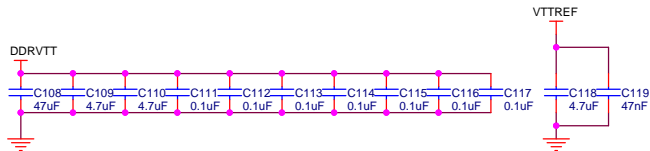


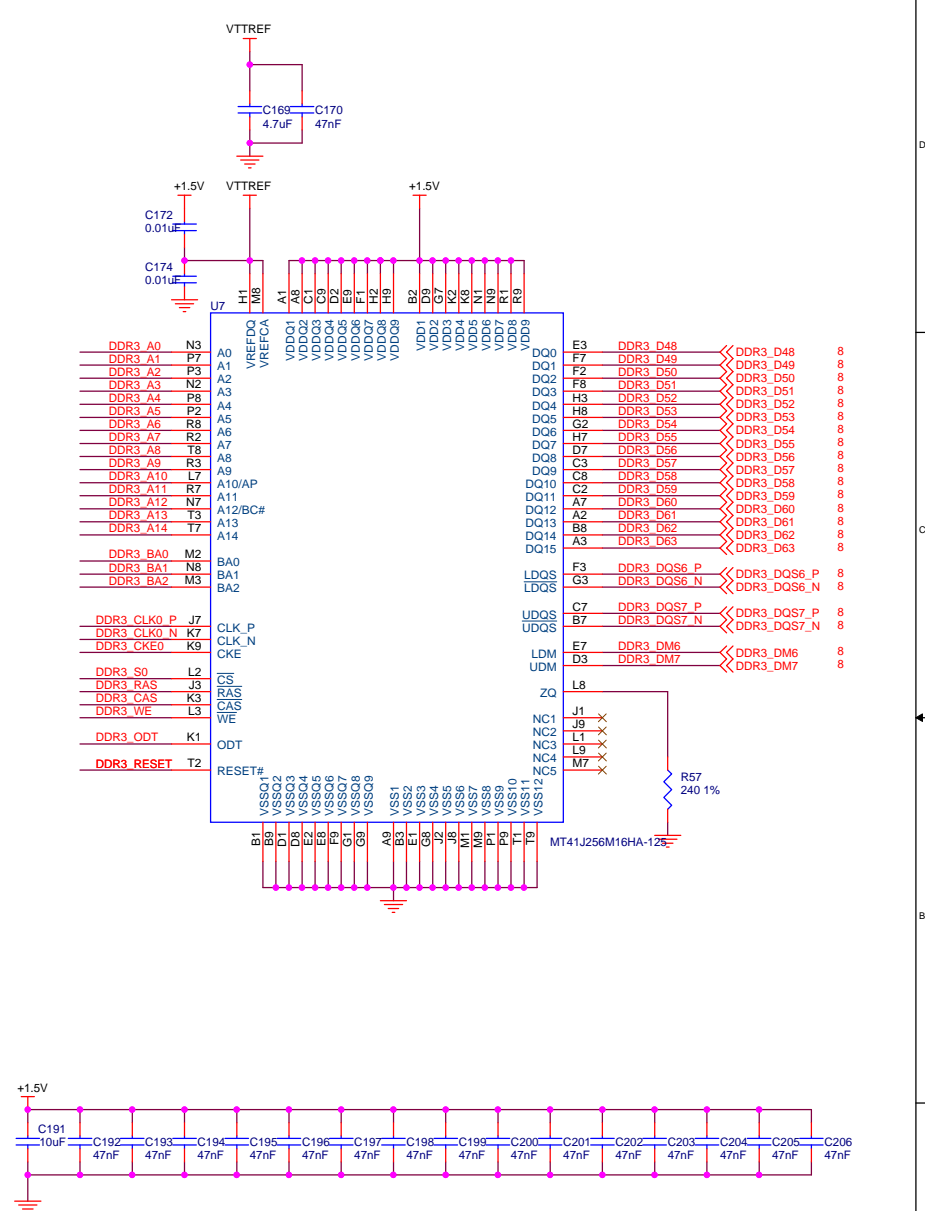
XC7K325TFFG900

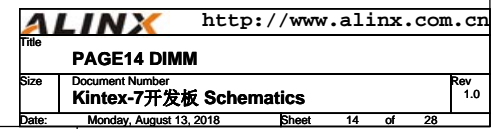
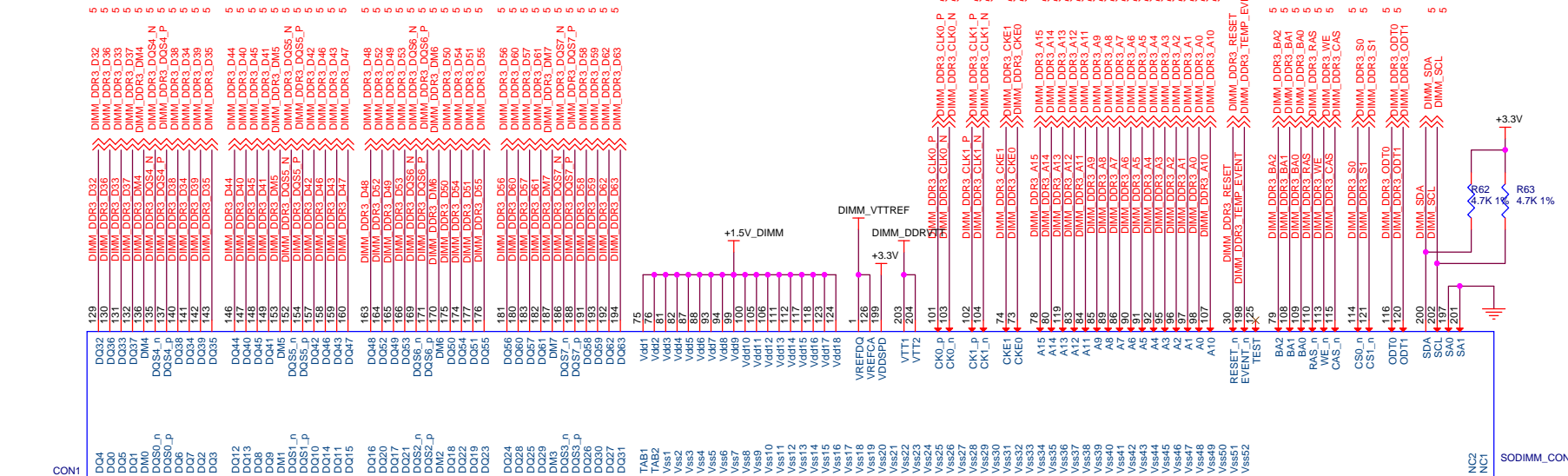


XC7K325TFFG900

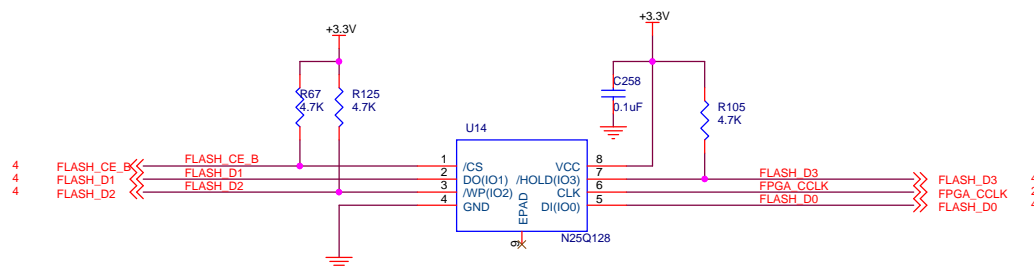






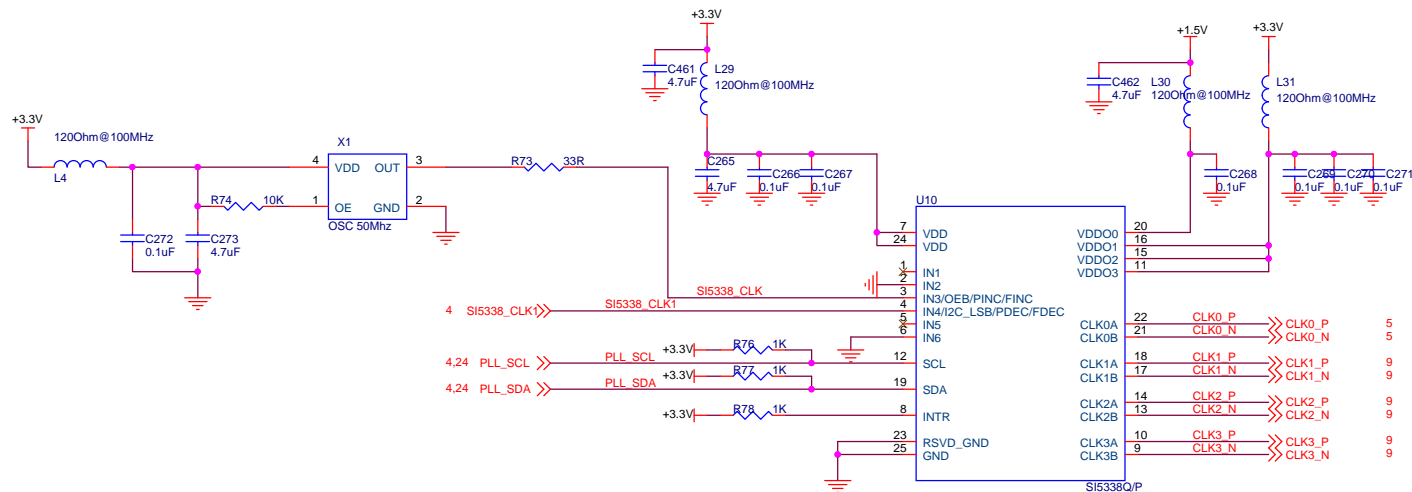
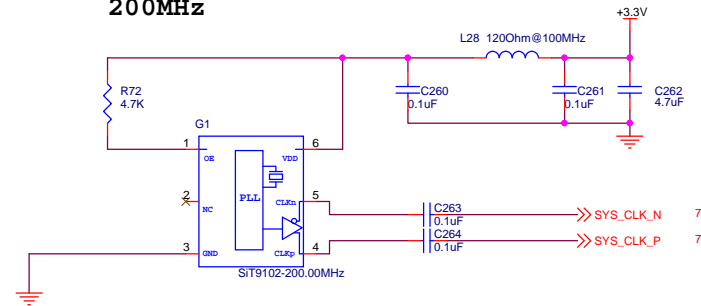


FPGA CONFIG SPI



SYSTEM CLOCK

200MHz



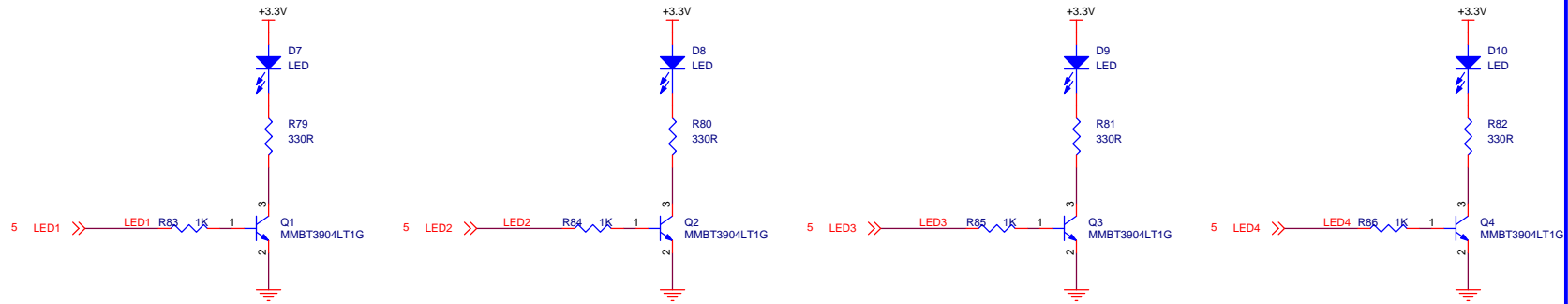
200Mhz SSTL/HSTL 1V5 For DDR3

156.25Mhz LVDS QSFP

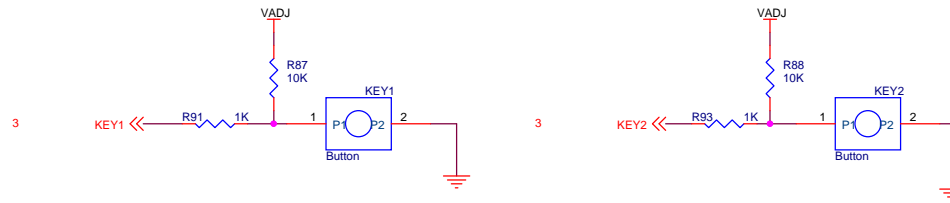
156.25Mhz LVDS For SFP

LVDS For PCIE

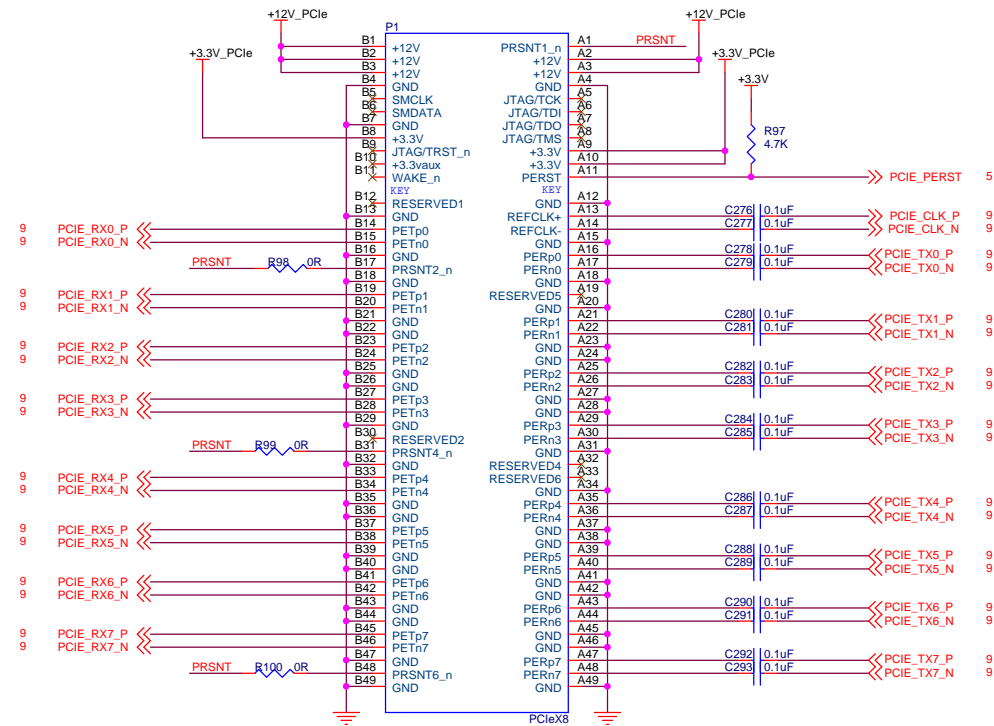
USER LED



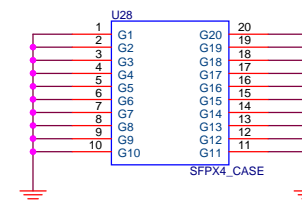
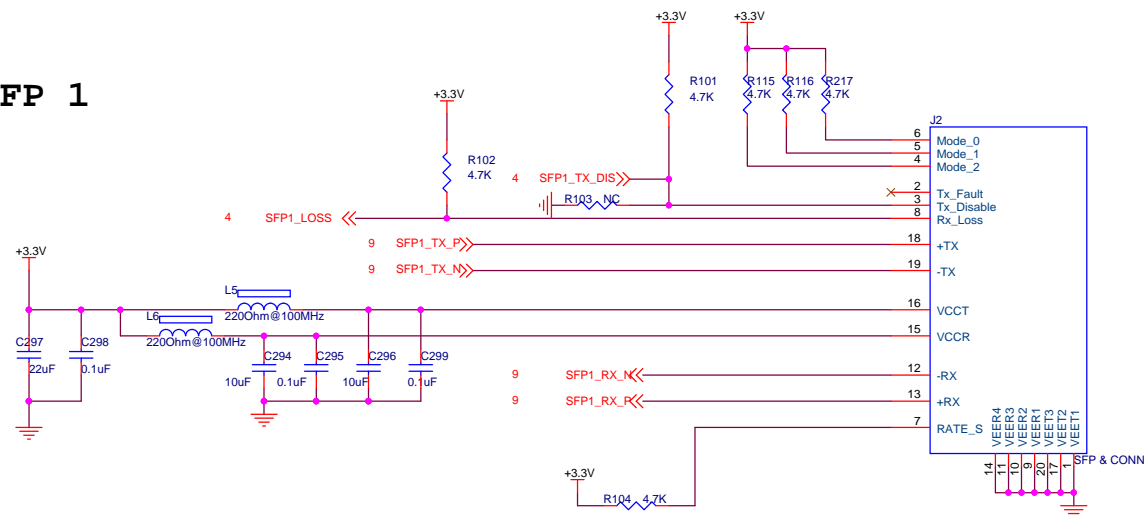
USER KEY



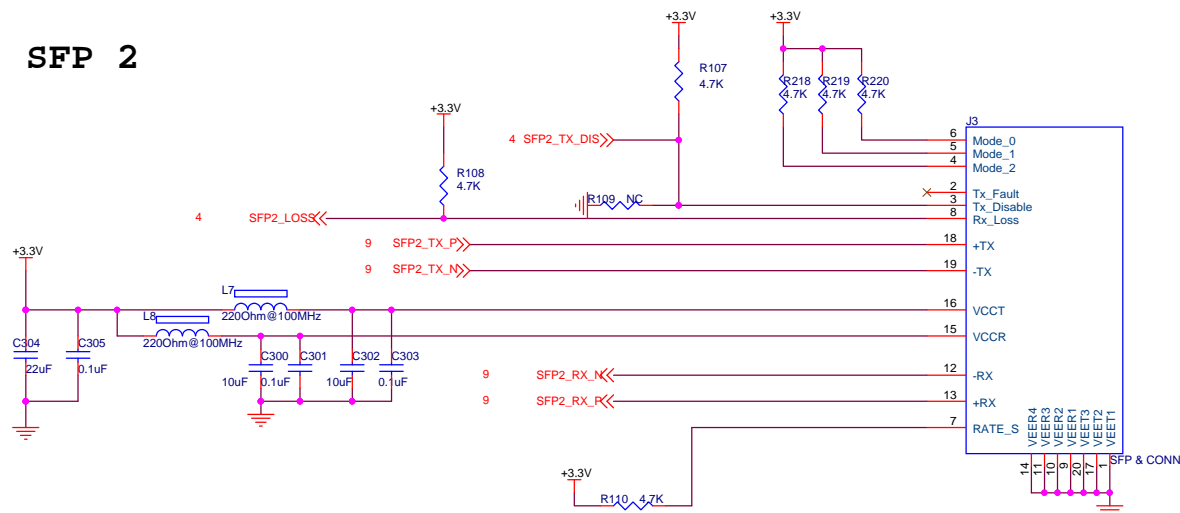
PCIE X8 SLOT



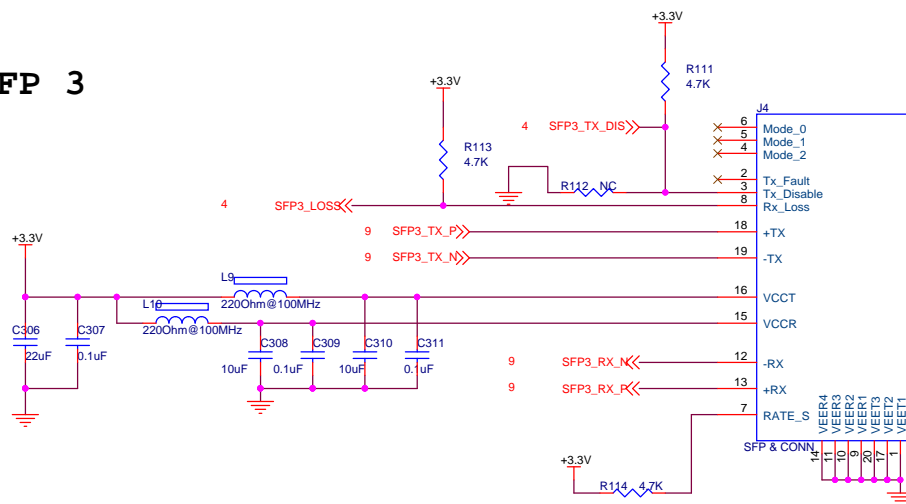
SFP 1



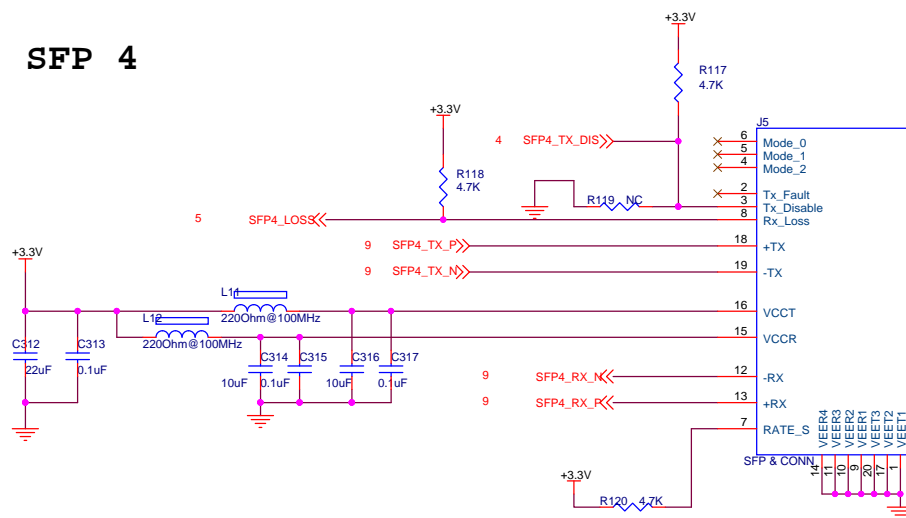
SFP 2

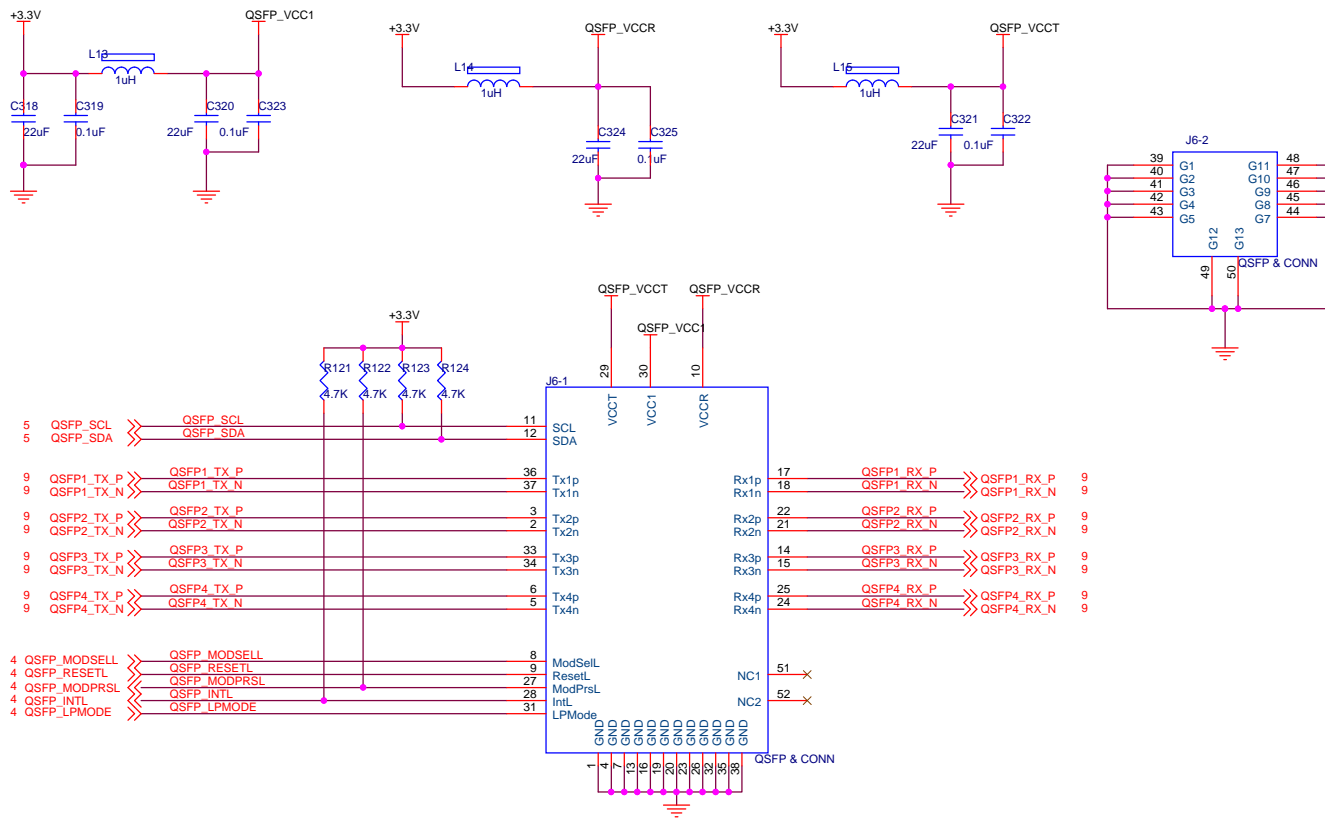


SFP 3



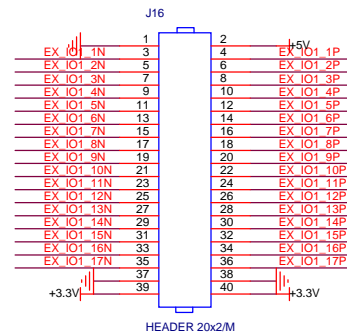
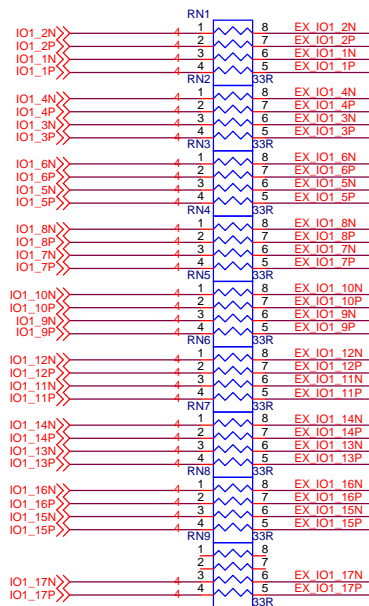
SFP 4

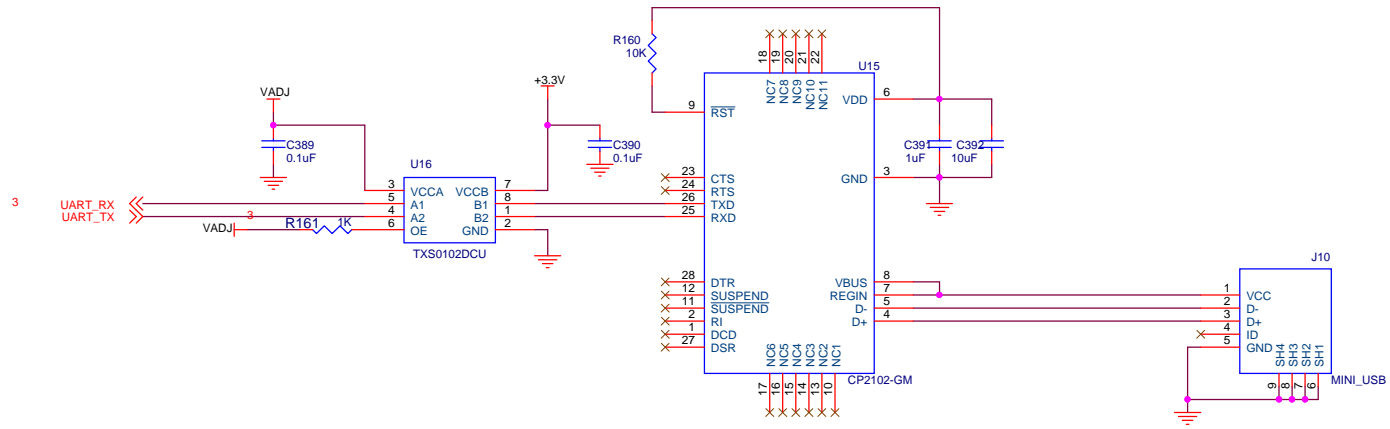




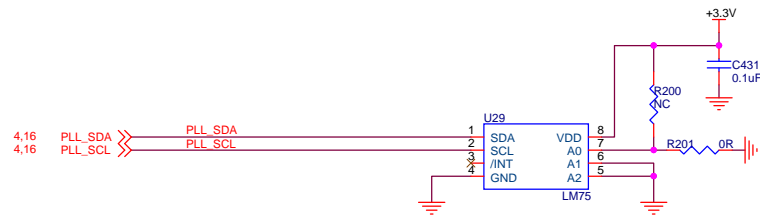
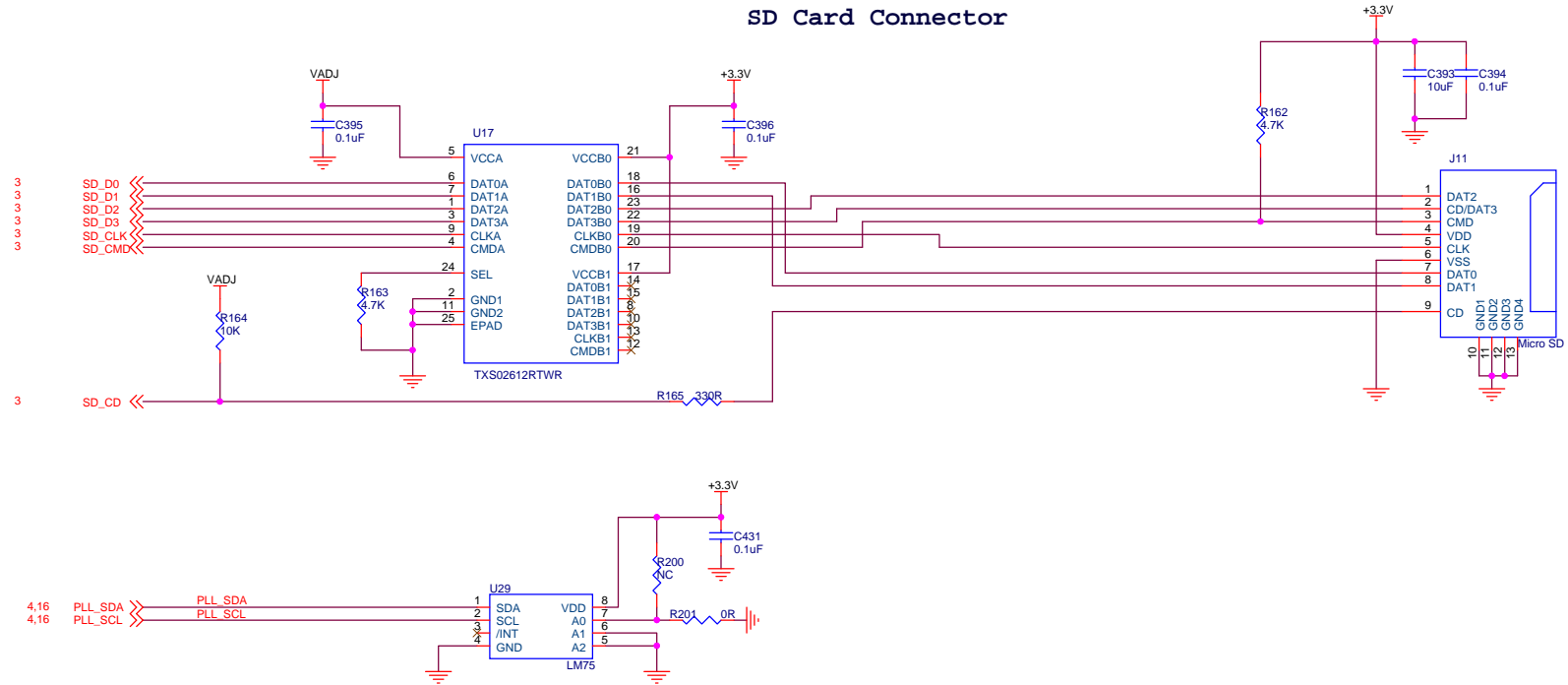
QSFP

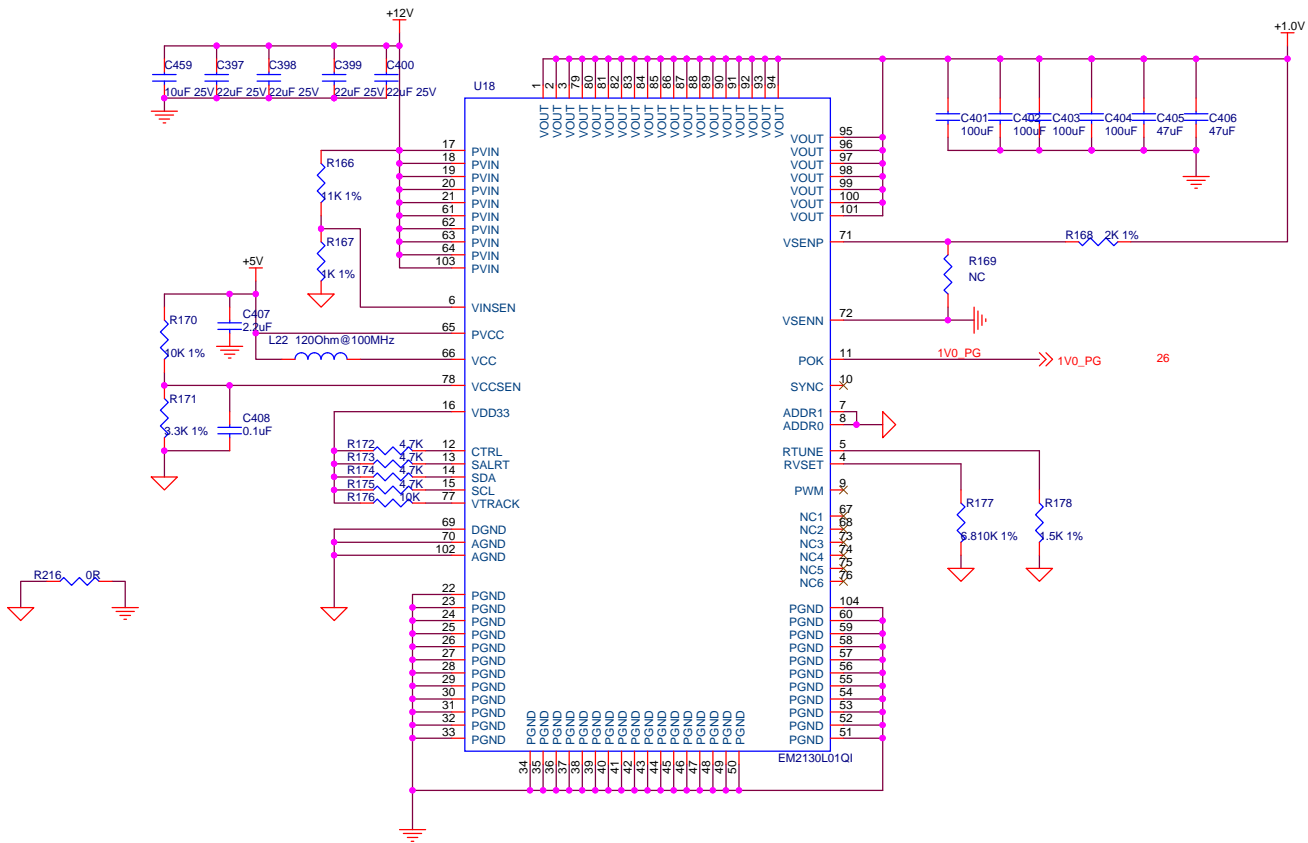
FPGA 40 PIN External IO

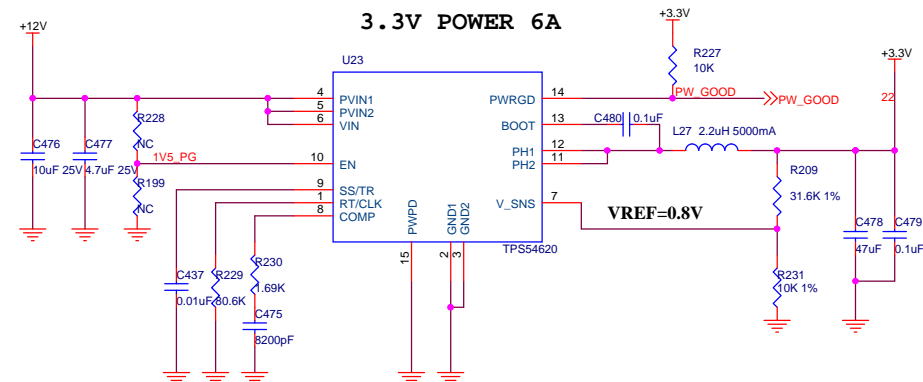
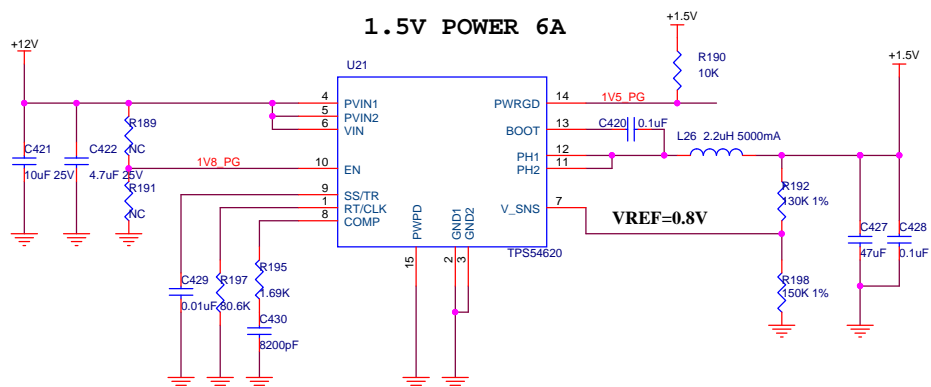
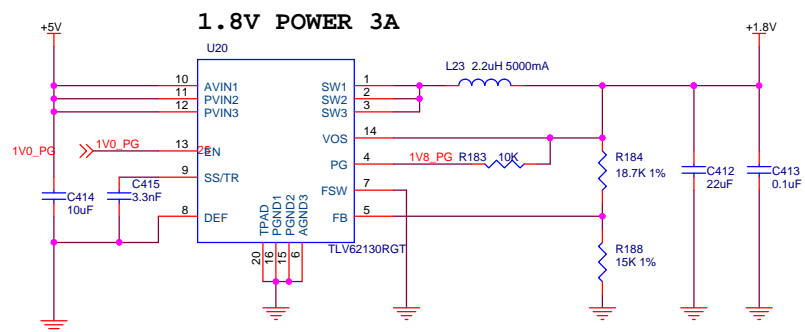
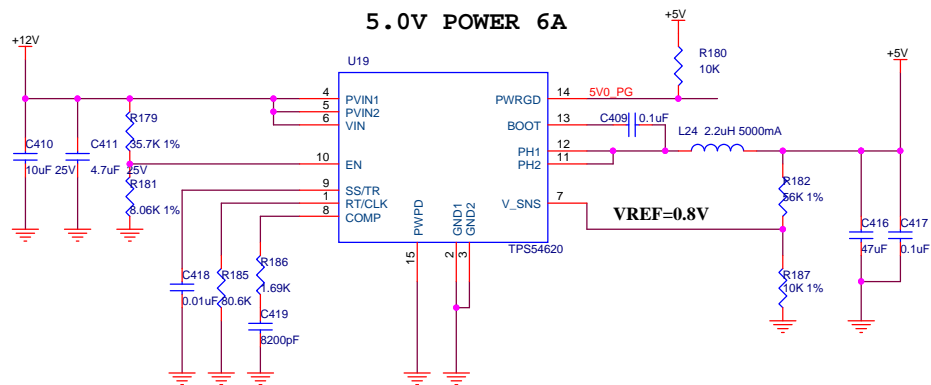




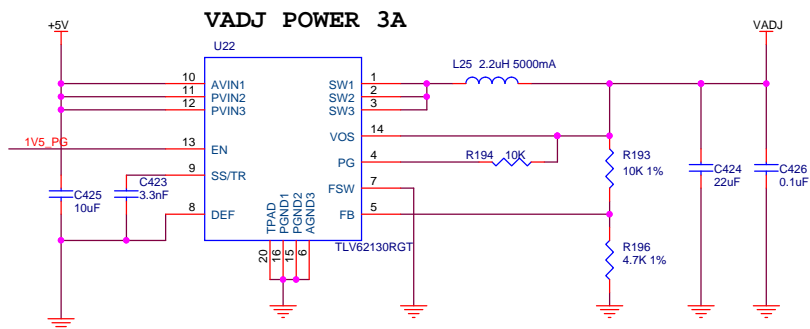
SD Card Connector





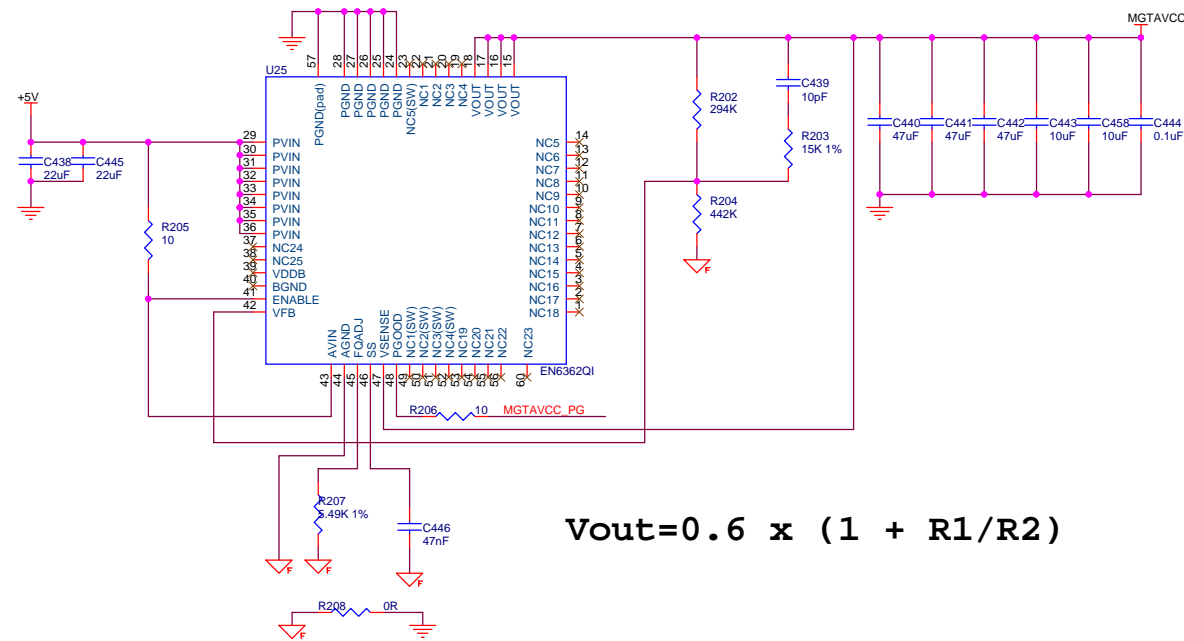


Default is 2.5V



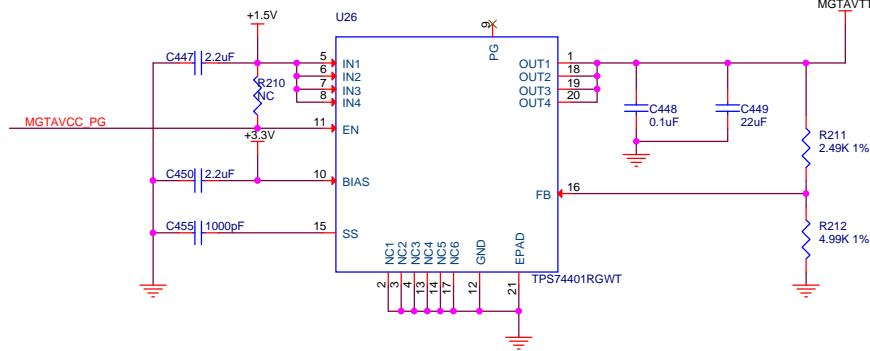
$$V_{out} = 0.8 \times (1 + R1/R2)$$

+1.0V 6A

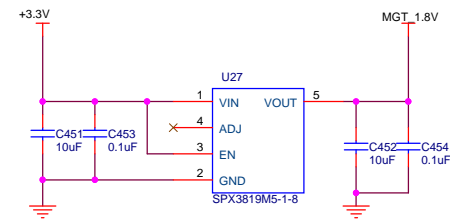


$$V_{out} = 0.6 \times (1 + R1/R2)$$

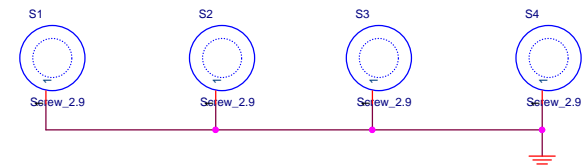
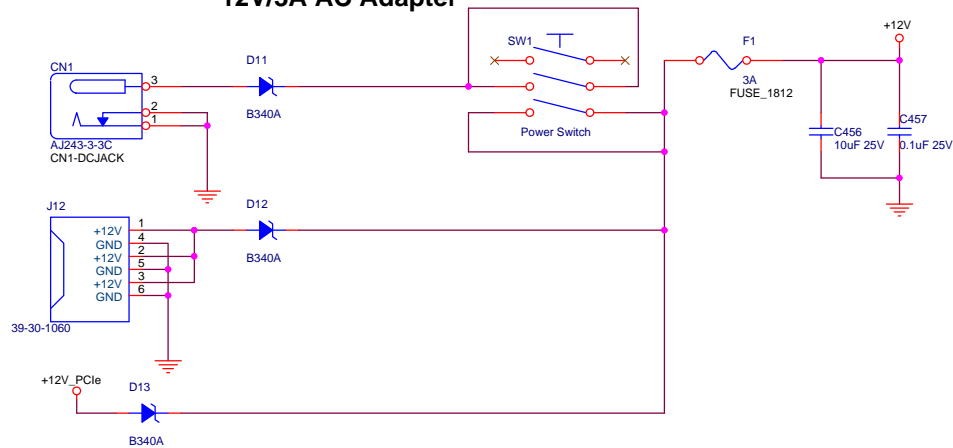
+1.2V 3A



$$V_{out} = 0.8 \times (1 + R1/R2)$$



12V/3A AC Adapter



FAN

