IMDADUL HOQ

2545 Linden Blvd, APT 4B Email: hoq.imdadul@gmail.com Brooklyn, NY 11208 Phone: 917-617-6034

Education

• Stony Brook University – Stony Brook, NY

[Jan 2011- Dec 2013]

B.E. in Computer Engineering

Dean's List: Spring 2011, Spring 2013, Fall 2013

• LaGuardia Community College – Long Island City, NY

[Feb 2008- Dec 2010]

Major: Mechanical Engineering Dean's List: Fall 2008, Spring 2009

Skills:

• Hardware: Embedded Systems, Digital and Analog Design

- Lab Skills: Soldering, Oscilloscopes, Multimeter, Bread-boarding
- Programming Languages: C, C++, Embedded C, Java, VHDL, SystemC, Basic SQL, Assembly
- Software: CADENCE, ORCAD, MS Visual Studio, AVR Studio, MS Office, HP ALM
- Language: Bengali, Hindi, Urdu
- Other: Excellent problem solving skills

Independent Projects:

• **HP Quality Center:** assigned myself as project admin to created new user, assign user to on their role. Also created project entity, project list, set auto mail, imported test requirement from excel spreadsheet, and created test step from test plan.

Work Experience:

Broadridge Financial Solutions Clerical assistant (Temp)

Jan 2014- Present

Currently working in logistic department to ensure companies outgoing mail are send to the United States Postal Service in good hand. Additional responsibility includes processing payment electronically to USPS.

LSI Computer Inc.

Test Engineer (Intern)

Jan 2012- Dec 2013

Assisted lead test engineers to maintain and operate various test bench components used in testing integrated circuits. Also collected, evaluated, and analyzed data for quality test to assure an error free product to client.

Stony Book University

Teaching Assistance (C/C++)

Sep 2012- Dec 2012

Assisted student with their programing, grading homework. Also, was responsible for creating and assigning final project assignments of that semester.

Projects:

- Senior Design Project: A GPS enabled embedded ISA system that alert driver based on driving speed. Project includes written program in embedded C on Atmega16 using Active-HDL. Also, communication between microcontroller and GPS modules.
- VHDL: Designed a FPGA based Multi-digit Multiplexed Seven Segment Display Driver.
- **Computer Architecture:** Created a 64-bit dual stage pipelined cell-unit processor with reduce set of instructions in VHDL programming language.
- **Java:** Designed and created a virtual store management system which records store inventory, customers information, sells and returns transaction including time and date.
- C++: Designed and implemented an object-oriented simulation of Texas Hold 'em game.
- C: Programmed a simulation of the cellular automation based 'Game of Life'
- **Digital Design:** Designed a synchronous 4 bit binary multiplier.

Publications:

Aamir S Jahan, Imdadul Hoq, David Westerfeld "GPS enabled speed control embedded system speed limiting device with display and engine control interface," *Systems, Applications and Technology Conference (LISAT)*, 2013 IEEE Long Island, vol., no., pp.1,7, 3-3 May 2013