

ILC and SaUCy: A Calculus for Composable, Computational Cryptography

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Abstract

The universal composability (UC) framework is the established standard for analyzing cryptographic protocols in a modular way, such that security is preserved under concurrent composition with arbitrary other protocols. However, although UC is widely used for on-paper proofs, prior attempts at systemizing it have fallen short, either by using a symbolic model (thereby ruling out computational reduction proofs), or by limiting its expressiveness.

In this paper, we lay the groundwork for building a concrete, executable implementation of the UC framework. Our main contribution is a process calculus, dubbed the Interactive Lambda Calculus (ILC). ILC faithfully captures the computational model underlying UC—interactive Turing machines (ITMs)—by adapting ITMs to a subset of the π -calculus through its type system. In other words, *well-typed ILC programs are expressible as ITMs*. In turn, ILC’s strong confluence property enables reasoning about cryptographic security reductions. We use ILC to develop our implementation of UC, called SaUCy. To demonstrate the use of SaUCy, we work through an instantiation of UC-secure commitments from the literature, define a composition operator and proving its associated theorem, as well as examine a subtle definitional issue involving race conditions.

1 Introduction

In cryptography, a proof of security in the simulation-based universal composability (UC) framework is considered the gold standard for demonstrating that a protocol “does its job securely” [12]. In particular, a UC-secure protocol enjoys the strongest notion of compositionality—it maintains all security properties even when run *concurrently* with arbitrary other protocol instances. This is in contrast with weaker property-based notions that only guarantee security in a standalone setting [30] or under sequential composition [20]. Thus, the benefit of using UC is modularity—it supports analyzing complex protocols by composing simpler building blocks. However, the cost of UC is that security proofs tend to be quite complicated. We believe that applying a PL-style of systemization to UC can help simplify its use, bring new clarity, and provide useful tooling. We envision a future where

modularity of cryptographic protocol composition translates to modular implementation as well.

Reviewing prior efforts of applying PL techniques to cryptography, we find they run up against challenges when importing the existing body of UC theory. Either they do not support computational reasoning (which considers issues of probability and computational complexity) [9], do not support message-passing concurrency for distributed protocols [4], or are too expressive (allow for expressing non-determinism with no computational interpretation) [2].

Our observation is that these approaches diverge from UC at a low level: UC is defined atop the underlying computational model of *interactive Turing machines* (ITMs). The significance of ITMs is that they have a clear computational interpretation, so it is straightforward to relate execution traces to a probabilistic polynomial time computation, as is necessary for cryptographic reduction proofs. The presence of (non-probabilistic) nondeterminism in alternative process calculi would frustrate such reduction proofs. ITMs avoid this issue by having a deterministic (modulo random coin tosses), single-threaded execution semantics. That is, processes pass control from one to another each time a message is sent so that *exactly one process is active at any given time*, and, moreover, this order of activations is fully determined.

In this paper, we take up the challenge of faithfully capturing this sequentiality by adapting ITMs to a subset of the π -calculus through a type discipline. We call this process calculus the Interactive Lambda Calculus (ILC), and we use it to build a concrete, executable implementation of the UC framework, dubbed SaUCy.

1.1 Interactive Lambda Calculus

Where have existing concurrency models fallen short with respect to ITMs? On the one hand, some existing process calculi, such as the standard π -calculus [34] or its cryptography-oriented variants [1, 2, 29], are not a good fit to ITMs, since they permit non-confluent reductions by design (i.e., non-probabilistic nondeterminism). On the other hand, the various calculi that do enjoy confluence are restrictive, only allowing for fixed or two-party communications [9, 19, 23].

ILC fills this gap by adapting ITMs to a subset of the π -calculus through its type system. In other words, *well-typed ILC programs are expressible as ITMs*. To give a preview of the type system, it restricts the dataflow of read channels (via

affine typing) and restricts the composition of write effects (forbidding write-mode parallel processes), with the net effect being confluence. Therefore, the only nondeterminism in an ILC program is due to random coin tosses taken by processes, which have a well-defined distribution. Additionally, any apparent concurrency hazards, such as adversarial scheduling of messages in an asynchronous network, are due to an explicit adversary process rather than uncertainty built into the model itself.

1.2 Contributions

To summarize, our main contributions are these:

- We design a foundational calculus for the purpose of systemizing UC called the Interactive Lambda Calculus, which exhibits confluence and is a faithful abstraction of ITMs.
- We use ILC to build a concrete, executable implementation of the UC framework called SaUCy.
- We then use SaUCy to port over a sampling of theory from UC literature, including a composition theorem, an instantiation proof of UC commitments [14], and an examination of a subtle definitional issue involving reentrant concurrency [11].

2 Overview

We first provide background on the universal composability framework and then give a tour of ILC.

2.1 Background on Universal Composability

Security proofs in the UC framework follow the real/ideal paradigm [20]. To carry out some cryptographic task in the real world, we define a distributed protocol that achieves the task across *many untrusted processes*. Then, to show that it is secure, we compare it with an idealized protocol in which processes simply rely on a *single trusted process* to carry out the task for them (and so security is satisfied trivially).

The program for this single trusted process is called an *ideal functionality* as it provides a uniform way to describe all the security properties we want from the protocol. Roughly speaking, we say a protocol π *realizes* an ideal functionality \mathcal{F} (i.e., it meets its specification) if every adversarial behavior in the real world can also be exhibited in the ideal world.

Once we have defined π and \mathcal{F} , proving realization formally follows a standard rhythm:

1. The first step is a construction: We must provide a *simulator* \mathcal{S} that translates any attack \mathcal{A} on the protocol π into an attack on \mathcal{F} .
2. The second step is a relational analysis: We must show that running π under attack by any adversary \mathcal{A} (the real world) is *indistinguishable* from running \mathcal{F} under attack by \mathcal{S} (the ideal world) to any distinguisher \mathcal{Z} called the *environment*.

As mentioned, the primary goal of this framework is *compositionality*. Suppose a protocol π is a protocol module that

realizes a functionality \mathcal{F} (a specification of the module), and suppose a protocol ρ , which relies on \mathcal{F} as a subroutine, in turn realizes an application specification functionality \mathcal{G} . Then, the composed protocol $\rho \circ \pi$, in which calls to \mathcal{F} are replaced by calls to π , also realizes \mathcal{G} . Instead of analyzing the composite protocol consisting of ρ and π , it suffices to analyze the security of ρ itself in the simpler world with \mathcal{F} , the idealized version of π .

2.2 ILC by Example

We make the above more concrete by running through an example of *commitment*, an essential building block in many cryptographic protocols [10]. The idea behind commitment is simple: A *committer* provides a *receiver* with the digital equivalent of a “sealed envelope” containing some value that can later be revealed. The commitment scheme must be *hiding* in the sense that the commitment itself reveals no information about the committed value, and *binding* in the sense that the committer can only open the commitment to a single value. For security under composition, an additional *non-malleability* property is required, which roughly prevents an attacker from using one commitment to derive another related one.

All of these properties are captured at once using an ideal functionality. In Figure 1 (left), we show \mathcal{F}_{COM} , an ideal functionality for one-time bit commitment, as it would appear in the cryptography literature [14]. The functionality simply waits for the committer P to commit to some bit b , notifies the receiver Q that it has taken place, and reveals b to Q upon request by P . Notice that Q never actually sees a commitment to b (only the (Receipt) message), so the three properties hold trivially. In Figure 1 (right), we define a simplified version of \mathcal{F}_{COM} in ILC to highlight some key features of the language, such as its affine and modal type system. Keep in mind that the overarching feature of ILC is confluence, so many of our design choices are for achieving this.

Affine types. Let us first examine the function signature of fCom , which (glossing over some details) takes as arguments two channels, a read channel frP (reads message from P) and a write channel toQ (writes messages to Q), and returns Unit . There are a number of things to unpack here:

- The signature consists of affine arrows \multimap (or “lollipops”), which describe the types of functions that consume their argument at most once.
- Both lollipops and intuitionistic (unrestricted) arrows carry the *modal type* (or *mode*) of their function bodies. A mode can have one of three values: either write W , read R , or value V . In the example, the left lollipop has mode V , which we elide, and the right lollipop has mode R . We have more to say about modes later.
- Read channels are affinely typed to protect them from duplication.

\mathcal{F}_{COM} proceeds as follows, running with parties P and Q .

1. Upon receiving a message (Commit, b) from P , where $b \in \{0, 1\}$, record the value b and send the message (Receipt) to Q . Ignore any subsequent Commit messages.
2. Upon receiving a message (Open) from P , proceed as follows: If some value b was previously recorded, then send the message (Open, b) to Q and halt. Otherwise, halt.

```
fCom :: Rd Msg -> !(Wr Msg) -> !Unit
let fCom frP !toQ =
  let <!(Commit b), frP> = rd frP in
  wr Receipt -> toQ;
  let <!(Open, frP)> = rd frP in
  !(wr (Opened b) -> toQ)
```

Figure 1. An ideal functionality for a one-time commitment scheme in prose (left) and in ILC (right).

- Write channels and the unit value are intuitionistically typed. Hence, their types require the $!$ operator (pronounced “bang!”) to be lifted into an affine type.

ILC achieves the above with a two-kind system. That is, types are bifurcated into a kind for intuitionistic types and a kind for affine types.

The main takeaway of the affine type system is this: It restricts the dataflow of read channels so that no confusion arises at the receiving ends of communications.

Modal types. ILC expressions are typed with one of three modes: W (write mode), R (read mode), or V (value mode). Modes can be composed either sequentially or in parallel to yield new modes.

To give the high-level idea, suppose expressions e_1 and e_2 have modes m_1 and m_2 , respectively. Sequential mode composition shows up in the obvious case of sequentially evaluating the two expressions, so the mode of $e_1 ; e_2$ is derived as $m_1 ; m_2 \Rightarrow m_3$.¹ Parallel mode composition shows in the case of forking a child process, so the mode of $e_1 \parallel e_2$ (fork a process e_1 and continue as e_2) is derived as $m_1 \parallel m_2 \Rightarrow m_3$. The rules for mode composition are discussed in Section 3.

The main takeaway of the modal type system is this: Parallel composition of write mode processes is *not allowed*, i.e., $W \parallel W \Rightarrow p$ is not derivable for any mode p . This ensures that no confusion arises as to which process is currently writing, or put another way, which process is currently active.

Putting it all together. The function body of $f\text{Com}$ closely follows \mathcal{F}_{COM} , but there are several points worth mentioning.

First, we introduce the letrd and wr typing rules. Typing judgments have the form $\Delta; \Gamma \vdash e : A \triangleright m$, where Δ is an affine typing context, Γ is an intuitionistic typing context, and m is a mode.

$$\frac{\Delta_1; \Gamma \vdash e_1 : \text{Rd } A \quad \Delta_2, x_1 : !A, x_2 : \text{Rd } A; \Gamma \vdash e_2 : B \triangleright m}{\Delta_1, \Delta_2; \Gamma \vdash \text{let}_{\pi} \langle x_1, x_2 \rangle = \text{rd}(e_1) \text{ in } e_2 : B \triangleright R} \text{letrd}$$

The letrd rule says that if we can partition the affine context Δ as Δ_1, Δ_2 such that e_1 has type $\text{Rd } A$ and mode V (elided) under contexts $\Delta_1; \Gamma$, and e_2 has type B and mode m under contexts $\Delta_2, x_1 : !A, x_2 : \text{Rd } A; \Gamma$, then the full expression has type B and mode R. Notice several things:

¹The expression $e_1 ; e_2$ is syntactic sugar for $\text{let}_{\pi} () = e_1 \text{ in } e_2$.

- Reading on a channel of type $\text{Rd } A$ produces an affine pair (or “tensor”) of type $!A \otimes \text{Rd } A$.
- ILC only allows intuitionistically typed values to be sent over channels (A ranges over intuitionistic types), so the value read on the channel is lifted into the first element of the tensor as an affine type using the bang! operator.
- The read channel itself is rebound as the second tensor element, so that it may be used again.
- Pattern matching with the $!$ operator unpacks affine values, so the value bound to b in the first read from frP has an intuitionistic type.

$$\frac{\Delta_1; \Gamma \vdash e_1 : A \quad \Delta_2; \Gamma \vdash e_2 : \text{Wr } A}{\Delta_1, \Delta_2; \Gamma \vdash \text{wr}(e_1, e_2) : \mathbb{1} \triangleright W} \text{wr}$$

The wr rule says that if we can partition the affine context Δ as Δ_1, Δ_2 such that e_1 has type A and mode V, and e_2 has a type $\text{Wr } A$ and mode V, then the full expression has type $\mathbb{1}$ and mode W.

Revisiting $f\text{Com}$, observe that the two letrd expressions are composed sequentially, so the mode of the entire function body is derived as $R ; R \Rightarrow R$ according to our mode composition rules. This is reflected in the fact that the right lollipop in the function signature carries a mode R. Finally, because wr expressions return $()$, we wrap it inside a bang! to lift its type into the affine type $!\text{Unit}$.

3 Interactive Lambda Calculus

We now present the Interactive Lambda Calculus in full, formalizing its syntax, static semantics, and dynamic semantics.

3.1 Syntax

The syntax of ILC given in Figure 2 consists of types, modes, and expressions.

Types are bifurcated into two kinds: one for intuitionistic types (written A, B) and one for affine types (written X, Y). The standard intuitionistic types include unit ($\mathbb{1}$), products ($A \times_{\pi} B$), sums ($A + B$), and references ($\text{Ref } A$). The more interesting ones are write channels ($\text{Wr } A$) and arrows ($A \rightarrow_m B$), which importantly carry a mode. Affine types include read channels ($\text{Rd } A$), bang! types ($!A$), tensors ($X \otimes Y$), and lollipops ($X \multimap_m Y$).

All types	$U, V ::= A \mid X$	Sendable types	$S ::= \mathbb{1} \mid A \times B \mid A + B \mid U \rightarrow_\ell V$
Intuitionistic types	$A, B ::= S \mid \text{Wr } S \mid \text{Ref } A$	Intuitionistic typings	$\Gamma ::= \cdot \mid \Gamma, x : A$
Affine types	$X, Y ::= \text{Rd } S \mid !A \mid X \otimes Y \mid X \oplus Y$	Affine typings	$\Delta ::= \cdot \mid \Delta, x : X \mid \Delta, \textcircled{w}$
Arrow labels	$\ell ::= \pi \mid w$	Multiplicity labels	$\pi ::= 1 \mid \infty$
Expressions	$e ::= x \mid () \mid (e_1, e_2)_\pi \mid \text{inj}_\pi^i(e) \mid \text{ref}(e) \mid \text{split}_\pi(e_1, x_1.x_2.e_2) \mid \text{case}_\pi(e, x_1.e_1, x_2.e_2)$ $\mid \text{get}(e) \mid \text{set}(e_1, e_2) \mid \text{fix}_\ell(x.e) \mid \text{let}_\pi x = e_1 \text{ in } e_2 \mid !e \mid \text{!} e \mid \lambda_\pi x. e \mid (e_1 e_2)_\ell \mid v(x_1, x_2). e \mid \text{wr}(e_1, e_2) \mid \text{rd}(e) \mid e_1 \mid\triangleright e_2 \mid e_1 \oplus e_2$		

Figure 2. Syntax of ILC.

Modes consist of W (write mode), R (read mode), and V (value mode). The rules for sequential and parallel composition of modes are given in Figure ??.

The intuitionistic portion of ILC supports a fairly standard feature set. The more interesting expressions are those related to affinity and message-passing concurrency. Bang! types have introduction form $!e$ and elimination form $\text{let } x = e_1 \text{ in } e_2$. Lollipops have introduction form $\lambda x. e$ and elimination form $\langle e_1 e_2 \rangle$. Tensors have introduction form $\langle e_1, e_2 \rangle$ and elimination form $\text{split}(e_1, x_1.x_2.e_2)$. The expressions for message-passing concurrency are these:

- *Restriction*: $v(x_1, x_2). e$ binds a read channel x_1 and a corresponding write channel x_2 in e .
- *Write*: $\text{wr}(e_1, e_2)$ sends the result of evaluating e_1 on the write channel result of evaluating e_2 .
- *Read*: $\text{let}_\pi \langle x_1, x_2 \rangle = \text{rd}(e_1)$ in e_2 reads a value from the read channel result of evaluating e_1 , binding the value as x_1 and the read channel as x_2 in e_2 .
- *Fork*: $e_1 \mid\triangleright e_2$ forks a child process e_1 and continues as e_2 .
- *Choice*: $e_1 \oplus e_2$ allows a process to continue as either e_1 or e_2 based on some initial read event in each of the processes.

3.2 Static Semantics

The type system of ILC is given in Figure 3. The intuitionistic typing judgment $\Delta; \Gamma \vdash e : A \triangleright m$ should be read as follows:

Under affine context Δ and intuitionistic context Γ , the expression e has intuitionistic type A and mode m .

(The affine typing judgment follows similarly.) Importantly, the type system maintains these invariants:

- *No duplication of read channel ends*. Each channel (or “tape” in ITM parlance) has a read end and a write end. The read end of the channel is protected against duplication by binding it in the affine context Δ . This ensures that no nondeterminism arises at the receiving end of communications.
- *No parallel composition of write mode processes*. The typing rules do not allow parallel composition of two write mode processes ($W \parallel W$). This ensures that no nondeterminism arises at the sending end of communications.

Before we go through a quick tour of the typing rules, we point out a few things: Value mode derivations and ordinary type polymorphism are omitted to avoid clutter, and algorithmic typing rules are given in the Appendix.

Rules for preventing read nondeterminism. To protect read channels from duplication, the nu rule binds read channels in the affine context. When reading from a channel, the letrd rule rebinds the channel in the affine context so that it may be used again. The rules for typing lambda abstractions (abs), fixed points (fix), and bang! types (bang) stipulate that they must be closed with respect to affine variables. Otherwise, they could violate the affinity of read channels.

Rules for preventing write nondeterminism. The mode composition rules given in Figure ?? prevent write mode processes from being composed in parallel. This is reflected in the fork rule, which derives the mode of $e_1 \mid\triangleright e_2$ as the parallel composition of the mode of e_1 and the mode of e_2 . To ensure this property is preserved during normalization, we must also forbid sequential composition of write mode processes.

3.3 Dynamic Semantics

Figures 4 and 5 define the dynamic syntax and semantics of ILC, respectively. We define a *configuration* C as a tuple of dynamic channel and process names Σ , a shared mutable store σ , and a pool of running and terminated processes π .

To state and prove the meta theory of ILC (Sec. 4), we extend the type system given above with typing rules for configurations, including typing environments for channels and stores. We omit these details here for space reasons, and we refer the interested reader to Sec. B.

We read the configuration reduction judgment $C_1 \longrightarrow C_2$ as “configuration C_1 steps to configuration C_2 ,” and the local stepping judgment $\sigma_1; e_1 \longrightarrow \sigma_2; e_2$ for a single process e as “under store σ_1 , expression e_1 steps to store σ_2 and expression e_2 . The rules of local stepping follow a standard call-by-value semantics, where we streamline the definition with an evaluation context E .

Configuration stepping consists of five rules, including a congruence rule congr that permits some of the other rules to be simpler, by making the order of the pool unimportant.

$\Delta_{in}; \Gamma \vdash e : U \dashv \Delta_{out}$ Under input contexts Δ_{in} and Γ , expression e has type U and output context Δ_{out} .

$$\begin{array}{c}
\frac{}{\Delta; \Gamma, x : A \vdash x : A \dashv \Delta} \text{ivar} \qquad \frac{}{\Delta, x : X; \Gamma \vdash x : X \dashv \Delta} \text{avar} \qquad \frac{}{\Delta; \Gamma \vdash () : \mathbb{1} \dashv \Delta} \text{unit} \\
\\
\frac{\Delta_1; \Gamma \vdash e_1 : U_1 \dashv \Delta_2 \quad \Delta_2; \Gamma \vdash e_2 : U_2 \dashv \Delta_3}{\Delta_1; \Gamma \vdash (e_1, e_2)_\pi : U_1 \times_\pi U_2 \dashv \Delta_3} \text{pair} \qquad \frac{i \in \{1, 2\} \quad \Delta_1; \Gamma \vdash e : U_i \dashv \Delta_2}{\Delta_1; \Gamma \vdash \text{inj}_\pi^i(e) : U_1 +_\pi U_2 \dashv \Delta_2} \text{inj} \qquad \frac{\Delta_1; \Gamma \vdash e : A \dashv \Delta_2}{\Delta_1; \Gamma \vdash \text{ref}(e) : \text{Ref } A \dashv \Delta_2} \text{ref} \\
\\
\frac{\Delta_1; \Gamma \vdash e_1 : U_1 \times_\pi U_2 \dashv \Delta_2 \quad [\Delta_2; \Gamma], x_1 : U_1, x_2 : U_2 \vdash e : V \dashv \Delta_3}{\Delta_1; \Gamma \vdash \text{split}_\pi(e_1, x_1.x_2.e_2) : V \dashv \Delta_3 \div (x_1 : U_1, x_2 : U_2)} \text{split} \\
\\
\frac{\Delta_1; \Gamma \vdash e : U_1 +_\pi U_2 \dashv \Delta_2 \quad [\Delta_2; \Gamma], x_1 : U_1 \vdash e_1 : V \dashv \Delta_3 \quad [\Delta_2; \Gamma], x_2 : U_2 \vdash e_2 : V \dashv \Delta_3}{\Delta_1; \Gamma \vdash \text{case}_\pi(e, x_1.e_1, x_2.e_2) : V \dashv \Delta_3 \div (x_1 : U_1, x_2 : U_2)} \text{case} \qquad \frac{\Delta_1; \Gamma \vdash e : \text{Ref } A \dashv \Delta_2}{\Delta_1, \mathbb{W}; \Gamma \vdash \text{get}(e) : A \dashv \Delta_2, \mathbb{W}} \text{get} \\
\\
\frac{\Delta_1; \Gamma \vdash e_1 : \text{Ref } A \dashv \Delta_2 \quad \Delta_2; \Gamma \vdash e_2 : A \dashv \Delta_3}{\Delta_1, \mathbb{W}; \Gamma \vdash \text{set}(e_1, e_2) : \mathbb{1} \dashv \Delta_3, \mathbb{W}} \text{set} \qquad \frac{\cdot; \Gamma, x : A \rightarrow U \vdash e : A \rightarrow U \dashv \cdot}{\Delta; \Gamma \vdash \text{fix}_\infty(x.e) : A \rightarrow U \dashv \Delta} \text{ifix} \\
\\
\frac{\Delta_1, x : X \multimap U; \Gamma \vdash e : X \multimap U \dashv \Delta_2}{\Delta_1; \Gamma \vdash \text{fix}_\infty(x.e) : X \multimap U \dashv \Delta_2 \div (x : X \multimap U)} \text{afix} \qquad \frac{\Delta_1; \Gamma \vdash e_1 : U_1 \dashv \Delta_2 \quad [\Delta_2; \Gamma], x : U \vdash e_2 : V_1 \dashv \Delta_3}{\Delta_1; \Gamma \vdash \text{let}_\pi x = e_1 \text{ in } e_2 : V \dashv \Delta_3 \div (x : U)} \text{let} \\
\\
\frac{\Delta_1; \Gamma \vdash e : A \dashv \Delta_2}{\Delta_1; \Gamma \vdash !e : !A \dashv \Delta_2} \text{bang} \qquad \frac{\Delta_1; \Gamma \vdash e : !A \dashv \Delta_2}{\Delta_1; \Gamma \vdash !e : !A \dashv \Delta_2} \text{unbang} \qquad \frac{\cdot; \Gamma, x : A \vdash e : U \dashv \cdot}{\Delta; \Gamma \vdash \lambda_{\infty x}. e : A \rightarrow U \dashv \Delta} \text{iabs} \\
\\
\frac{\Delta_1, x : X; \Gamma \vdash e : U \dashv \Delta_2}{\Delta_1; \Gamma \vdash \lambda_1 x. e : X \multimap U \dashv \Delta_2 \div (x : X)} \text{aabs} \qquad \frac{\mathbb{W}; \Gamma \vdash e : U \dashv \Delta_2}{\Delta_1; \Gamma \vdash \lambda_w(). e : \mathbb{1} \multimap U \dashv \Delta_1} \text{wabs} \qquad \frac{\Delta_1; \Gamma \vdash e_2 : U \dashv \Delta_2 \quad \Delta_2; \Gamma \vdash e_1 : U \rightarrow_\pi V \dashv \Delta_3}{\Delta_1; \Gamma \vdash (e_1 e_2)_\pi : V \dashv \Delta_3} \text{app} \\
\\
\frac{\Delta_1; \Gamma \vdash e_2 : \mathbb{1} \dashv \Delta_2 \quad \Delta_2; \Gamma \vdash e_1 : \mathbb{1} \multimap U \dashv \Delta_3}{\Delta_1, \mathbb{W}; \Gamma \vdash (e_1 e_2)_w : U \dashv \Delta_3} \text{wapp} \qquad \frac{\Delta_1, x_1 : \text{Rd } S; \Gamma, x_2 : \text{Wr } S \vdash e : A \dashv \Delta_2}{\Delta_1; \Gamma \vdash \nu(x_1, x_2). e : A \dashv \Delta_2} \text{nu} \\
\\
\frac{\Delta_1; \Gamma \vdash e_1 : S \dashv \Delta_2 \quad \Delta_2; \Gamma \vdash e_2 : \text{Wr } S \dashv \Delta_3}{\Delta_1, \mathbb{W}; \Gamma \vdash \text{wr}(e_1, e_2) : \mathbb{1} \dashv \Delta_3} \text{wr} \qquad \frac{\mathbb{W} \notin \Delta_1 \quad \Delta_1; \Gamma \vdash e : \text{Rd } S \dashv \Delta_2}{\Delta_1; \Gamma \vdash \text{rd}(e) : !S \otimes \text{Rd } S \dashv \Delta_2, \mathbb{W}} \text{rd} \qquad \frac{\Delta_1; \Gamma \vdash e_1 : U \dashv \Delta_2 \quad \Delta_2; \Gamma \vdash e_2 : V \dashv \Delta_3}{\Delta_1; \Gamma \vdash e_1 \mid \triangleright e_2 : V \dashv \Delta_3} \text{fork} \\
\\
\frac{\Delta_1; \Gamma \vdash e_1 : U \dashv \Delta_2 \quad \Delta_1; \Gamma \vdash e_2 : U \dashv \Delta_2}{\Delta_1; \Gamma \vdash e_1 \oplus e_2 : U \dashv \Delta_2} \text{choice}
\end{array}$$

Figure 3. Typing rules.

The other four rules consist of local stepping (via *local*), creating new processes (via *fork*), creating new channels (via *nu*), and read-write interactions (via *rw*). To avoid allocating the same name twice, the name set Σ records names of allocated channels and processes. Rule *rw* uses the syntax R for

a combination of several (mutually exclusive) read-mode expressions, with a chosen read filling the (single) hole. When synchronized, the rule uses this syntax to eliminate all of the unchosen read-mode expressions before control continues with the chosen read effect. The relation $c_1 \leadsto c_2$ holds when

Channel names	$c ::= \dots$		
Process names	$p, q ::= \dots$	Evaluation	$E ::= \bullet \mid \text{let}_\pi x = E \text{ in } e \mid \text{let! } x = E \text{ in } e$
Store locations	$\ell ::= \dots$	contexts	$\mid (E e)_\pi \mid (v E)_\pi \mid \text{ref}(E) \mid \text{get}(E)$
Name sets	$\Sigma ::= \varepsilon \mid \Sigma, c \mid \Sigma, p$		$\mid \text{set}(E, e) \mid \text{set}(\ell, E)$
Stores	$\sigma ::= \sigma \mid \sigma, \ell : v$		$\mid \text{split}_\pi(E, x_1.x_2.e) \mid \text{case}_\pi(E, x_1.e_1, x_2.e_2)$
Process pools	$\pi ::= \varepsilon \mid \pi, p : e$	Read contexts	$R ::= \bullet \mid e \oplus R \mid R \oplus e$
Configurations	$C ::= \langle \Sigma; \sigma; \pi \rangle$		

Figure 4. Channel names, process names, configurations and evaluation contexts.

$C_1 \equiv C_2$ Configurations C_1 and C_2 are equivalent.

$$\frac{\pi_1 \equiv_{\text{perm}} \pi_2}{\langle \Sigma; \sigma; \pi_1 \rangle \equiv \langle \Sigma; \sigma; \pi_2 \rangle} \text{permProcs}$$

$C_1 \longrightarrow C_2$ Configuration C_1 reduces to C_2 .

$$\begin{array}{c}
\frac{\sigma_1; e_1 \longrightarrow \sigma_2; e_2}{\langle \Sigma; \sigma_1; \pi, p : E[e_1] \rangle \longrightarrow \langle \Sigma; \sigma_2; \pi, p : E[e_2] \rangle} \text{local} \quad \frac{q \notin \Sigma}{\langle \Sigma; \sigma; \pi, p : E[e_1 \mid \triangleright e_2] \rangle \longrightarrow \langle \Sigma, q; \sigma; \pi, q : e_1, p : E[e_2] \rangle} \text{fork} \\
\frac{C_1 \equiv C'_1 \quad C'_1 \longrightarrow C'_2 \quad C'_2 \equiv C_2}{C_1 \longrightarrow C_2} \text{congr} \quad \frac{c_1, c_2 \notin \Sigma}{\langle \Sigma; \sigma; \pi, p : E[v(x_1, x_2). e] \rangle \longrightarrow \langle \Sigma, c_1, c_2; \sigma; \pi, p : E[[c_1/x_1][c_2/x_2]e] \rangle} \text{nu} \\
\frac{c_2 \rightsquigarrow c_1}{\langle \Sigma; \sigma; \pi, p : E_1[R[\text{rd}(c_1)]] , q : E_2[\text{wr}(c_2, v)] \rangle \longrightarrow \langle \Sigma; \sigma; \pi, p : E_1[(v, c_1)] , q : E_2[()] \rangle} \text{rw}
\end{array}$$

$\sigma_1; e_1 \longrightarrow \sigma_2; e_2$ Under store σ_1 , expression e_1 reduces to $\sigma_2; e_2$.

$$\begin{array}{c}
\frac{}{\sigma; \text{let}_\pi x = v \text{ in } e \longrightarrow \sigma; [v/x]e} \text{let} \quad \frac{}{\sigma; \text{let! } x = v \text{ in } e \longrightarrow \sigma; [v/x]e} \text{let!} \quad \frac{}{\sigma; ((\lambda x. e) v)_\pi \longrightarrow \sigma; [v/x]e} \text{app} \\
\frac{}{\sigma; \text{split}_\pi((v_1, v_2), x_1.x_2.e) \longrightarrow \sigma; [v_1/x_1][v_2/x_2]e} \text{split} \quad \frac{}{\sigma; \text{case}_\pi(\text{inj}_\pi^i(v), x_1.e_1, x_2.e_2) \longrightarrow \sigma; [v/x_i]e_i} \text{case} \\
\frac{}{\sigma; \text{fix}(x.e) \longrightarrow \sigma; [\text{fix}(x.e)/x]e} \text{fix} \quad \frac{\ell \notin \text{dom}(\sigma)}{\sigma; \text{ref}(v) \longrightarrow \sigma, \ell : v; \ell} \text{ref} \quad \frac{\sigma(\ell) = v}{\sigma; \text{get}(\ell) \longrightarrow \sigma; v} \text{get} \quad \frac{}{\sigma; \text{set}(\ell, v) \longrightarrow \sigma\{\ell := v\}; ()} \text{set}
\end{array}$$

Figure 5. Reduction semantics.

channel names c_1 and c_2 refer to the write and read ends of a common channel. (The rules abstract over how channel names, and this relation, are structured.)

4 ILC Metatheory

Intuitively, ILC's type system design enforces that a configuration's reduction consists of a unique (*deterministic*) sequence of reader-writer process pairings, and is *confluent* with any other reduction choice that exchanges the order of *other* (non-interactive) reduction steps. As explained in Sec. 3, ILC's type system does so by restricting the dataflow of read channels (via affine typing) and by restricting the

composition of write effects (forbidding write-mode parallel processes). The proofs of type soundness, whose statements we discuss next, establish the validity of these invariants. These language-level invariants support confluence theorems, also stated below. These theorems include *full confluence*: Any two full reductions of a configuration yield a pair of equivalent configurations (isomorphic, up to a renaming of non-deterministic name choices). For space reasons, we omit the full proof of confluence, but we include a detailed sketch in Sec. B.

4.1 Type Soundness

We prove type soundness of ILC via mostly-standard notions of progress and preservation. To state these theorems, we follow the usual recipe, except that we give a special definition of “program termination” which permits deadlocks. Informally, C **term** holds when:

1. C is fully normal: every process in C is normalized (consists of a value).
2. C is (at least partially) deadlocked: some (possibly-empty) portion of C is normal, and there exists one or more read-mode processes in C , or there exists one or more write-mode processes in C , however, no reader-writer pair exists for a common channel c .

Recall that ILC is concerned with enforcing *confluence* as its central meta theoretic property, *not* deadlock freedom. Confluence implies, among other things, that the order of reduction steps is inconsequential, and that no process scheduling choices will affect the final outcome.

Lemma 4.1 (Non-progress). *For all configurations C , store typings Ω , channel typings Ψ , and process typings Φ , if $\Omega; \Psi \vdash C : \Phi$ and C **term**, then $\nexists C'$ such that $C \longrightarrow C'$.*

Theorem 4.2 (Progress). *For all configurations C , store typings Ω , channel typings Ψ , and process typings Φ , if $\Omega; \Psi \vdash C : \Phi$ then either C **term**, or $\exists C'$ such that $C \longrightarrow C'$.*

Theorem 4.3 (Preservation). *For all configurations C, C' , store typings Ω , channel typings Ψ and process typings Φ , if $\Omega; \Psi \vdash C : \Phi$ and $C \longrightarrow C'$ then there exists store typing extension $\Omega' \supseteq \Omega$ channel typing extension $\Psi' \supseteq \Psi$ and process typing extension $\Phi' \supseteq \Phi$ such that $\Omega'; \Psi' \vdash C' : \Phi'$.*

4.2 Confluence

ILC’s type system enforces confluence up to non-deterministic naming choices in rules *ref*, *nu*, and *fork* (Fig. 5). To account for different choices of dynamically-named store locations, channels and processes, respectively, we state and prove confluence with respect to a renaming function f , which consistently renames these choices in a related configuration:

Theorem 4.4 (Single-step confluence). *For all well-typed configurations C , if $C \longrightarrow C_1$ and $C \longrightarrow C_2$ then there exists renaming function f such that either:*

1. $C_1 = f(C_2)$, or
2. $\exists C_3$ such that $C_1 \longrightarrow C_3$ and $f(C_2) \longrightarrow C_3$.

Intuitively, the sister configuration C_2 is either different because of a name choice (case 1), or a different process scheduling choice (case 2). In either case, there exists a renaming of any choice made to reach C_2 , captured by function f . By composing multiple uses of this theorem, and the renaming functions that they construct, we prove a multi-step notion of confluence that reduces a single configuration C to two equivalent terminal configurations, C_1 and C_2 :

Theorem 4.5 (Full confluence). *For all well-typed configurations C , if $C \longrightarrow^* C_1$ and $C \longrightarrow^* C_2$ and C_1 **term** and C_2 **term** then there exists renaming function f such that $C_1 = f(C_2)$.*

5 Implementation

We have implemented an ILC interpreter in Haskell, which at present consists of 2.3K source lines of code. We plan to open source our implementations of ILC and SaUCy.

For economy of use, our implementation performs polymorphic type inference, bounded polymorphic mode inference, and affinity inference. Polymorphism on modes is bounded precisely due to our restriction on parallel write mode composition. Moreover, a consequence of any kind of mode polymorphism at all is that the modes of higher order functions can be dependent on the modes of its function arguments. We give a taste of this below.

This first example features full mode polymorphism and no dependent modes.

```
loop :: ∀ a b m . Rd a → !(a →m b) →R b
letrec loop c !f = let (lv, c) = rd c in f v; loop c !f
```

The *loop* function takes as arguments a read channel c and an intuitionistic function wrapped in a bang!, which is unpacked as f . It says to first read from the channel c , unpack the read value as v , apply the function f to v , and then recursively call *loop* again. Here, the mode m carried over the function argument is fully polymorphic, since no parallel compositions take place, and the function’s mode R is monomorphic.

This next example features bounded mode polymorphism and dependent modes.

```
par :: ∀ a b c {(m, n) | m || n ⇒ p} .
      (a →m b) → (a →n c) → a →p c
let par f g v = f v |> g v
```

The *par* function takes as arguments two functions f and g and a third argument v . It says to compute $f v$ and $g v$ in parallel. Here, because write mode expressions cannot be composed in parallel, at most one of the modes carried over the function arguments can have mode W . Additionally, the mode p carried over the rightmost arrow is dependent on the modes of the function arguments.

6 SaUCy

Using ILC, we build a concrete, executable implementation of the UC framework, dubbed SaUCy. Then, we demonstrate the versatility of SaUCy in three ways:

1. We define a protocol composition operator and its associated composition theorem.
2. We walk through an instantiation of UC commitments.
3. We use ILC’s type system to reason about “reentrancy,” a subtle definitional issue in UC that has only recently been studied.

6.1 Probabilistic Polynomial Time in ILC

The goal of cryptography reduction is to relate every bad event in a protocol to a *probabilistic polynomial time computation* that solves a hard problem. The ILC typing rules do not guarantee termination, let alone polynomial time normalization, so we must tackle this in metatheory. Also, since ILC is effectively deterministic (confluent), we will need to express random choices some other way. To meet these needs we define a judgment about ILC terms that take a security parameter and a stream of random bits.

Definition 6.1 (Polynomial time normalization). The judgment that e is polynomial time normalizable, written $\text{PPT } e$, is defined as follows:

$$\frac{\cdot ; \cdot \vdash e : !\text{Nat} \multimap ![\text{Bit}] \multimap_m !\text{Bit} \quad \forall k \in \text{Nat}. \forall r \in [\text{Bit}]^{(\text{poly}(k))}. e !k !r \rightarrow^{\text{poly}(k)} v}{\text{PPT } e} \text{ ppt}$$

This says that if for all security parameters k and all bitstrings r with length polynomial in k the term $e !k !r$ normalizes to a value v in $\text{poly}(k)$ steps.²

Definition 6.2 (Value Distribution). Because processes are confluent, we know that if $e !k !r \rightarrow^* v$ then the value v is unique. We can therefore define the probability distribution ensemble $D(e) = \{D_{e,k}\}_k$ based on a uniform distribution U_k over k -bit strings r , so the distribution $D_{e,k}$ is given as

$$D_{e,k}(v) = \sum_{r \in R} U_k(r), \quad \text{for } R = \{r \mid e !k !r \rightarrow^* v\}.$$

Definition 6.3 (Indistinguishability). What remains is to define a notion of indistinguishability for value distributions. However, we need to clarify when polynomial time normalization is an assumption or a proof obligation. To simplify things later, we define a partial order $e_1 \leq e_2$, which captures that e_2 must be PPT if e_1 is PPT, and if so, that their value distributions are statistically similar.

$$\frac{\text{PPT } e_1 \implies (\text{PPT } e_2 \text{ and } D(e_1) \sim D(e_2))}{e_1 \leq e_2} \text{ indist}$$

6.2 The UC Framework, Concretely

The implementation of SaUCy is centered around a definition of UC execution model in ILC. For the most part, this just involves connecting channels as illustrated in Figure 6. For demonstration, we only show the case of two-party protocols (à la Simplified UC [13]), which will suffice for our example of instantiating universally composable commitments. Also, we will only aim to show the case of *static* corruptions, in which parties are corrupted at the onset of the execution.

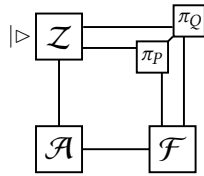


Figure 6. execUC .

²Note that the normalization must be polynomial time for all r , though we could have defined it to hold with high probability.

This is in contrast to *adaptive* corruptions, in which parties can be corrupted as the execution proceeds.

$\text{execUC} :: \forall \dots . X_z \multimap X_p \otimes X_q \multimap X_f \multimap X_a$
 $\multimap !\text{Nat} \multimap ![\text{Bit}] \multimap !\text{Crupt} \multimap_m !\text{Bit}$

let $\text{execUC } z(p, q) f a k !r \text{ crupt} =$
 $v \dots \text{let } \dots \text{ in}$
 $\text{let } (rf, ra, rp, rq, rz) = \text{splitBits5 } r \text{ in}$
 $f !\text{crupt } \dots k !rf$
 $\triangleright a !\text{crupt } \dots k !ra$
 $\triangleright \text{wrapperP } p \text{ crupt } \dots k !rp$
 $\triangleright \text{wrapperQ } q \text{ crupt } \dots k !rq$
 $\triangleright z \dots k !rz$

The function execUC is parameterized by an environment z , protocol parties p and q , an adversary a , an ideal functionality f , a security parameter k , a random bitstring r , and a static corruption model $\text{crupt} :: !\text{Crupt}$. The Crupt datatype is defined below, with its variants denoting the cases when party p is corrupt, party q is corrupt, or no party is corrupt, respectively.

$\text{data Crupt} = \text{CruptP} \mid \text{CruptQ} \mid \text{CruptNone}$

Some careful programming is needed to handle the cases when the adversary sends messages on behalf of corrupted parties; this is captured by $\text{wrapper}\{P, Q\}$ (shown in the Appendix).

Note that for space and readability, we elide channel allocation/distribution and wrapper definitions (with ellipses). We also abbreviate the type signature (e.g., X_z is the type of z). More details can be found in the Appendix.

6.3 Defining UC Security in ILC

The central security definition in UC is protocol emulation. The guiding principle is that π emulates ϕ if the environment cannot distinguish between the two protocols. Our first attempt is the following, where \mathcal{S} is the simulator that translates every attack in the real world into an attack expressed in the ideal world:

$$\frac{\forall \mathcal{Z}. \text{execUC } \mathcal{Z} \pi \mathcal{F}_1 \mathbb{1}_{\mathcal{A}} \leq \text{execUC } \mathcal{Z} \phi \mathcal{F}_2 \mathcal{S}}{\mathcal{S} \vdash (\pi, \mathcal{F}_1) \approx (\phi, \mathcal{F}_2)} \text{ emulate}$$

To remark on a few notation choices: we make the functionality explicit, so emulation is a relationship between protocol-functionality pairs. Here, $\mathbb{1}_{\mathcal{A}}$ is the dummy adversary, which just relays messages between the environment and the parties/functionality. We elide the standard dummy lemma that shows this is without loss of generality; the intuition is that whatever an adversary can do, the environment can achieve using $\mathbb{1}_{\mathcal{A}}$.

Unfortunately this simple definition turns out to be vacuous: a degenerate protocol π can emulate anything simply failing to be PPT, e.g. by diverging. To put it another way, the problem is the definition imposes a proof obligation on the simulator \mathcal{S} but not on π . What we want to say is that the *real*

world protocol (π, \mathcal{F}_1) must be well behaved whenever the *ideal world* (ϕ, \mathcal{F}_2) is. However, even a reasonable protocol can result in non-PPT executions if paired with a divergent environment. In fact, giving a precise but composable notion of polynomial-time for interactive processes has been an ongoing challenge in UC. In GNUC, the approach is to define a well behaved environment, independently of its execution context—roughly that the total size of its outgoing messages is bounded by a polynomial, and that its running time is bounded if its total received input size is bounded [21]. This notion is composable as desired, although its use requires additional distinctions between “invited” and “uninvited” messages, which seems cluttered. RSIM makes analogous choices [3]. While we think these could be equally applied to ILC, our goal here is to provide a simpler notion.

We define protocol emulation by requiring a simulation in both directions, so every behavior in the ideal world must correspond to a behavior in the real world and vice versa.

Definition 6.4 (Protocol Emulation). The judgment that one protocol-functionality pair (π, \mathcal{F}_1) securely emulates another (ϕ, \mathcal{F}_2) (as proven the simulators $\mathcal{S}_R, \mathcal{S}_I$) is defined as

$$\frac{\forall \mathcal{Z}. \text{execUC } \mathcal{Z} \phi \mathcal{F}_2 \mathbb{1}_{\mathcal{A}} \leq \text{execUC } \mathcal{Z} \pi \mathcal{F}_1 \mathcal{S}_R \quad \text{execUC } \mathcal{Z} \pi \mathcal{F}_1 \mathbb{1}_{\mathcal{A}} \leq \text{execUC } \mathcal{Z} \phi \mathcal{F}_2 \mathcal{S}_I}{\mathcal{S}_R, \mathcal{S}_I \vdash (\pi, \mathcal{F}_1) \approx (\phi, \mathcal{F}_2)} \text{emulate}$$

We remark this definition goes against the UC convention of requiring simulation in one direction only. One direction is preferable intuitively because it should be OK if the protocol is even more secure than its specification. Requiring both could be too restrictive, although we have not encountered such problem in our examples. In any case, the benefit is this simplifies the polynomial time notion: vacuous protocols are clearly ruled out by the top condition, and both simulations are only required to be PPT if \mathcal{Z} is.

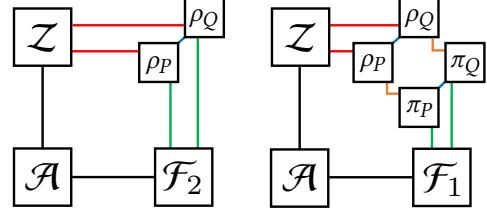
6.4 A Composition Theorem in SaUCy

As a first demonstration of SaUCy, we work through the development of a composition operator, and give a theorem explaining its use.

Definition 6.5 (UC realizes). To set out, we introduce the notation of “realizes,” which views a protocol as a way of instantiating a specification functionality \mathcal{F}_2 from a setup assumption functionality \mathcal{F}_1 ,

$$\frac{(\pi, \mathcal{F}_1) \approx (\text{id}_\pi, \mathcal{F}_2)}{\mathcal{F}_1 \xrightarrow{\pi} \mathcal{F}_2} \text{realizes}$$

where id_π is the *dummy protocol*, which simply relays messages between the environment and the functionality. This notation is convenient because it suggests a categorical approach to composition.



$$\begin{aligned} \text{let } (\circ) (\rho_P, \rho_Q) (\pi_P, \pi_Q) \\ & \quad \text{w}_{\rho_P} 2Z \text{ r}_{Z2\rho_P} \text{ w}_{\rho_Q} 2Z \text{ r}_{Z2\rho_Q} \\ & \quad \text{w}_{\rho_P} 2F \text{ r}_{F2\rho_P} \text{ w}_{\rho_Q} 2F \text{ r}_{F2\rho_Q} \\ & \quad \text{w}_{\rho_P} 2\rho_Q \text{ r}_{\rho_Q 2\rho_P} \text{ w}_{\rho_Q} 2\rho_P \text{ r}_{\rho_P 2\rho_Q} = \\ & \quad \nu \dots \pi_P \text{ w}_{\pi_P} 2\rho_P \text{ r}_{\rho_P 2\pi_P} \text{ w}_{\rho_P} 2F \text{ r}_{F2\rho_P} \text{ w}_{\pi_P} 2\pi_Q \text{ r}_{\pi_P 2\pi_Q} \\ & \quad \mid \triangleright \pi_Q \text{ w}_{\pi_Q} 2\rho_Q \text{ r}_{\rho_Q 2\pi_Q} \text{ w}_{\rho_Q} 2F \text{ r}_{F2\rho_Q} \text{ w}_{\pi_Q} 2\pi_P \text{ r}_{\pi_P 2\pi_Q} \\ & \quad \mid \triangleright \rho_P \text{ w}_{\rho_P} 2Z \text{ r}_{Z2\rho_P} \text{ w}_{\rho_P} 2\pi_P \text{ r}_{\pi_P 2\rho_P} \text{ w}_{\rho_P} 2\rho_Q \text{ r}_{\rho_Q 2\rho_P} \\ & \quad \mid \triangleright \rho_Q \text{ w}_{\rho_Q} 2Z \text{ r}_{Z2\rho_Q} \text{ w}_{\rho_Q} 2\pi_Q \text{ r}_{\pi_Q 2\rho_Q} \text{ w}_{\rho_Q} 2\rho_P \text{ r}_{\rho_P 2\rho_Q} \end{aligned}$$

Figure 7. Protocol composition operator.

Theorem 6.1 (Composition Theorem).

$$\frac{\mathcal{F}_1 \xrightarrow{\pi} \mathcal{F}_2 \quad \mathcal{F}_2 \xrightarrow{\rho} \mathcal{F}_3}{\mathcal{F}_1 \xrightarrow{\rho \circ \pi} \mathcal{F}_3}$$

The idea is that the $\rho \circ \pi$ can be defined in a natural way, where the ideal functionality channel of ρ is connected to the environment channel of π , as illustrated and defined in Figure 7.

Proof. To prove the theorem we construct the simulators $\mathcal{S}_{R,\rho} \circ \mathcal{S}_{R,\pi}$ (respectively $\mathcal{S}_{I,\rho} \circ \mathcal{S}_{I,\pi}$) in the natural way as well (given in the Appendix). Our proof obligation is to introduce an arbitrary environment \mathcal{Z} and conclude

$$\text{execUC } \mathcal{Z} (\rho \circ \pi) \mathcal{F}_1 \mathbb{1}_{\mathcal{A}} \leq \text{execUC } \mathcal{Z} \mathbb{1}_\pi \mathcal{F}_3 (\mathcal{S}_{I,\rho} \circ \mathcal{S}_{I,\pi})$$

The main idea is to notice that that we can bring ρ from the composed protocol into the environment as $(\mathcal{Z} \circ \rho)$, reflecting the fact that the environment is meant to represent arbitrary outer protocols. The following derivation completes the proof:

$$\begin{aligned} & \text{execUC } \mathcal{Z} (\rho \circ \pi) \mathcal{F}_1 \mathbb{1}_{\mathcal{A}} \\ &= \text{execUC } (\mathcal{Z} \circ \rho) \pi \mathcal{F}_1 \mathbb{1}_{\mathcal{A}} \quad (\text{By inspection}) \\ &\leq \text{execUC } (\mathcal{Z} \circ \rho) \text{id}_\pi \mathcal{F}_2 \mathcal{S}_{I,\pi} \quad (\text{From } \mathcal{F}_1 \xrightarrow{\pi} \mathcal{F}_2) \\ &= \text{execUC } (\mathcal{S}_{I,\pi} \circ \mathcal{Z}) \rho \mathcal{F}_2 \mathbb{1}_{\mathcal{A}} \quad (\text{By inspection}) \\ &\leq \text{execUC } (\mathcal{S}_{I,\pi} \circ \mathcal{Z}) \text{id}_\pi \mathcal{F}_3 \mathcal{S}_{I,\rho} \quad (\text{From } \mathcal{F}_2 \xrightarrow{\rho} \mathcal{F}_3) \\ &= \text{execUC } \mathcal{Z} \text{id}_\pi \mathcal{F}_3 (\mathcal{S}_{I,\pi} \circ \mathcal{S}_{I,\rho}) \quad (\text{By inspection}) \end{aligned}$$

The remaining case for $\mathcal{S}_{R,\rho} \circ \mathcal{S}_{R,\pi}$ is symmetric. \square

Other notions of composition. Our composition operator above is just a starting point. The “universal composition” [12] operator essentially multiplexes sessions identified by unique tags (*session ids*), while a joint state composition theorem collapses multiple subroutines into one [15]. Despite its name,

development in UC often involves defining additional composition operators. For example, interesting composition often happens “in the functionality” through higher order “wrapper” functionalities [22, 24] which we would express through abstraction. Some security properties require a generalized notion of ideal functionality that the environment can interact with directly. All the above motivate the development of the ILC core calculus as a flexible foundation; developing them in ILC is important future work.

6.5 Instantiating UC Commitments

We now walk through an instantiation of UC commitments (à la Canetti and Fischlin [14]). Instantiation proofs in SaUCy follow a standard rhythm. We start with a security definition as an ideal functionality (such as \mathcal{F}_{COM}), give the protocol, construct a simulator, and finally complete the relational analysis on paper.

UC commitments can be instantiated with standard cryptographic assumptions, for example the RSA problem [30]. They also rely on a “trusted setup,” or common reference string, which are essentially public parameters generated ahead of time (modeled as an ideal functionality \mathcal{F}_{CRS}).

Extending ILC with cryptographic primitives. UC Commitments are realized from cryptographic primitives, such as trapdoor permutations, which require extensions to ILC. The new syntactic forms are given with their static and dynamic semantics in Figure 10.

Commitment Protocol. The commitment protocol by Canetti and Fischlin [14] is implemented in ILC as follows:

```

committer ::  $\forall \dots \cdot ! \text{Wr Msg} \dots ! \text{Nat} \multimap ! [\text{Bit}] \multimap_{\mathcal{R}} ! \mathbb{I}$ 
let committer toZ frZ !toF frF !toQ frQ !k !bits =
  let  $\langle !(\text{Commit } b), \text{frZ} \rangle = \text{rd frZ in}$ 
    wr GetCRS  $\rightarrow \text{toF}$  ;
    let  $\langle !(\text{PublicStrings } \sigma \text{ pk}_0 \text{ pk}_1), \text{frF} \rangle = \text{rd frF in}$ 
    let r = take k bits in
    let x = if b == 0 then prg pk0 r
      else xors (prg pk1 r)  $\sigma$  in
    wr Commit' x  $\rightarrow \text{toQ}$  ;
    let  $\langle !(\text{Open}, \text{frZ}) \rangle = \text{rd frZ in}$ 
       $!(\text{wr (Open' } b \text{ r)} \rightarrow \text{toQ})$ 

receiver ::  $\forall \dots \cdot ! \text{Wr Msg} \dots ! \text{Nat} \multimap ! [\text{Bit}] \multimap_{\mathcal{R}} ! \mathbb{I}$ 
let receiver !toZ frZ toF frF toP frP k bits =
  let  $\langle !(\text{Commit' } x), \text{frP} \rangle = \text{rd frP in}$ 
    wr GetCRS  $\rightarrow \text{toF}$  ;
    let  $\langle !(\text{PublicStrings } \sigma \text{ pk}_0 \text{ pk}_1), \text{frF} \rangle = \text{rd frF in}$ 
    wr Receipt  $\rightarrow \text{toZ}$  ;
    let  $\langle !(\text{Open' } b \text{ r}), \text{frP} \rangle = \text{rd frP in}$ 
      if (b == 0 && x == prg pk0 r) ||
        (b == 1 && x == xors (prg pk1 r)  $\sigma$ )

```

```

then  $!(\text{wr (Opened } b) \rightarrow \text{toZ})$ 
else error "Cannot occur in honest case."

```

To briefly summarize what is going on: The setup CRS functionality (given in the Appendix) samples a random string σ and two trapdoor pseudorandom generators (prgs pk_0, pk_1). To commit to b , the committer produces a string y that is the result of applying one or the other of the prgs, and if $b = 1$ additionally applying xor with σ . The intuitive explanation why this is hiding is that without the trapdoor, it is difficult to tell whether a random $4k$ -bit string is in the range of either prg. To open the commitment, the committer simply reveals the preimage and the receiver checks which of the two cases applies. The intuitive explanation why this is binding is that it is difficult to find a pair $y, y \oplus \sigma$ that are respectively in the range of both prgs.

Defining the simulator. The UC proof consists of two simulators, one for the ideal world and one for the real world. The ideal world simulator is ported directly from the UC literature [14]. The non-standard real world simulator, given in the Appendix, is trivial, but necessary because our protocol emulation definition requires simulation in both directions.

The ideal world simulator generates its own “fake” CRS for which it stores the trapdoors. The string σ is not truly random, but instead is the result of combining two evaluations of the prgs. In Figure 8 we show the case that the committer P is corrupt (the other case is in the Appendix). The simulator is activated when \mathcal{Z} sends a message (Commit' y); in the real world, this is relayed by the dummy adversary to Q, who outputs Committed back to the environment. Hence to achieve the same effect in the ideal world, the simulator must send (Commit b) to \mathcal{F}_{COM} . To extract b from y , the simulator makes use of the prg trapdoor check which one has y in its range. It is necessary to argue by cryptographic reduction that this simulation is sound.

Relational argument. The goal of the relational analysis is to show that an environment’s output in the real world is indistinguishable from its output in the ideal world. The proof follows the one in Canetti and Fischlin [14].

Proof Sketch. Consider the following ensembles:

$$\begin{aligned}
 D_{\mathcal{R}} &= D(\text{execUC } \mathcal{Z} \text{ (committer, receiver) fCrs dummyA}) \\
 D'_{\mathcal{R}} &= D(\text{execUC } \mathcal{Z} \text{ (committer, receiver) bCrs dummyA}) \\
 D_{\mathcal{I}} &= D(\text{execUC } \mathcal{Z} \text{ (dummyP, dummyQ) fCom simI})
 \end{aligned}$$

The ensemble $D_{\mathcal{R}}$ is over the output of \mathcal{Z} in a real world execution. The ensemble $D'_{\mathcal{R}}$ is similar, except \mathcal{Z} runs with a bad functionality that computes fake public strings in the same way that the simulator does. The ensemble $D_{\mathcal{I}}$ is over the output of \mathcal{Z} in an ideal world execution. The goal is to show that $D_{\mathcal{R}} \sim D_{\mathcal{I}}$. The proof proceeds by first showing that distinguishing between $D_{\mathcal{R}}$ and $D'_{\mathcal{R}}$ reduces to breaking the pseudorandomness of tdp (hence, $D_{\mathcal{R}} \sim D'_{\mathcal{R}}$),

```

let siml !crupt !toZ frZ !toF frF !toP frP !toQ frQ !k !bits =
  let rbits = ref bits in
  let (pk0, td0) = kgen k in
  let (pk1, td1) = kgen k in
  let r0 = sample k rbits in
  let r1 = sample k rbits in
  let σ = xors (prg pk0 r0) (prg pk1 r1) in
  match crupt with
  | CruptP ⇒
    let ⟨!GetCRS, frZ⟩ = rd frZ in
    wr (X2Z (PublicStrings σ pk0 pk1)) →toZ ;
    let ⟨!(A2P (Commit' y)), frZ⟩ = rd frZ in
    if check td0 pk0 y then
      wr (Commit 0) → toP
    else
      if check td1 pk1 (xors y σ) then
        wr (Commit 1) → toP
      else error "Fail" ;
    let ⟨!(A2P (Open' b r)), frZ⟩ = rd frZ in
    if b == 0 && y == prg pk0 r ||
      b == 1 && y == xors (prg pk1 r) σ
    then !(wr Open → toP)
    else error "Fail"
  | ...

```

Figure 8. Ideal world simulator (excerpt) for UC commitment (full version in Appendix).

and then by showing that distinguishing between $D'_{\mathcal{R}}$ and D_I also reduces to breaking the pseudorandomness of tdp (hence, $D'_{\mathcal{R}} \sim D_I$). By the transitivity of indistinguishability, $D_{\mathcal{R}} \sim D_I$. \square

6.6 Reentrancy in SaUCy

The cryptography community has recently identified subtleties in defining UC ideal functionalities that relate to reentrancy and the scheduling of concurrent code, such that several functionalities in the literature are ambiguous as ITMs [11]. Although concerning, these issues have no cryptographic flavor, but instead are better addressed from the PL viewpoint. To illustrate, consider the following fragment of (untyped) ILC syntax, which allows an adversary to control the delivery schedule of messages from P to Q (an asynchronous channel):

```

let reentrantF frP frA ... =
  loop frP !(λ m . (let ⟨!Ok, frA⟩ = rd frA in wr m → toQ)
    |> wr m → toA)

```

After receiving input from party P , it notifies the adversary, then forks a background thread to wait for Ok before delivering the message. This introduces a race condition: suppose

input message m_1 is sent by P , but then the adversary \mathcal{A} , before sending Ok, instead returns control to \mathcal{Z} , which passes P a second input m_2 . Now there are two queued messages. Which one gets delivered when the adversary sends Ok?

To resolve this paradox, notice that this fragment is untypeable in ILC. The race condition occurs because the read channel frA is duplicated. Since frA is linear in the function body, the function would not be typeable as intuitionistic as required by the loop construct. Camenisch et al. [11] identified several strategies for resolving this problem in UC, which in turn are expressible ILC. One approach is to make the process explicitly sequential, such that the arrival of a second message before the first is delivered causes execution to get stuck:

```

letrec sequentialF frP frA ... =
  let ⟨!m, frP⟩ = rd frP in
  wr m → toA;
  let ⟨!Ok, frA⟩ = rd frA in
  wr m → toQ;
  sequentialF frP frA

```

Alternatively, we may discard such messages arriving out of order, returning them to sender; we express this in ILC using the external choice operator,

```

letrec discardingF frP frA ... =
  let ⟨!m, frP⟩ = rd frP in
  wr m → toA;
  letrec iloop frP frA =
    (let ⟨_, frP⟩ = rd frP in
      wr Discard → toP;
      iloop frP frA)
    ⊕ (let ⟨!Ok, frA⟩ = rd frA in
      wr m → toQ;
      discardingF frP frA ...)
  in iloop frP frA

```

7 Related Work

7.1 Process Calculi

Process calculi have a long and rich history. ILC occupies a point in this space that is particularly suited to faithfully capturing interactive Turing machines (and hence, computational cryptography), but plenty of existing calculi are also cryptographically-flavored and/or enjoy similar properties to ILC. We survey some of them here.

With symbolic semantics. Two early adaptations of process calculi for reasoning about cryptographic protocols were the spi calculus [2] and the applied π -calculus [1], both of which extend the π -calculus with cryptographic operations [34]. Symbolic UC [9] is a simulation-based security

framework in this setting. However, protocols proven secure in the symbolic setting may not be realizable with any cryptographic primitives based on hardness assumptions.

With computational semantics. Naturally, ensuing work has turned to bridging the gap between this PL-style of formalization and the computational model of cryptography by outfitting these calculi with a computational semantics. Lincoln et al. [29] give a computational semantics to a variant of the π -calculus, which allows one to define communicating probabilistic polynomial-time processes; Mateus et al. [31] adapts their calculus to explore (sequential) compositionality properties in protocols. A drawback of these protocols is that they embed probabilistic choices directly into the definition—essentially when faced with non-determinism, each path has equal probability. Laud [27] gives a computational semantics to the spi calculus, which additionally includes a type system for ensuring well-typed protocols preserve the secrecy of messages given to it by users.

With confluence. There are a number of other process calculi that enjoy confluence. Berger et al. [6] describe a type system for capturing deterministic (sequential) computation in the π -calculus. The type system uses affineness and stateless replication to achieve deterministic computation. Fowler et al. [19] present a core linear lambda calculus with (binary) session-typed channels and exception handling that enjoys confluence and termination. The calculus only considers two-party protocols, so for our multiparty setting, ILC requires a sophisticated type system to achieve confluence.

7.2 Tools for Cryptographic Analysis

There are a variety of computer-aided tools for cryptographic analysis. Among the symbolic tools include the NRL protocol analyzer [32], Maude-NPA [18], and Proverif [8]. Among the computational tools include CertiCrypt [5], EasyCrypt [4], CryptoVerif [7], Cryptol [28], and F* [35]. Although the computational tools do not support message-passing concurrency, it would be interesting to embed ILC into EasyCrypt or F* in order to use its tooling.

7.3 Variations of the UC Framework

A number of variants of the UC framework have been proposed to address some of its hitches or to make it easier to use. The reactive simulatability framework (RSIM) [3] is a framework for representing and analyzing cryptographic protocols in a composable way. In contrast with UC, which uses ITMs as its computational model, RSIM uses probabilistic IO automata, which is amenable automated reasoning tools. In contrast to RSIM, ILC is intended to be the basis for a convenient and flexible programming language to which we can easily port existing UC pseudocode. The IITMs model [25, 26] has similar goals to ours, namely to provide a simple basis for UC. However, our approach is simpler, for example our

type system subsumes their notion of *compatibility* and *connectability*, we avoid the distinctions of *net*, *external*, *io*, etc. tapes, and give a concrete programming model.

Simplified universal composability (SUC) [13] gives a simpler and restricted variant of the UC framework that is suitable for two-party multiparty computation tasks. The main difference from vanilla UC is that the set of parties is fixed, which greatly simplifies polynomial time reasoning and protocol composition while maintaining the same strong properties. We follow this in our execUC definition.

8 Conclusion and Future Work

The universal composability (UC) framework is widely used in cryptography for proofs. SaUCy takes a step towards mechanizing UC as a programming framework for constructing and analyzing large systems. We envision using SaUCy to tackle, for example, applications involving blockchains and smart contracts [16, 17, 33], which comprise an array of cryptography and distributed computing components and suffer from increasingly unwieldy formalisms.

We can view ILC typechecking of simulators in SaUCy as a partial mechanization of UC proofs, though the indistinguishability analysis is still on paper. Even partial mechanization is useful for catching bugs; we imagine using SaUCy to systematically implement functionalities and protocols from the literature and fuzz test them. Future work would be to embed ILC within a mechanized proof system, such as F* or EasyCrypt.

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A ILC Metatheory Attempt

Lemma A.1 (Canonical forms). *If*

$$\Delta_{in}; \Gamma; \Omega \vdash v : U; \Delta_{out}$$

then

1. *If* U is $\mathbb{1}$, *then* $v = ()$.
2. *If* U is $A \times B$, *then* $v = (v_1, v_2)_\infty$ *where*

$$\Delta_{in}; \Gamma; \Omega \vdash v_1 : A; \Delta_{out}$$

and

$$\Delta_{in}; \Gamma; \Omega \vdash v_2 : B; \Delta_{out}.$$

3. *If* U is $A_1 + A_2$, *then* $v = \text{inj}_\pi^i(v_i)$ *where* $i \in \{1, 2\}$ *and*

$$\Delta_{in}; \Gamma; \Omega \vdash v_i : A_i; \Delta_{out}.$$

4. *If* U is $V_1 \rightarrow_\pi V_2$, *then* $v = \lambda_\pi x. e$ *where*

$$\Delta_{in}; \Gamma; \Omega \vdash x : V_1; \Delta_{out}$$

and

$$\Delta_{in}; \Gamma; \Omega \vdash e : V_2; \Delta_{out}.$$

5. *If* U is WrS , *then* $v = \text{TODO}$.

6. *If* U is $RefA$, *then* $v = \text{ref}(v')$ *where*

$$\Delta_{in}; \Gamma; \Omega \vdash v' : A; \Delta_{out}.$$

7. *If* U is RdS , *then* $v = \text{TODO}$.

8. *If* U is $!A$, *then* $v = \text{TODO}$.

9. *If* U is $X \otimes Y$, *then* $v = (v_1, v_2)_1$ *where*

$$\Delta_{in}; \Gamma; \Omega \vdash v_1 : X; \Delta_{out}$$

and

$$\Delta_{in}; \Gamma; \Omega \vdash v_2 : Y; \Delta_{out}.$$

Theorem A.2 (Local progress). *If*

$$\Delta_{in}; \Gamma; \Omega \vdash e : U; \Delta_{out}$$

$$\Gamma; \Omega \vdash \sigma$$

then either e **lterm** *or there exists* $\sigma'; e'$ *such that*

$$\sigma; e \rightarrow \sigma'; e'.$$

Proof. By induction on the derivation of e .

- **Case** $\frac{}{\Delta; \Gamma, x : A \vdash x : A; \Delta} \text{var}$

Theorem A.3 (Local preservation). *If*

$$\begin{array}{c} \Delta_{in}; \Gamma; \Omega \vdash e : U; \Delta_{out} \\ \Gamma; \Omega \vdash \sigma \\ \sigma; e \rightarrow \sigma'; e' \end{array}$$

then there exists store typing extension $\Omega' \supseteq \Omega$ such that

$$\begin{array}{c} \Delta_{in}; \Gamma; \Omega' \vdash e' : U; \Delta_{out} \\ \Gamma; \Omega'; \vdash \sigma'. \end{array}$$

B ILC meta theory

B.1 Additional syntax

We define syntax for store, process and channel typings, which each map a kind of identifier (location, process name, or channel name) to its associated type:

$$\begin{array}{ll} \text{Store typings} & \Omega ::= \cdot \mid \Omega, \ell : A \\ \text{Process pool typings} & \Phi ::= \cdot \mid \Phi, p : A \mid \Phi, p : X \\ \text{Channel pool typings} & \Psi ::= \cdot \mid \Psi, c : A \mid \Psi, c : X \end{array}$$

B.2 Configuration typings

Using the syntax above, we define configuration typing as a straightforward extension of single-process typing, given in Sec. 3.2:

$\boxed{\Omega; \Psi \vdash C : \Phi \triangleright m}$ Configuration C is well-typed

$$\begin{array}{c} \frac{\Omega; \Psi \vdash \sigma : \Omega}{\Omega; \Psi \vdash \langle \Sigma; \sigma; \varepsilon \rangle : \cdot \triangleright V} \text{empty} \\ \\ \frac{\Omega; \Psi \vdash e : X \triangleright m_1 \quad \Omega; \Psi \vdash \langle \Sigma; \sigma; \pi \rangle : \Phi \triangleright m_2 \quad m_1 \parallel m_2 \Rightarrow m_3}{\Omega; \Psi \vdash \langle \Sigma; \sigma; \pi, p : e \rangle : \Phi, p : X \triangleright m_3} \text{aff} \\ \\ \frac{\Omega; \Psi \vdash e : A \triangleright m_1 \quad \Omega; \Psi \vdash \langle \Sigma; \sigma; \pi \rangle : \Phi \triangleright m_2 \quad m_1 \parallel m_2 \Rightarrow m_3}{\Omega; \Psi \vdash \langle \Sigma; \sigma; \pi, p : e \rangle : \Phi, p : A \triangleright m_3} \text{int} \end{array}$$

Notably, parallel processes with modes m_1 and m_2 must have modes that compose, where $m_1 \parallel m_2$ is defined; this invariant ensures that there is at most one write mode process in a well-typed pool.

Because of the premise in the base case rule (empty), all well-typed configurations consist of well-typed stores. We define store typing in the standard way:

$\boxed{\Omega; \Psi \vdash \sigma : \Omega}$ Under Ω and Ψ , store σ is well-typed

$$\frac{}{\Omega; \Psi \vdash \cdot : \cdot} \text{empty} \quad \frac{\Omega; \Psi \vdash v : A \quad \Omega; \Psi \vdash \sigma : \Omega}{\Omega; \Psi \vdash (\sigma, \ell : v) : (\Omega, \ell : A)} \text{loc}$$

B.3 Type soundness

Lemma B.1 (Non-progress). *For all configurations C , store typings Ω , channel typings Ψ , and process typings Φ , if $\Omega; \Psi \vdash C : \Phi$ and C **term**, then $\nexists C'$ such that $C \longrightarrow C'$.*

Theorem B.2 (Progress). *For all configurations C , store typings Ω , channel typings Ψ , and process typings Φ , if $\Omega; \Psi \vdash C : \Phi \triangleright m$ then either C **term**, or $\exists C'$ such that $C \longrightarrow C'$.*

Theorem B.3 (Preservation). *For all configurations C, C' , store typings Ω , channel typings Ψ and process typings Φ , if $\Omega; \Psi \vdash C : \Phi \triangleright m$ and $C \longrightarrow C'$ then there exists store typing extension $\Omega' \supseteq \Omega$ channel typing extension $\Psi' \supseteq \Psi$ and process typing extension $\Phi' \supseteq \Phi$ such that $\Omega'; \Psi' \vdash C' : \Phi' \triangleright m'$.*

B.4 Confluence

The following lemmas state structural invariants over write effects and read channels of a well-typed configuration: no two writes are in parallel composition, and every read channel end is a non-duplicable (affine) resource.

Lemma B.4 (Unique writer process). *If C is a well-typed configuration with process pool π , then there exists at most one write-mode process in π .*

Proof. By structural induction over the typing derivation for C . \square

Lemma B.5 (Unique reader process). *If C is a well-typed configuration with process pool π , and c is a read channel in this configuration, then there exists at most one process in π where c appears.*

Proof. By structural induction over the typing derivation for C . \square

Theorem B.6 (Single-step confluence). *For all well-typed configurations C , if $C \rightarrow C_1$ and $C \rightarrow C_2$ then there exists renaming function f such that either:*

1. $C_1 = f(C_2)$, or
2. $\exists C_3$ such that $C_1 \rightarrow C_3$ and $f(C_2) \rightarrow C_3$.

Proof. By induction on the pair of steps $\langle C \rightarrow C_1, C \rightarrow C_2 \rangle$.

We consider the following cases:

Case: congruence: if either step uses *congr*, we apply the inductive hypothesis.

Case: independent processes: the two steps advance distinct processes, using any of the rules *local*, *fork* and *nu*, we produce C_3 by combining those two (independent) steps.

Case: one process: the two steps advance the same process, we show that this was deterministic (up to naming) by constructing the naming function f such that $C_2 = f(C_1)$. Most cases are straightforward since they perform no non-deterministic choices. The only source of non-determinism is the name choices, in rules *nu*, *fork*, and *new*; in each case, we map the name choice from the second step to that of the first step.

Case: read-write interaction: In the case that either step uses *rw*, we rely on lemmas B.4 and B.5. to show that both steps use *rw*, and that the reader-writer process pair is unique. \square

By composing multiple uses of this theorem we prove multi-step confluence. However, to carry forth this composition, we need a more general notion of single-step confluence, which is parameteric in a renaming function for the initial configurations.

Theorem B.7 (Single-step confluence, generalized). *For all well-typed configurations C and renaming functions f , if $C \rightarrow C_1$ and $f(C) \rightarrow C_2$ then there exists renaming function g such that either:*

1. $C_1 = g(C_2)$, or
2. $\exists C_3$ such that $C_1 \rightarrow C_3$ and $g(C_2) \rightarrow C_3$.

Proof. (Analogous to first single-step confluence proof.) \square

We prove a full confluence theorem that is generalized similarly, by accepting a renaming function f to produce a new one g :

Theorem B.8 (Full confluence). *For all well-typed configurations C , and renaming functions f , if $C \rightarrow^* C_1$ and $f(C) \rightarrow^* C_2$ and C_1 **term** and C_2 **term** then there exists renaming function g such that $C_1 = g(C_2)$.*

Proof. By induction on the reduction sequence pair $\langle C \rightarrow^* C_1, f(C) \rightarrow^* C_2 \rangle$. Because of single-step confluence, we know that if either reduction sequence is empty, then the other must be empty, and that if either takes a step, the other must take a step.

Empty case: When empty, we have the resulting renaming function g via single-step confluence.

Step case: We consider the case where each reduction consists of at least one step: $C \rightarrow C'_1$ and $C'_1 \rightarrow^* C_1$ and $f(C) \rightarrow C'_2$ and $C'_2 \rightarrow^* C_2$. By single-step confluence, we have that there exists g_0 such that $g_0(C'_2) = C'_1$. By the inductive hypothesis, we have that there exists g such that $C_1 = g(C_2)$. \square

C ILC Implementation of execUC

The full definition of the UC execution experiment is given in Figure 9.

```

execUC ::  $\forall \dots . X_z \multimap X_p \otimes X_q \multimap X_f \multimap X_a \multimap ! \text{Nat} \multimap ! [\text{Bit}] \multimap ! \text{Crupt} \multimap_m ! \text{Bit}$ 
let execUC z (p,q) f a k !r crupt =
  v (rZ2P, wZ2P), (rP2Z, wP2Z)
  , (rZ2Q, wZ2Q), (rQ2Z, wQ2Z)
  , (rP2F, wP2F), (rF2P, wF2P)
  , (rQ2F, wQ2F), (rF2Q, wF2Q)
  , (rF2A, wF2A), (rA2F, wA2F)
  , (rA2Z, wA2Z), (rZ2A, wZ2A)
  , (rP2A, wP2A), (rA2P, wA2P)
  , (rQ2A, wQ2A), (rA2Q, wA2Q)
  , (rP2Q, wP2Q), (rQ2P, wQ2P)
  . let wrapperP p !crupt toZ frZ toF frF toA frA toQ frQ k bits =
    if crupt == CruptP then
      loop frZ !(\lambda _ . error "Z cannot write to corrupted party.")
      |> fwd frF toA
      |> fwd frP toA
      |> fwd frA toF
    else
      p toZ frZ toF frF toQ frQ k bits in
  let wrapperQ q !crupt toZ frZ toF frF toA frA toP frP k bits =
    if crupt == CruptQ then
      loop frZ !(\lambda _ . error "Z cannot write to corrupted party.")
      |> fwd frF toA
      |> fwd frP toA
      |> fwd frA toF
    else
      q toZ frZ toF frF toP frP k bits in
  let (rf,ra,rp,rq,rz) = splitBits5 r in
  f crupt !wF2P rP2F !wF2Q rQ2F !wF2A rA2F k !rf
  |> a crupt !wA2Z rZ2A !wA2F rF2A !wA2P rP2A !wA2Q rQ2A k !ra
  |> wrapperP p crupt !wP2Z rZ2P !wP2F rF2P !wP2A rA2P !wP2Q rQ2P k !rp
  |> wrapperQ q crupt !wQ2Z rZ2Q !wQ2F rF2Q !wQ2A rA2Q !wQ2P rP2Q k !rq
  |> z !wZ2P rP2Z !wZ2Q rQ2Z !wZ2A rA2Z k !rz

```

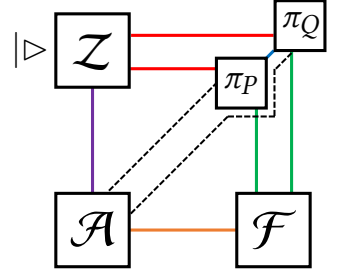


Figure 9. Full definition of execUC. The channels follow a uniform naming scheme. The read end of a channel is prefixed with r- and the write end of a channel is prefixed with w-. The channel rZ2P denotes the read end of communications from the environment z to the party p. First, the random bitstring is split amongst each of the five parties. Then, the functionality, the adversary, and both protocol parties are spawned in a child process (given the appropriate channels and parameters), and the process continues as the environment process. Notice that parties are run in wrapper functions, which alter their behavior depending on whether or not they are corrupted. If a party is corrupted, then the adversary masquerades as the party. The mode carried over the rightmost lollipop is derived as $\{(m_f, m_a, m_z) \mid m_f \parallel (m_a \parallel (R \parallel (R \parallel m_z))) \Rightarrow m\}$.

D Extending ILC with Trapdoor Permutations

UC Commitments are realized from cryptographic primitives, such as trapdoor permutations, which require extensions to ILC. The new syntactic forms are *kgen*, *tdp*, *inv*, and *hc* with the static and dynamic semantics shown in Figure 10. The semantics are written in terms of the cryptographic objects themselves.

The key generation function *keygen* takes as input a random bitstring and outputs a random public key v_{pk} and a trapdoor v_{td} . The trapdoor permutation function *tdp* takes as inputs a key v_{pk} and a bitstring v_{in} and outputs a bitstring v_{out} . The *inv* function takes as inputs a key-trapdoor pair (v_{pk}, v_{td}) and a bitstring v_{in} and outputs a bitstring v_{out} . The hardcore predicate function *hc* takes as input a key v_{pk} and outputs a single bit.

We can use these to implement a special pseudorandom number generator $G_{pk} : \{0, 1\}^k \rightarrow \{0, 1\}^{4k}$ that has a trapdoor property, i.e., it is easy to compute, but difficult to invert except with special information called the “trapdoor.”

$$G_{pk}(r) = (\mathbf{f}_{pk}^{(3n)}(r), \mathbf{B}(\mathbf{f}_{pk}^{(3n-1)}(r)), \dots, \mathbf{B}(\mathbf{f}_{pk}(r)), \mathbf{B}(r))$$

Here, \mathbf{f}_{pk} is a trapdoor permutation over $\{0, 1\}^k$, with $\mathbf{f}_{pk}^{(i)}(r)$ denoting the i^{th} -fold application of \mathbf{f}_{pk} , and \mathbf{B} is a hardcore predicate for \mathbf{f}_{pk} . In ILC, this can be implemented as:

```

iterate :: ∀ a . Int → (a → a) → a → a
prg :: [Bit] → [Bit] → Nat → [Bit]
let prg pk r k =
  letrec aux j =
    if j ≤ 0 then [hc r]
    else hc (iterate j (tdp pk) r) : aux pk r (j - 1) in
  iterate (3 * k) (tdp pk) r # aux pk r (3 * k - 1)

```

E Universally Composable Commitment Protocol

In this section we give the full elaboration of our UC commitment instantiation. The specification functionality is given in the body in Figure 1, along with the protocol implementation in Section 6.5. Our development follows closely from the pseudocode in the UC literature [14], which we show here in Algorithm 1. The protocol relies on the CRS functionality which we define here in Figure 13. To briefly summarize what is going: the setup CRS samples a random string σ and two trapdoor pseudorandom generators (*prgs* pk_0, pk_1). To commit to the bit b , the committer produces a string y that is the result of applying one or the other of the *prgs*, and if $b = 1$ additionally applying xor with σ . The intuitive explanation why this is hiding is that without the trapdoor, it is difficult to tell whether a random $4k$ -bit string is in the range of either *prg*. To open the commitment, the committer simply reveals the preimage and the receiver checks which

of the two cases applies. The intuitive explanation why this is binding is that it is difficult to find a pair $y, y \oplus \sigma$ that are respectively in the range of both *prgs*.

The UC proof consists of two simulators, one for the ideal world and one for the real world. The ideal world simulator, given in Figure 15 is ported directly from the UC literature [14], while the non-standard real world simulator, given in Figure 16, is required because our protocol emulation definition requires simulation in both directions. The key to the ideal world simulator is to allow the simulator to generate its own “fake” CRS, for which it stores the trapdoors. The string σ is not truly random, but instead is the result of combining two evaluations of the *prgs*. The ideal world simulator consists of two cases, depending on which of the parties is corrupt.

In the case that the committer P is corrupt, the simulator needs to be able to *extract* the committed value. The simulator is activated when \mathcal{Z} sends a message (*Commit' y*); in the real world, this is relayed by the dummy adversary to Q , who outputs *Committed* back to the environment. Hence to achieve the same effect in the ideal world, the simulator must send (*Commit b*) to \mathcal{F}_{COM} . To extract b from y , the simulator makes use of the *prg* trapdoor check which one has y in its range. It is necessary to argue by cryptographic reduction that this simulation is sound. To show this, we would define an alternative execution where the *prg* is substituted for a truly random function (i.e., a random oracle). If an environment \mathcal{Z} could distinguish between these two worlds, then we could adapt the execution to distinguish the *prg* from random, violating the *prg* assumption.

In the case that the receiver Q is corrupt, the simulator needs to *equivocate*. The simulator is activated when \mathcal{Z} inputs (*Commit b*) to P , after which \mathcal{F}_{COM} sends *Committed* to the simulator. In the real world, the environment receives a commitment message (*Commit' y*) from corrupted Q for some seemingly-random y . To achieve the same effect, the simulator must choose y . However, the simulator is next activated when the \mathcal{Z} inputs (*Open b*) to P , after which the simulator learns b from \mathcal{F}_{COM} . However, in the real world the environment receives a valid opening (*Opened' b r*) that is consistent with y and with the value chosen by the environment. Thus the simulator must initially choose y so that it can later be opened to either value b may take. The simulator achieves this by choosing σ and y ahead of time while generating the fake CRS. The reduction step is the same, and involves replacing *prg* with a true random function.

Recall that the motivation for the real world simulator is to rule out degenerate protocols that diverge in some way. For every well behaved environment such that the ideal world is PPT, we need to demonstrate an adversary in the real world that is also PPT. Fortunately, the real world simulator, shown in Figure 16 is much simpler than ideal world simulator. Essentially the simulator runs a copy of the honest protocol

Expressions $e ::= \text{kgen}(e) \mid \text{tdp}(e_1, e_2) \mid \text{inv}(e_1, e_2) \mid \text{hc}(e)$	
$\Delta; \Gamma \vdash e : A \triangleright m$	Under Δ and Γ , expression e has intuitionistic type A and mode m .
$\frac{\Delta; \Gamma \vdash e : [\text{Bit}]}{\Delta; \Gamma \vdash \text{kgen}(e) : ([\text{Bit}], [\text{Bit}])} \text{kgen}$	$\frac{\Delta_1; \Gamma \vdash e_1 : [\text{Bit}] \quad \Delta_2; \Gamma \vdash e_2 : [\text{Bit}]}{\Delta_1, \Delta_2; \Gamma \vdash \text{tdp}(e_1, e_2) : [\text{Bit}]} \text{tdp}$
$\frac{\Delta_1; \Gamma \vdash e_1 : [\text{Bit}] \times_{\pi} [\text{Bit}] \quad \Delta_2; \Gamma \vdash e_2 : [\text{Bit}]}{\Delta_1, \Delta_2; \Gamma \vdash \text{inv}(e_1, e_2) : [\text{Bit}]} \text{inv}$	$\frac{\Delta; \Gamma \vdash e : [\text{Bit}] \rightarrow \text{Bit}}{\Delta; \Gamma \vdash \text{hc}(e) : \text{Bit}} \text{hc}$
$\sigma_1; e_1 \longrightarrow \sigma_2; e_2$	Under store σ_1 , expression e_1 reduces to $\sigma_2; e_2$.
$\frac{\mathbf{Gen}(v_r) = (v_{pk}, v_{td}) \quad v_{pk}, v_{td} \in \{0, 1\}^k}{\sigma; \text{kgen}(v_r) \longrightarrow \sigma; (v_{pk}, v_{td})} \text{kgen}$	$\frac{\mathbf{f}(v_{pk}, v_{in}) = v_{out} \quad \mathbf{f}: \{0, 1\}^k \rightarrow \{0, 1\}^k \rightarrow \{0, 1\}^k}{\sigma; \text{tdp}(v_{pk}, v_{in}) \longrightarrow \sigma; v_{out}} \text{tdp}$
$\frac{\mathbf{Inv}((v_{pk}, v_{td}), v_{in}) = v_{out} \quad \mathbf{Inv}: \{0, 1\}^k \times \{0, 1\}^k \rightarrow \{0, 1\}^k \rightarrow \{0, 1\}^k}{\sigma; \text{inv}((v_{pk}, v_{td}), v_{in}) \longrightarrow \sigma; v_{out}} \text{inv}$	$\frac{\mathbf{B}(v_{pk}) = v \quad \mathbf{B}: \{0, 1\}^k \rightarrow \{0, 1\}}{\sigma; \text{hc}(v_{pk}) \longrightarrow \sigma; v} \text{hc}$

Figure 10. Extending ILC with trapdoor permutations. The semantics are parameterized by a security parameter k .

for each of the corrupted parties. The simulation that results in this case is identical.

Protocol 1: Universally Composable Commitment

- 1 Public strings:
 - 2 σ : Random string in $\{0, 1\}^{4n}$
 - 3 pk_0, pk_1 : Keys for generator $G_k: \{0, 1\}^n \rightarrow \{0, 1\}^{4n}$
 - 4 Commit(b):
 - 5 $r \leftarrow \{0, 1\}^n$
 - 6 $y := G_{pk_b}(r)$
 - 7 if $b = 1$ then $y := y \oplus \sigma$
 - 8 Send (Commit, y) to receiver.
 - 9 Upon receiving (Commit, y) from A , B outputs (Receipt).
 - 10 Decommit(x):
 - 11 Send (b, r) to receiver.
 - 12 Receiver checks $y = G_{pk_b}(r)$ for $b = 0$, or $y = G_{pk_b}(r) \oplus \sigma$ for $b = 1$. If verification succeeds, then B outputs (Open, b).
-

```

dummy :: ∀ a ... ! Crupt → ... → ! Nat → ! [Bit] → a
let dummyA !crupt !toZ frZ !toF frF !toP frP toQ frQ !toQasP toPasQ k r =
  let fwd2Z c = loop c !(λ m . wr (X2Z m) → toZ) in
    loop frZ !(λ x . match x with
      | A2F m ⇒ wr m → toF
      | A2P m ⇒ if crupt == CruptP
        then wr m → toQasP
        else wr m → toP)

|▷ fwd2Z frF
|▷ fwd2Z frP
|▷ fwd2Z frQ

```

Figure 11. Dummy adversary. The dummy adversary forwards messages from the environment to either the functionality (if the message has constructor A2F) or the party p (if the message has constructor A2P). Similarly, the dummy adversary forwards messages from the functionality or the protocol parties to the environment.

```

dummyP :: ∀ a b ... ! Wr a → ... → ! Nat → ! [Bit] → b
let dummyP toZ frZ toF frF k r = fwd frZ toF |▷ fwd frF toZ

```

Figure 12. Dummy party. The dummy party simply relays information between the environment and the functionality.

```

fCrs :: ∀ ... ! Crupt → ... → ! Nat → ! [Bit]
let fCrs crupt !toP frP !toQ frQ !toA frA !k !bits =
  let rbits = ref bits in
  let pubref = ref Nothing in
  let replyCrs fr to = loop fr !(match @pubref with
    | Nothing ⇒ let σ = sample (4*k) rbits in
      let pk0 = kgen k (sample k rbits) in
      let pk1 = kgen k (sample k rbits) in
      let pub = (σ, pk0, pk1) in
      set pubref := Just pub ; wr pub → to
    | Just pub ⇒ wr pub → to) in

  replyCrs frP toP
|▷ replyCrs frQ toQ
|▷ replyCrs frA toA

```

Figure 13. Ideal functionality for common reference string.

```

fCom :: ∀ ... ! Crupt → ... → ! Nat → R ! ℤ
let fCom crupt toP frP !toQ frQ toA frA k bits =
  let ⟨!(Commit b), frP⟩ = rd frP in
    wr Receipt → toQ ;
  let ⟨!Open, frP⟩ = rd frP in
    !(wr (Opened b) → toQ)

```

Figure 14. Ideal functionality for one-time bit commitment.

```

let siml !crupt !toZ frZ !toF frF !toP frP !toQ frQ !k !bits =
  let rbits = ref bits in
  let (pk0,td0) = kgen k in
  let (pk1,td1) = kgen k in
  let r0 = sample k rbits in
  let r1 = sample k rbits in
  let σ = xors (prg pk0 r0) (prg pk1 r1) in
  match crupt with
  | CruptP ⇒
    let ⟨!GetCRS, frZ⟩ = rd frZ in
    wr (X2Z (PublicStrings σ pk0 pk1)) →toZ ;
    let ⟨!(A2P (Commit' y)), frZ⟩ = rd frZ in
    if check td0 pk0 y then
      wr (Commit 0) → toP
    else
      if check td1 pk1 (xors y σ) then
        wr (Commit 1) → toP
      else error "Fail" ;
    let ⟨!(A2P (Open' b r)), frZ⟩ = rd frZ in
    if b == 0 && y == prg pk0 r ||
      b == 1 && y == xors (prg pk1 r) σ
    then !(wr Open → toP)
    else error "Fail"
  | CruptQ ⇒
    let ⟨!GetCRS, frZ⟩ = rd frZ in
    wr (X2Z (PublicStrings σ pk0 pk1)) →toZ ;
    let ⟨!Receipt, frQ⟩ = rd frQ in
    let y = prg pk0 r0 in
    wr (X2Z (Commit' y)) →toZ ;
    let ⟨!(Opened b'), frQ⟩ = rd frQ in
    if (b' == 0) then
      !(wr (X2Z (Opened' r0)) →toZ)
    else
      !(wr (X2Z (Opened' r1)) →toZ)
  | CruptNone ⇒ error "Fail"

```

Figure 15. Ideal world simulator for UC commitment.

SaUCy

```
let simR !crupt !toZ frZ !toF frF toP frP !toQ frQ !k !bits =  
  match crupt with  
  | CruptP  $\Rightarrow$   
    let  $\langle!(\text{Commit } b), \text{frZ}\rangle = \text{rd frZ in}$   
      wr GetCRS  $\rightarrow$  toF ;  
      let  $\langle!(\text{PublicStrings } \sigma \text{ pk}_0 \text{ pk}_1), \text{frF}\rangle = \text{rd frF in}$   
      let r = take k bits in  
      let y = if b == 0 then prg pk0 r else xors (prg pk0 r)  $\sigma$  in  
        wr (Commit' y)  $\rightarrow$  toQ ;  
        let  $\langle!(\text{Open}), \text{frZ}\rangle = \text{rd frZ in}$   
          !(wr (Open' b r)  $\rightarrow$  toQ)  
  | CruptQ  $\Rightarrow$   
    let  $\langle!(\text{Commit' } y), \text{frQ}\rangle = \text{rd frQ in}$   
      wr Receipt  $\rightarrow$  toZ ;  
      let  $\langle!(\text{Open' } b \text{ r}), \text{frQ}\rangle = \text{rd frQ in}$   
        !(wr (Opened b)  $\rightarrow$  toZ)  
  | CruptNone  $\Rightarrow$  error "Fail"
```

Figure 16. Real world simulator for UC commitment.