

## SGM4992 1.1W Stereo Audio Power Amplifier

#### **GENERAL DESCRIPTION**

The SGM4992 is a stereo audio power amplifier that is designed for demanding applications in mobile phones and other portable communication device applications. It is capable of delivering 1.1W of continuous average power per channel into an  $8\Omega$  Bridge Tied Load (BTL) with less than 1% distortion (THD+N) from a 5V power supply.

The SGM4992 features independent shutdown control for each channel and a low-power consumption shutdown mode, which is achieved by driving both shutdown pins with logic low. Additionally, it features an internal thermal shutdown protection mechanism.

The SGM4992 does not require output coupling capacitors or bootstrap capacitors, and therefore is ideally suited for mobile phone and other low voltage applications where minimal power consumption is a primary requirement.

The SGM4992 contains advanced pop & click circuitry which eliminates noise which would otherwise occur during turn-on and turn-off transitions.

The SGM4992 is unity-gain stable and can be configured by external gain-setting resistors.

The SGM4992 is available in Pb-free TDFN-14 (4.0mm×3.0mm) package. It operates over an ambient temperature range of -40°C to +85°C.

### **FEATURES**

- 1.1W to 8Ω BTL Load from 5V Supply at THD+N = 1% Typical (per Channel)
- Excellent PSRR: Direct Connection to the Battery
- Unity Gain Stable
- 2.5V to 5.5V Operation
- Shutdown Current: 0.1µA (TYP)
- Shutdown Pin is Compatible with 1.8V Logic
- Improved Pop & Click Circuitry
- No Output Coupling Capacitors
- External Gain Configuration Capability
- -40°C to +85°C Operating Temperature Range
- Available in Pb-Free TDFN-14 (4.0mm×3.0mm)
  Package

#### **APPLICATIONS**

Portable Systems MP3 Players Mobile Phones PDAs GPS

### PACKAGE/ORDERING INFORMATION

MODEL	ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION	MARKING INFORMATION	
SGM4992	SGM4992YDA14/TR	TDFN-14 (4.0mm × 3.0mm)	Tape and Reel, 3000	SGM4992DA	

#### **ABSOLUTE MAXIMUM RATINGS**

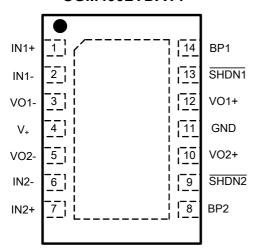
Supply Voltage 6V Input Voltage
Storage Temperature Range65°C to +150°C
Junction Temperature
Operating Temperature Range40°C to +85°C
Lead Temperature Range (Soldering 10 sec)
ESD Susceptibility
HBM
$MM \dots $

#### **NOTES**

1. Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### **SGM4992YDA14**

PIN CONFIGURATION (Top View)



TDFN-14 (4.0mm×3.0mm)

### **CAUTION**

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### SGM4992

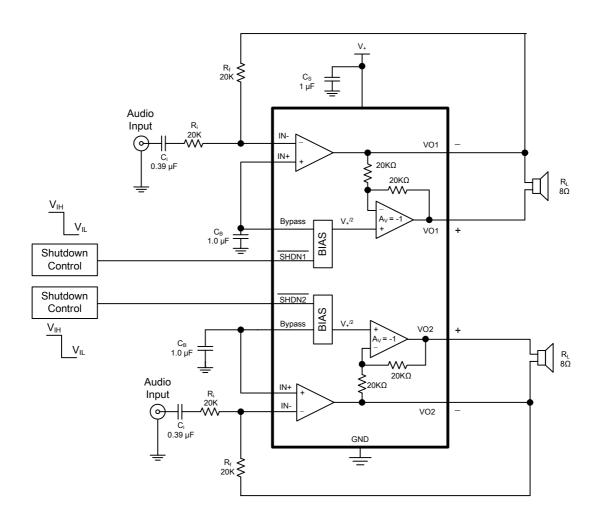
# ELECTRICAL CHARACTERISTICS: $T_A = 25^{\circ}C$

DADAMETED	SYMBOL	CONDITIONS		SGM4992			LINUTO
PARAMETER	SYMBOL			MIN	TYP	MAX	UNITS
Supply Voltage	V <sub>+</sub>		2.9			5.5	V
	IQ	V <sub>IN</sub> = 0V, I <sub>O</sub> = 0A	V <sub>+</sub> = 5.0V, No Load		8	16	mA
			V <sub>+</sub> = 5.0V, 8Ω Load		10	20	
Ovice and Bayes County County			V <sub>+</sub> = 3.3V, No Load		7.5	14	
Quiescent Power Supply Current			V <sub>+</sub> = 3.3V, 8Ω Load		9.5	17	
			V <sub>+</sub> = 2.6V, No Load		7		
			V <sub>+</sub> = 2.6V, 8Ω Load		9		
Shutdown Current	I <sub>SD</sub>	V <sub>SHDN</sub> = 0V			0.1	3	μA
Shutdown Voltage Input High	V <sub>SDIH</sub>			1.8			V
Shutdown Voltage Input Low	V <sub>SDIL</sub>					0.4	V
Output Offset Voltage	Vos			-55	2	55	mV
		f = 1kHz, THD+N=1%	V <sub>+</sub> = 5.0V		1.10		W
Output Dower per Channel (90)	l p		V+ = 3.6V		0.58		
Output Power per Channel (8 $\Omega$ )	Po		V <sub>+</sub> = 3.0V		0.40		
			V <sub>+</sub> = 2.6V		0.30		
Total Harmonic Distortion + Noise	THD+N	P <sub>O</sub> = 0.5Wrms, f =	= 1kHz		0.01		%
		f = 217Hz	V <sub>+</sub> = 5.0V		-66		- dB
			V <sub>+</sub> = 3.6V		-63		
	PSRR		V <sub>+</sub> = 3.0V		-63		
Power Supply Rejection Ratio			V <sub>+</sub> = 2.6V		-62		
rower Supply Rejection Ratio		f = 1kHz	V <sub>+</sub> = 5.0V		-72		
			V <sub>+</sub> = 3.6V		-68		
			V <sub>+</sub> = 3.0V		-66		
			V <sub>+</sub> = 2.6V		-64		
Crosstalk	X <sub>talk</sub>	$f = 1kHz, V_{+} = 5V,$	V <sub>O</sub> = 250mW		-120		dB
	Twu	C <sub>B</sub> = 1μF	V <sub>+</sub> = 5.0V		110		- ms
Wake-up Time			V+ = 3.6V		110		
wake-up fillie			V <sub>+</sub> = 3.0V		100		
			V <sub>+</sub> = 2.6V		100		
	T	8Ω Load	V <sub>+</sub> = 5.0V		10		μs
Shut Down Time			V <sub>+</sub> = 3.6V		16		
Shut Down Time	T <sub>SDT</sub>		V <sub>+</sub> = 3.0V		17.8		
			V <sub>+</sub> = 2.6V		17.8		

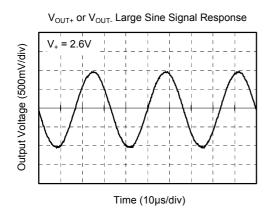
Specifications subject to changes without notice.

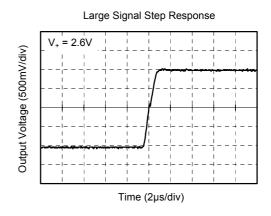


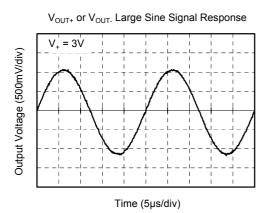
# **TYPICAL APPLICATION**

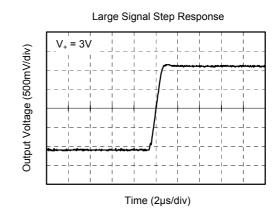


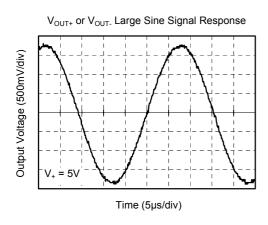
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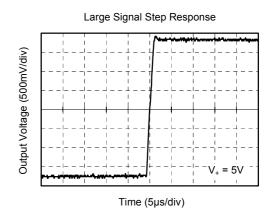




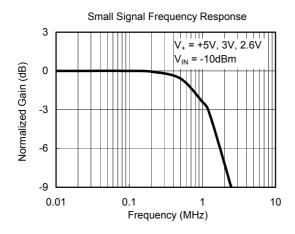


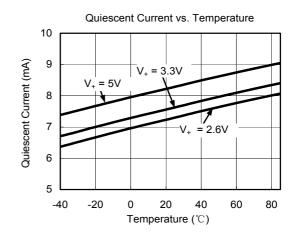


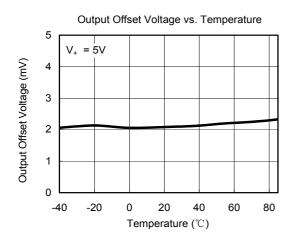


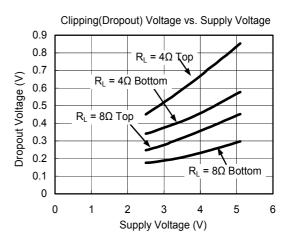


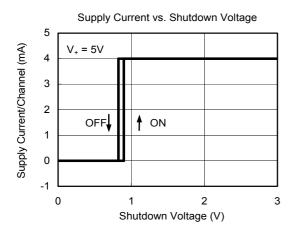
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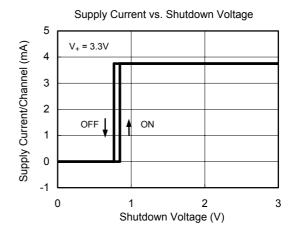




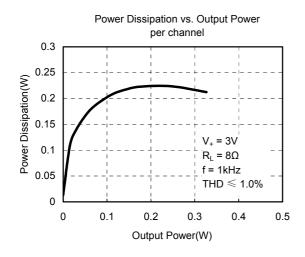


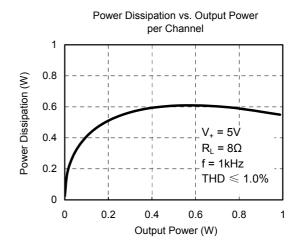


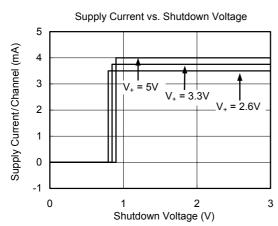




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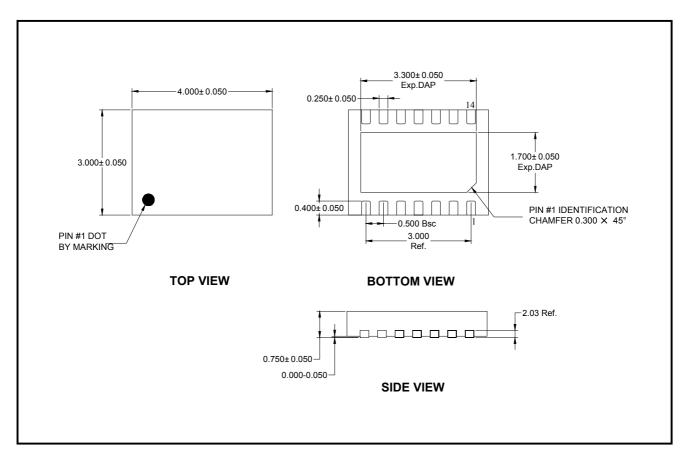






### PACKAGE OUTLINE DIMENSIONS

### **TDFN-14**



Note: All linear dimensions are in millimeters.

SGMICRO is dedicated to provide high quality and high performance analog IC products to customers. All SGMICRO products meet the highest industry standards with strict and comprehensive test and quality control systems to achieve world-class consistency and reliability.

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