BUK653R4-40C

N-channel TrenchMOS intermediate level FET

Rev. 02 — 12 October 2010

Product data sheet

1. Product profile

1.1 General description

Intermediate level gate drive N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using advanced TrenchMOS technology. This product has been designed and qualified to the appropriate AEC Q101 standard for use in high performance automotive applications.

1.2 Features and benefits

- AEC Q101 compliant
- Suitable for standard and logic level gate drive sources
- Suitable for thermally demanding environments due to 175 °C rating

1.3 Applications

- 12 V Automotive systems
- Electric and electro-hydraulic power steering
- Motors, lamps and solenoid control
- Start-Stop micro-hybrid applications
- Transmission control
- Ultra high performance power switching

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	-	40	V
I _D	drain current	$V_{GS} = 10 \text{ V}; T_{mb} = 25 \text{ °C};$ see <u>Figure 1</u>	[1]	-	-	100	А
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>		-	-	204	W
Static characteristics							
R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V; } I_{D} = 25 \text{ A;}$ $T_{j} = 25 \text{ °C; see } \frac{\text{Figure 11}}{\text{ Figure 11}}$		-	3.05	3.6	mΩ



Table 1. Quick reference data ...continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Avalanche	ruggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	$I_D = 100 \text{ A}; V_{\text{sup}} \le 40 \text{ V};$ $R_{\text{GS}} = 50 \Omega; V_{\text{GS}} = 10 \text{ V};$ $T_{\text{j(init)}} = 25 ^{\circ}\text{C}; \text{ unclamped}$	-	-	368	mJ
Dynamic cl	haracteristics					
Q_{GD}	gate-drain charge	$I_D = 25 \text{ A}$; $V_{DS} = 32 \text{ V}$; $V_{GS} = 10 \text{ V}$; see <u>Figure 13</u> ; see <u>Figure 14</u>	-	42	-	nC

^[1] Continuous current is limited by package.

2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	D	Drain	mb	D
3	S	source		_G (EA)
mb	D	mounting base; connected to drain	1 2 3	mbb076 S
			SOT78A (TO-220AB)	

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BUK653R4-40C	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78A

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	40	V
V_{GS}	gate-source voltage	Pulsed	<u>[1]</u>	-20	20	V
		DC	[2]	-16	16	V
I _D	drain current	T_{mb} = 25 °C; V_{GS} = 10 V; see <u>Figure 1</u>	[3]	-	100	Α
		T_{mb} = 100 °C; V_{GS} = 10 V; see Figure 1	[3]	-	100	Α
I _{DM}	peak drain current	T_{mb} = 25 °C; $t_p \le 10 \mu s$; pulsed; see <u>Figure 3</u>		-	657	А
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>		-	204	W
T _{stg}	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
Source-drain	diode					
Is	source current	T _{mb} = 25 °C	[3]	-	100	Α
I _{SM}	peak source current	$t_p \le 10 \ \mu s$; pulsed; $T_{mb} = 25 \ ^{\circ}C$		-	657	Α
Avalanche rug	ggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I_D = 100 A; $V_{sup} \le$ 40 V; R_{GS} = 50 Ω ; V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; unclamped		-	368	mJ
E _{DS(AL)R}	repetitive drain-source avalanche energy		[4][5][6]	-	-	J

^[1] Accumulated pulse duration not to exceed 5mins.

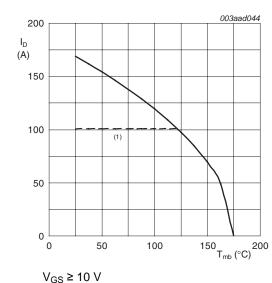
^{[2] -16}V accumulated duration not to exceed 168 hrs.

^[3] Continuous current is limited by package.

^[4] Single-pulse avalanche rating limited by maximum junction temperature of 175 °C.

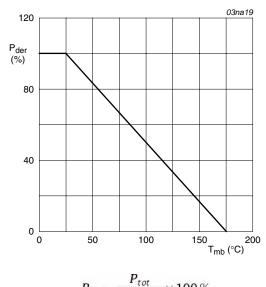
^[5] Repetitive avalanche rating limited by an average junction temperature of 170 °C.

^[6] Refer to application note AN10273 for further information.



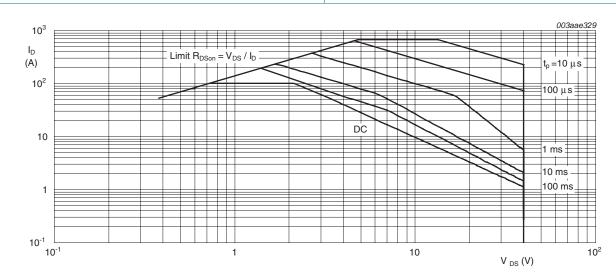
(1) Capped at 100 A due to package.

Continuous drain current as a function of mounting base temperature



 $P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100\%$

Fig 2. Normalized total power dissipation as a function of mounting base temperature



T_{mb} = 25 °C; I_{DM} is a single pulse

Fig 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	see Figure 4	-	-	0.74	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	vertical in free air	-	60	-	K/W

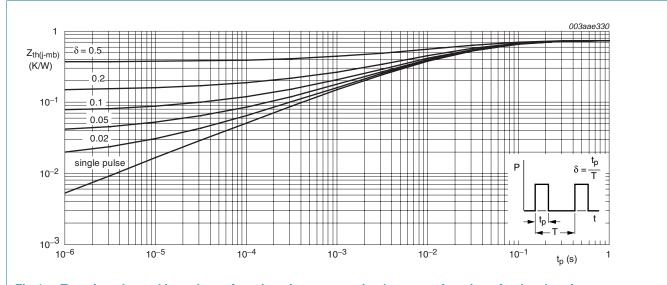


Fig 4. Transient thermal impedance from junction to mounting base as a function of pulse duration

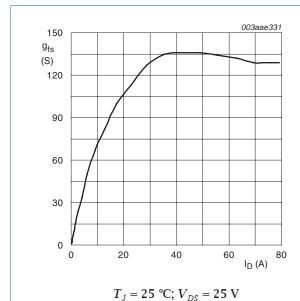
6. Characteristics

Table 6. Characteristics

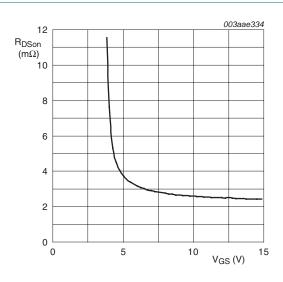
Table 6.	Characteristics						
Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
Static cha	aracteristics						
V _{(BR)DSS}	drain-source breakdown	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	40	-	-	V	
	voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = -55 °C$	36	-	-	V	
$V_{GS(th)}$ ga		$I_D = 1$ mA; $V_{DS} = V_{GS}$; $T_j = 25$ °C; see <u>Figure 9</u> ; see <u>Figure 10</u>	1.8	2.3	2.8	V	
		$I_D = 1$ mA; $V_{DS} = V_{GS}$; $T_j = -55$ °C; see Figure 10	-	-	3.3	V	
		I_D = 2.5 mA; V_{DS} = V_{GS} ; T_j = 175 °C; see Figure 10	0.8	-	-	V	
I _{DSS}	drain leakage current	$V_{DS} = 40 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 \text{ °C}$	-	-	500	μΑ	
		$V_{DS} = 40 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.02	1	μΑ	
I_{GSS}	gate leakage current	$V_{DS} = 0 \text{ V}; V_{GS} = 20 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	2	100	nΑ	
		$V_{DS} = 0 \text{ V}; V_{GS} = -20 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nΑ	
R _{DSon} drain-source on-state resistance	R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C};$ see Figure 11	-	3.05	3.6	mΩ
		$V_{GS} = 5 \text{ V}; I_D = 25 \text{ A}; T_j = 25 ^{\circ}\text{C};$ see Figure 11	-	4.2	5.3	mΩ	
		$V_{GS} = 4.5 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C};$ see Figure 11	-	4.5	6	mΩ	
	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 175 ^{\circ}\text{C};$ see Figure 12; see Figure 11	•	-	-	7.6	mΩ	
Dynamic	characteristics						
$Q_{G(tot)}$	total gate charge	$I_D = 25 \text{ A}$; $V_{DS} = 32 \text{ V}$; $V_{GS} = 10 \text{ V}$; see Figure 13; see Figure 14	-	125	-	nC	
		$I_D = 25 \text{ A}$; $V_{DS} = 32 \text{ V}$; $V_{GS} = 5 \text{ V}$; see Figure 14	-	71	-	nC	
Q _{GS}	gate-source charge	$I_D = 25 \text{ A}; V_{DS} = 32 \text{ V}; V_{GS} = 10 \text{ V};$	-	23	-	nC	
Q_{GD}	gate-drain charge	see Figure 13; see Figure 14	-	42	-	nC	
C _{iss}	input capacitance	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz};$	-	6016	8020	pF	
C _{oss}	output capacitance	T _j = 25 °C; see <u>Figure 15</u>	-	739	870	pF	
C _{rss}	reverse transfer capacitance		-	510	700	pF	
t _{d(on)}	turn-on delay time	V_{DS} = 30 V; R_L = 1.2 Ω ; V_{GS} = 10 V;	-	40	-	ns	
t _r	rise time	$R_{G(ext)} = 10 \Omega$	-	87	-	ns	
t _{d(off)}	turn-off delay time		-	224	-	ns	
t _f	fall time		-	117	-	ns	
L _D	internal drain inductance	from drain lead 6 mm from package to centre of die ; $T_j = 25 ^{\circ}\text{C}$	-	4.5	-	nΗ	
L _S	internal source inductance	from source lead to source bond pad ; $T_j = 25 ^{\circ}\text{C}$	-	7.5	-	nΗ	

Table 6. Characteristics ... continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Source-drain diode						
V _{SD}	source-drain voltage	$I_S = 25 \text{ A}$; $V_{GS} = 0 \text{ V}$; $T_j = 25 \text{ °C}$; see Figure 16	-	0.8	1.2	V
t _{rr}	reverse recovery time	$I_S = 20 \text{ A}$; $dI_S/dt = -100 \text{ A/}\mu\text{s}$;	-	48	-	ns
Q _r	recovered charge	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}$	-	82	-	nC



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$$T_j = 25$$
 °C; $I_D = 25$ A

Fig 6. Drain-source on-state resistance as a function

Fig 5. Forward transconductance as a function of drain current; typical values

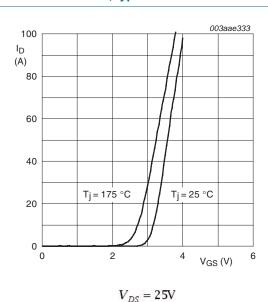


Fig 7. Transfer characteristics: drain current as a function of gate-source voltage; typical values

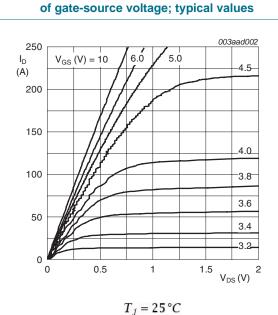


Fig 8. Output characteristics: drain current as a function of drain-source voltage; typical values

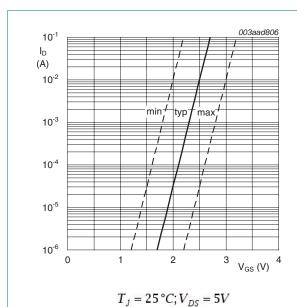


Fig 9. Sub-threshold drain current as a function of gate-source voltage

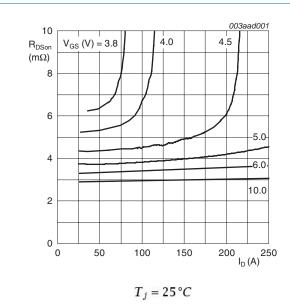
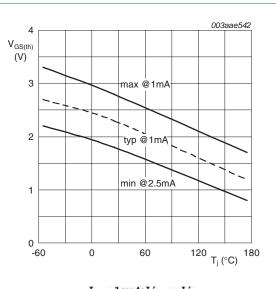


Fig 11. Drain-source on-state resistance as a function of drain current; typical values



 $I_D = 1 mA; V_{DS} = V_{GS}$

Fig 10. Gate-source threshold voltage as a function of junction temperature

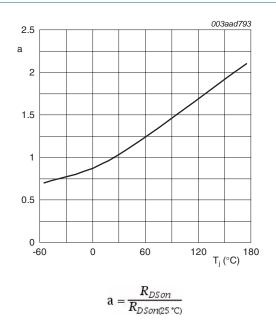


Fig 12. Normalized drain-source on-state resistance factor as a function of junction temperature

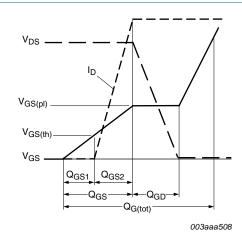
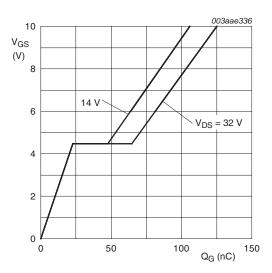
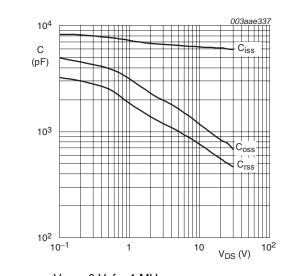


Fig 13. Gate charge waveform definitions



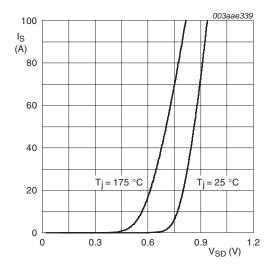
 $T_i = 25 \, ^{\circ}C; I_D = 25 \, A$

Fig 14. Gate-source voltage as a function of gate charge; typical values



 $V_{GS} = 0 \text{ V}; f = 1 \text{ MHz}$

Fig 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



 $V_{GS} = 0 V$

Fig 16. Source (diode forward) current as a function of source-drain (diode forward) voltage; typical values

Package outline

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB

SOT78A

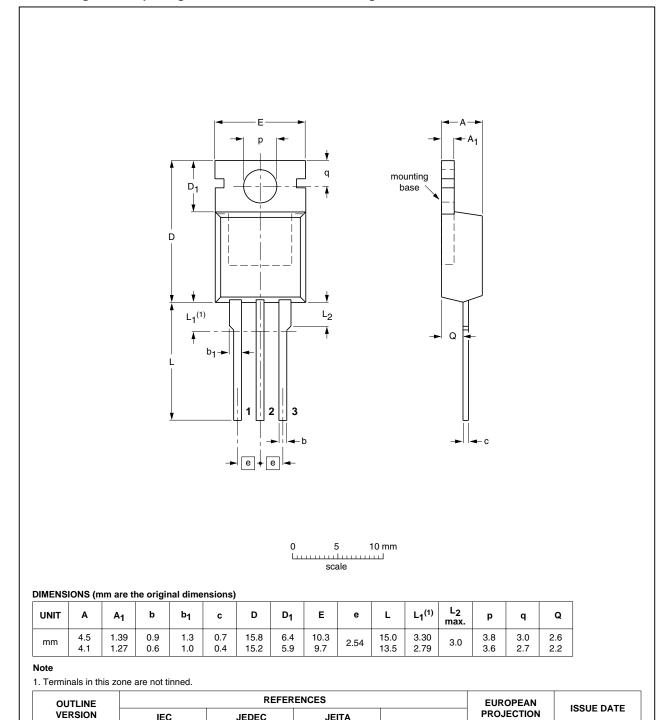


Fig 17. Package outline SOT78A (TO-220AB)

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3-lead TO-220AB

SOT78A

8. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BUK653R4-40C v.2	20101012	Product data sheet	-	BUK653R4-40C v.1
Modifications:	 Status change 	d from objective to product.		
	 Various change 	es to content.		
BUK653R4-40C v.1	20100520	Objective data sheet	-	-

9. Legal information

9.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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BUK653R4-40C

BUK653R4-40C

N-channel TrenchMOS intermediate level FET

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11. Contents

1	Product profile
1.1	General description
1.2	Features and benefits
1.3	Applications1
1.4	Quick reference data1
2	Pinning information2
3	Ordering information2
4	Limiting values3
5	Thermal characteristics5
6	Characteristics6
7	Package outline
8	Revision history11
9	Legal information12
9.1	Data sheet status
9.2	Definitions12
9.3	Disclaimers
9.4	Trademarks
10	Contact information13

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