INNOLUX DISPLAY CORPORATION

LCD MODULE SPECIFICATION

Customer: _____

Model Name: AT080TN03 V.1

SPEC NO.: A080-03-TT-15

Date: 2007/04/18

Version: 05

☐ Preliminary Specification

■ Final Specification

Remark
■ LCM (75mm light bar length)

For Customer's Acceptance

Approved by	Comment

Approved by	Reviewed by	Prepared by
		陳格文 4/23/07

INNOLUX

Record of Revision

Version	Revise Date	Page	Content
Final Spec. 01	2006/04/03		
02	2006/06/26	1	Modify Backlight Power Consumption.
		9	Modify Note2 of Backlight Driving Conditions.
		10	Add Power Sequence.
		19	Modify Luminance from Typ. “250 nits” to “300 nits”.
03	2006/09/25	2	Add connector type
		3	Modify STHL&STHR in pin assignment
		4	Modify Note 1 in pin assignment
		7	Add LED reverse voltage& forward current
		9	Modify backlight driving conditions
		10	Modify power sequence
		11	Modify timing conditions
		13	Modify timing diagram
		20	Modify Luminance from Typ. “300 nits” to “250 nits”.
		26	Modify mechanical drawing
04	2007/01/16	10	Add note to power sequence
		24	Add note 4
		26	Modify LED light bar length from “75” to “125”
			Modify fasten-tape of mechanical drawing
05	2007/04/18	26	Modify LED light bar length from “125” to “75”

INNOLUX

Contents

1. General Specifications

No.	Item	Specification	Remark
1	LCD size	8.0 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	800X3(RGB)X480	
4	Display mode	Normally White, Transmissive	
5	Dot pitch	0.0736(W)X0.2070(H) mm	
6	Active area	176.64(W)X99.36(H) mm	
7	Module size	192.8(W)X116.9(H)X6.4(D) mm	Note 1
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	Digital	
11	Backlight Power consumption	1.782W (Typ.)	
12	Panel Power consumption	268mW (Typ.)	
13	Weight	245g(Typ.)	

Note 1: Refer to Mechanical Drawing.

2. Pin Assignment

2.1. TFT LCD Panel Driving Section

FPC connector is used for the module electronics interface. The recommended model is P-TWO “AF 730L-A2G1T” manufactured by P-TWO.

Pin No.	Symbol	I/O	Function	Remark
1	POL	I	Polarity selection	
2	STVD	I/O	Vertical start pulse input when U/D= H	Note 1
3	OEV	I	Output enable	
4	CKV	I	Vertical clock	
5	STVU	I/O	Vertical start pulse input when U/D= L	Note 1
6	GND	P	Power ground	
7	EDGSL	I	Select rising edge or falling edge	
8	V_{CC}	P	Power supply for digital circuit	
9	V9	I	Gamma voltage level 9	
10	V_{GL}	P	Gate OFF voltage	
11	V2	I	Gamma voltage level 2	
12	V_{GH}	P	Gate ON voltage	
13	V6	I	Gamma voltage level 6	
14	U/D	I	Up/down selection	Note 1,2
15	V_{COM}	I	Common voltage	
16	GND	P	Power ground	
17	AV_{DD}	P	Power supply for analog circuit	
18	V14	I	Gamma voltage level 14	
19	V11	I	Gamma voltage level 11	

20	V8	I	Gamma voltage level 8	
21	V5	I	Gamma voltage level 5	
22	V3	I	Gamma voltage level 3	
23	GND	P	Power ground	
24	R5	I	Red data(MSB)	
25	R4	I	Red data	
26	R3	I	Red data	
27	R2	I	Red data	
28	R1	I	Red data	
29	R0	I	Red data(LSB)	
30	GND	P	Power ground	
31	GND	P	Power ground	
32	G5	I	Green data(MSB)	
33	G4	I	Green data	
34	G3	I	Green data	
35	G2	I	Green data	
36	G1	I	Green data	
37	G0	I	Green data(LSB)	
38	STHL	I/O	Horizontal start pulse input when R/L = L	Note 1
39	REV	I	Control signal are inverted or not	Note3
40	GND	I	Power ground	
41	DCLK	I	Sample clock	
42	V_{CC}	P	Power supply for digital circuit	
43	STHR	I/O	Horizontal start pulse input when R/L = H	Note 1
44	LD	I	Latches the polarity of outputs and switches the new data to outputs	

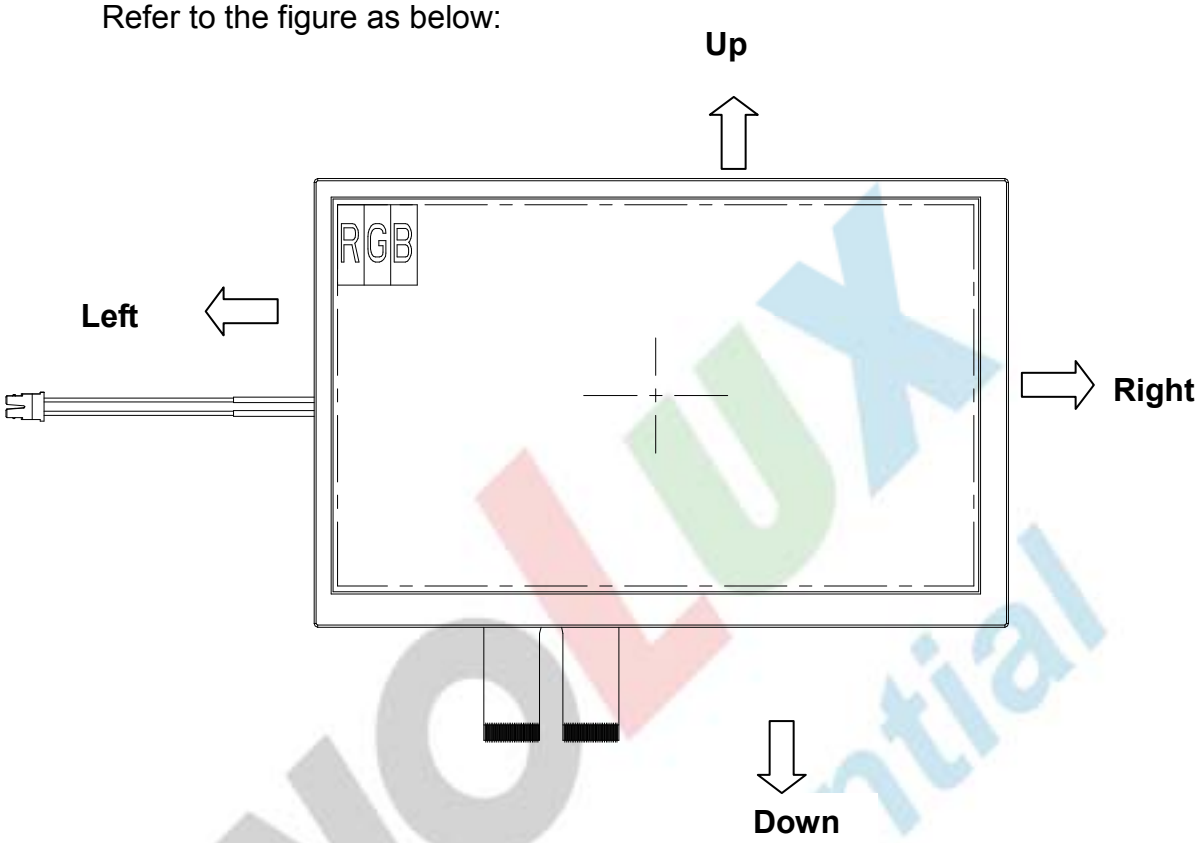
45	B5	I	Blue data (MSB)	
46	B4	I	Blue data	
47	B3	I	Blue data	
48	B2	I	Blue data	
49	B1	I	Blue data	
50	B0	I	Blue data (LSB)	
51	R/L	I	Right/ left selection	Note 1,2
52	V1	I	Gamma voltage level 1	
53	V4	I	Gamma voltage level 4	
54	V7	I	Gamma voltage level 7	
55	V10	I	Gamma voltage level 10	
56	V12	I	Gamma voltage level 12	
57	V13	I	Gamma voltage level 13	
58	AV_{DD}	P	Power supply for analog circuit	
59	GND	P	Power ground	
60	V_{COM}	I	Common voltage	

I: input, O: output, P: Power

Note 1: Selection of scanning mode

Setting of scan control input		IN/OUT state for start pulse				Scanning direction
U/D	R/L	STVD	STVU	STHR	STHL	
GND	V_{CC}	O	I	I	O	Up to down, left to right
V_{CC}	GND	I	O	O	I	Down to up, right to left
GND	GND	O	I	O	I	Up to down, right to left
V_{CC}	V_{CC}	I	O	I	O	Down to up, left to right

Note 2: Definition of scanning direction.
Refer to the figure as below:



Note 3: When REV="L", normally
REV="H", these data will be inverted.

2.2. Backlight Unit Section

LED connector is used for the integral backlight system. The recommended model is JST “BHSR-02VS-1” manufactured by JST.

Pin No.	Symbol	I/O	Function	Remark
1	HI	P	Power supply for backlight unit	Pink
2	GND	P	Ground for backlight unit	White

3. Operation Specifications

3.1. Absolute Maximum Rating

(GND=AV_{SS}=0V, Note 2)

Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power voltage	V_{CC}	-0.3	5.0	V	
	AV_{DD}	-0.5	13.5	V	
	V_{GH}	-0.3	18.0	V	
	V_{GL}	-15.0	0.3	V	
	V_{GH}-V_{GL}	-	33.0	V	
Input signal voltage	V1~V7	0.4 AV_{DD}	AV_{DD}+0.3	V	Note 1
	V8~V14	-0.3	0.6AV_{DD}	V	Note 1
Operation Temperature	T_{OP}	-30	85	℃	
Storage Temperature	T_{ST}	-30	85	℃	
LED Reverse Voltage	Vr	-	1.2	V	Each LED Note 3
LED Forward Current	I_f	-	25	mA	Each LED

Note 1: AV_{DD}-0.1≥ V1≥ V2≥ V3≥ V4≥ V5≥ V6≥ V7≥ V8≥ V9≥ V10≥ V11≥ V12≥ V13≥ V14≥ AV_{SS}+0.1

Note 2: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 3: Vr conditions: Zener Diode 20mA.

3.1.1. Typical Operation Conditions

(GND=AV_{SS}=0V, Note 1)

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	V_{CC}	3.0	3.3	3.6	V	Note 2
	AV_{DD}	9.8	10.0	10.2	V	
	V_{GH}	14.3	15.0	15.7	V	
	V_{GL}	-10.5	-10.0	-9.5	V	
Input signal voltage	V_{COM}	-	4.1	-	V	
	V1~V7	0.4 A_{VDD}	-	A_{VDD} -0.1	V	
	V8~V14	0.1	-	0.6 A_{VDD}	V	
Input logic high voltage	V_{IH}	0.7V_{CC}	-	V_{CC}	V	Note 3
Input logic low voltage	V_{IL}	0	-	0.3V_{CC}	V	

Note 1: Be sure to apply V_{CC} and V_{GL} to the LCD first, and then apply V_{GH}.
Note 2: V_{CC} setting should match the signals output voltage (refer to Note 3) of customer's system board.
Note 3: STHL, STHR, OEH, L/R, CPH1~CPH3, STVD, STVU, OEV, CKV, U/D.

3.1.2. Current Consumption

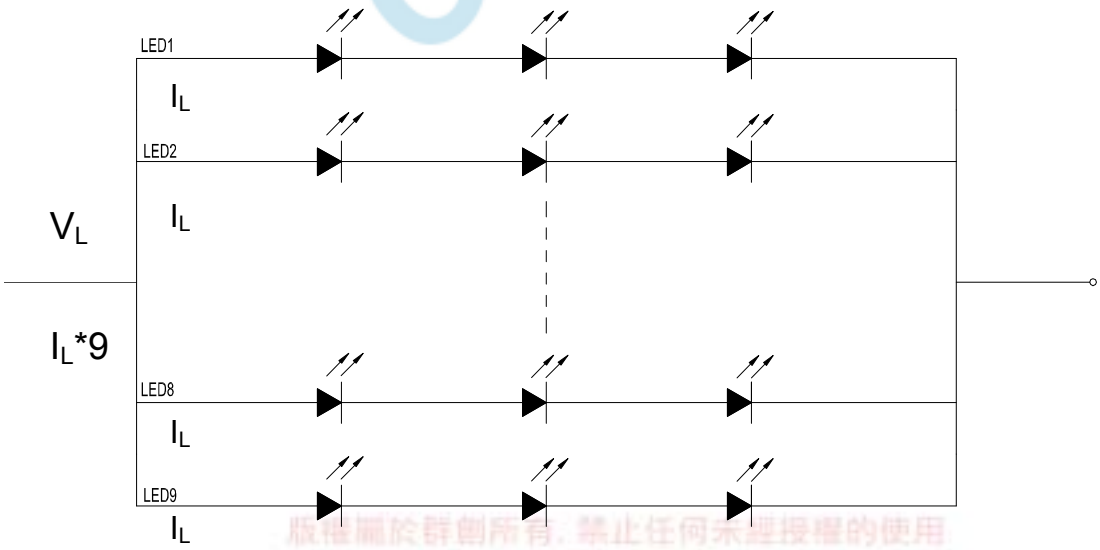
(GND=AV_{SS}=0V)

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Current for Driver	I_{GH}	-	0.2	0.5	mA	V_{GH} =15.0V
	I_{GL}	-	0.2	1.0	mA	V_{GL} = -10.0V
	I_{CC}	-	4.0	10.0	mA	V_{CC} =3.3V
	IAV_{DD}	-	25.0	50.0	mA	AV_{DD} =10.0V

3.1.3. Backlight Driving Conditions

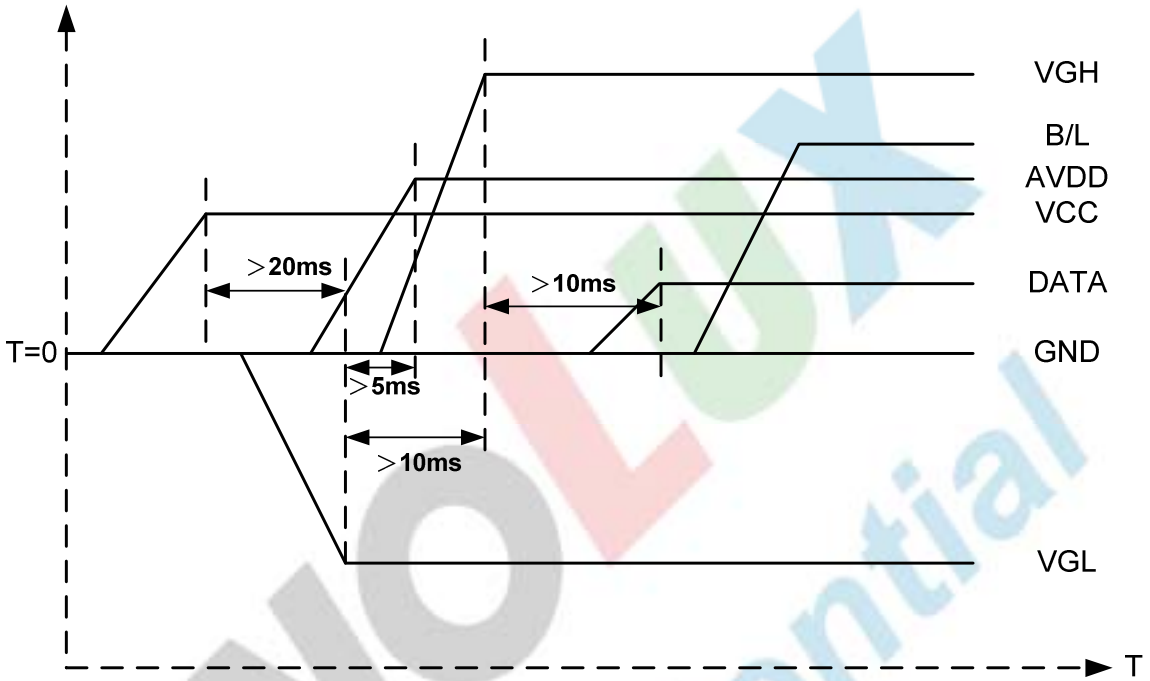
Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
LED forward voltage	V_L	9.3	9.9	10.5	V	Note2, 3
LED forward current	I_L	18	20	22	mA	Note 3
LED life time	-	20,000	-	-	Hr	Note 1

Note 1: The “LED life time” is defined as the module brightness decrease to 50% original brightness at Ta=25℃ and I_L =20mA. The LED lifetime could be decreased if operating I_L is larger than 20 mA.
Note 2: The LED Supply Voltage is defined by the number of LED at Ta=25℃ and I_L =20mA. In the case of 3pcs LED, V_L=3.3*3=9.9V.
Note 3: The LED driving condition is defined for each LED module (3 LED Serial).



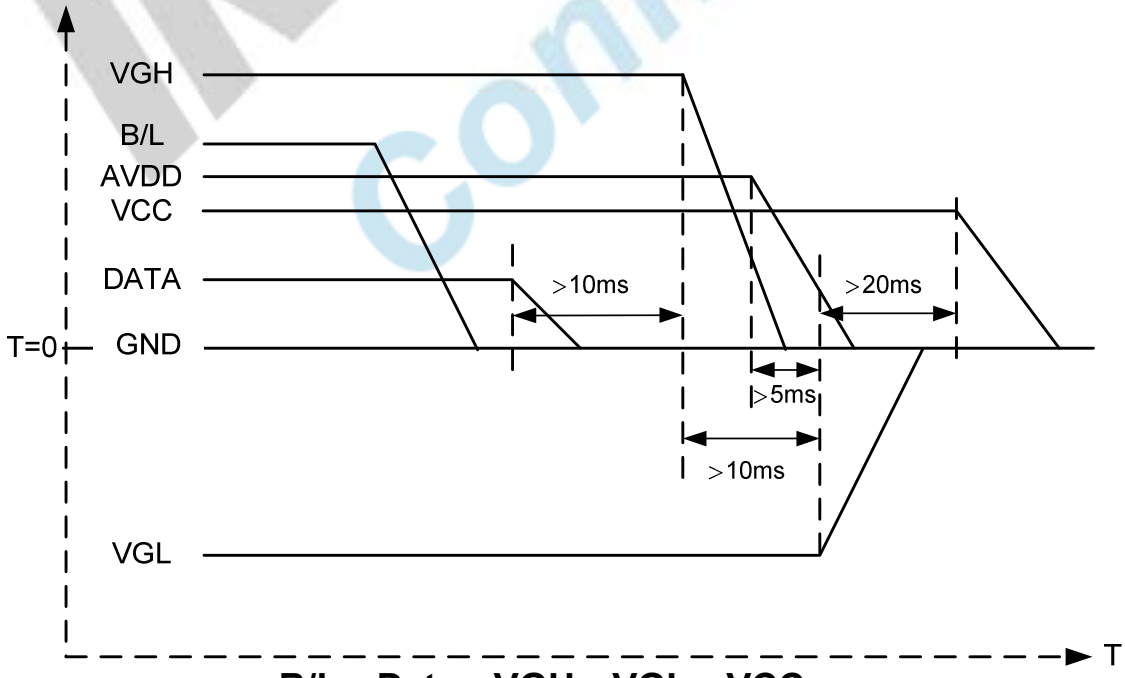
3.2. Power Sequence

3.2.1. Power on:



VCC→VGL→VGH→Data→B/L

3.2.2. Power off:



B/L→Data→VGH→VGL→VCC

Note: Data include POL, STVD, OEV, CKV, STVU, R0~R5, B0~B5, GO~G5, STHL, REV, DCLK, STHR, LD.

3.3. Timing Characteristics

3.3.1. Timing Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
DCLK frequency	Fdclk	-	40	45	MHz	
DCLK cycle	Tcph	22	25	-	ns	
DCLK pulse width	Tcw	8	-	-	ns	
Data set-up time	Tsu	4	-	-	ns	
Data hold time	Thd	2	-	-	ns	
Time that the last data to LD	Tld	1	-	-	Tcph	
Pulse width of LD	Twld	2	-	-	Tcph	
Time that LD to STHL/R	Tlds	5	-	-	Tcph	
POL set-up time	Tpsu	6	-	-	ns	
POL hold time	Tphd	6	-	-	ns	
CKV frequency	Fvclk	-	-	200	KHz	
CKV rise time	Trck	-	-	100	ns	
CKV falling time	Tfck	-	-	100	ns	
CKV pulse width	PWCLK	500	-	-	ns	
Horizontal display timing range	Tdh	-	800	-	Tcph	
Horizontal timing range	Th	-	1056	-	Tcph	
STVU/D setup time	Tsuv	200	-	-	ns	
STVU/D hold time	Thdv	300	-	-	ns	
STVU/D delay time	Tdt	-	-	500	ns	
Driver output delay time	Tdo	-	-	900	ns	

Output rise time	Ttlh	-	500	1000	ns	
Output falling time	Tthl	-	400	800	ns	
OEV pulse width	Twcl	1	-	-	us	
OEV to Driver output delay time	Toe	-	-	900	ns	
Horizontal lines per field	Tv	512	525	610	Tdh	
Vertical display timing range	Tvd	-	480	-	Tdh	

3.3.2. Timing Diagram

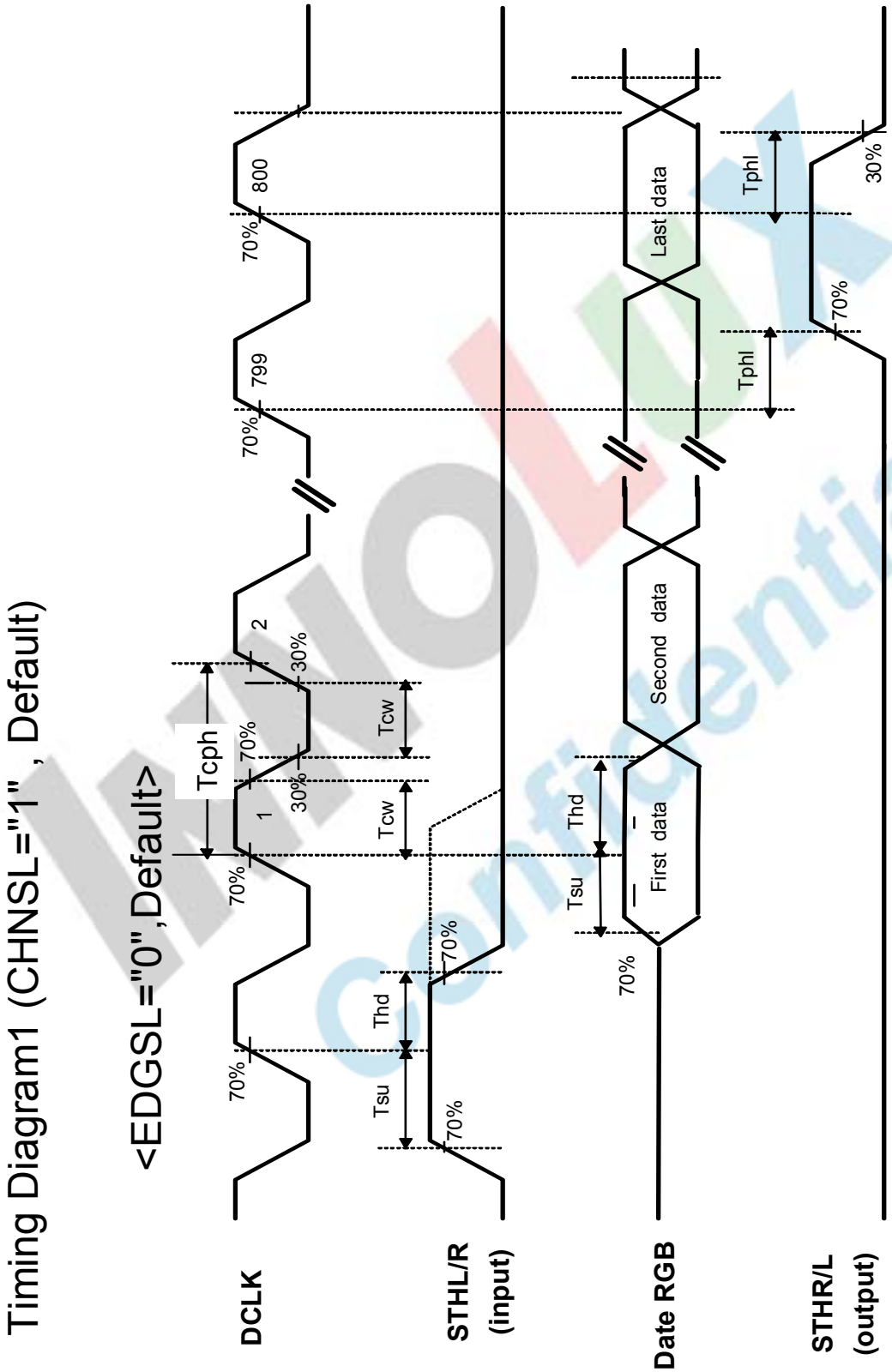


Fig.3-1 operation model 1

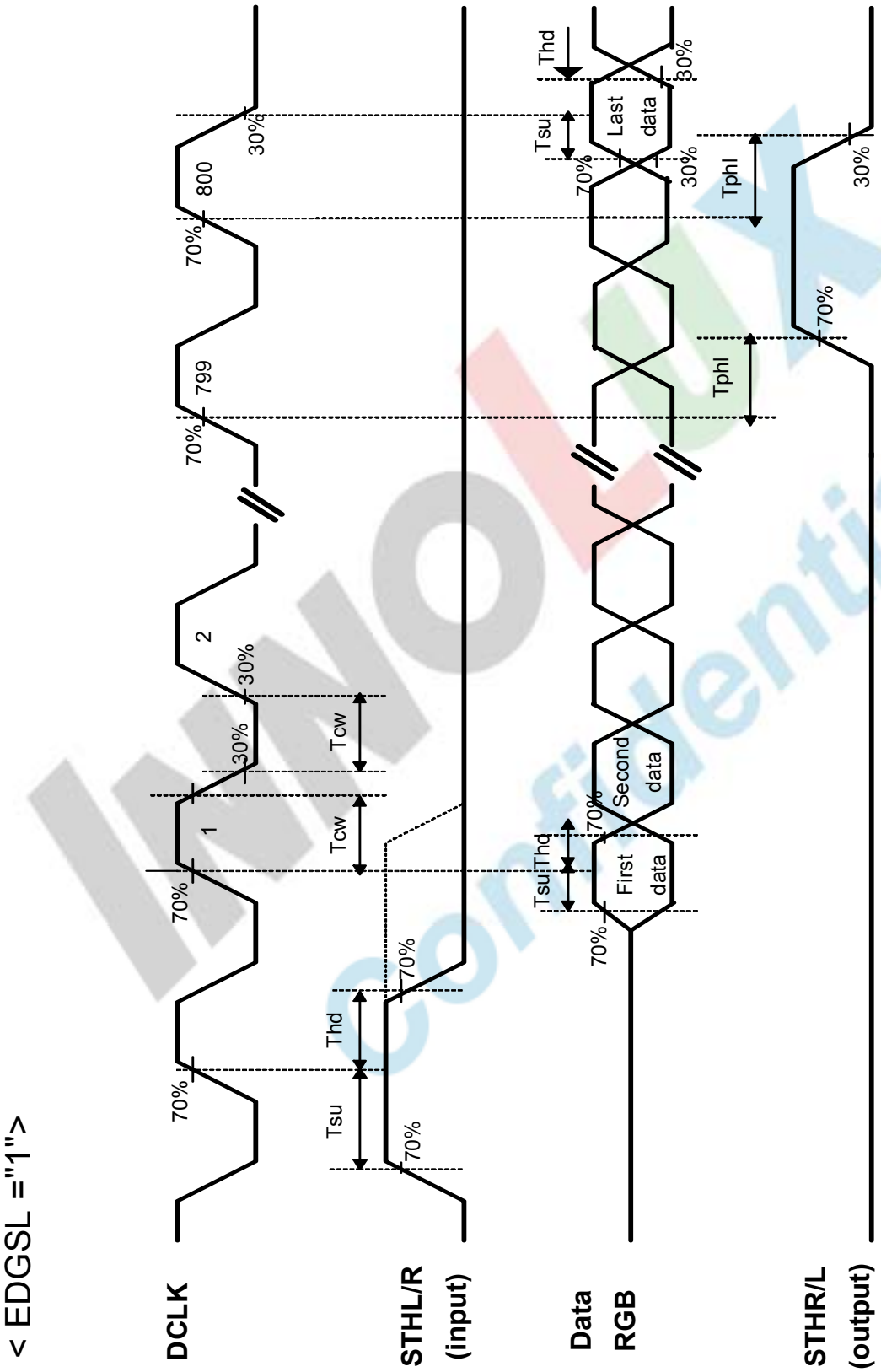


Fig.3-2 operation model 2

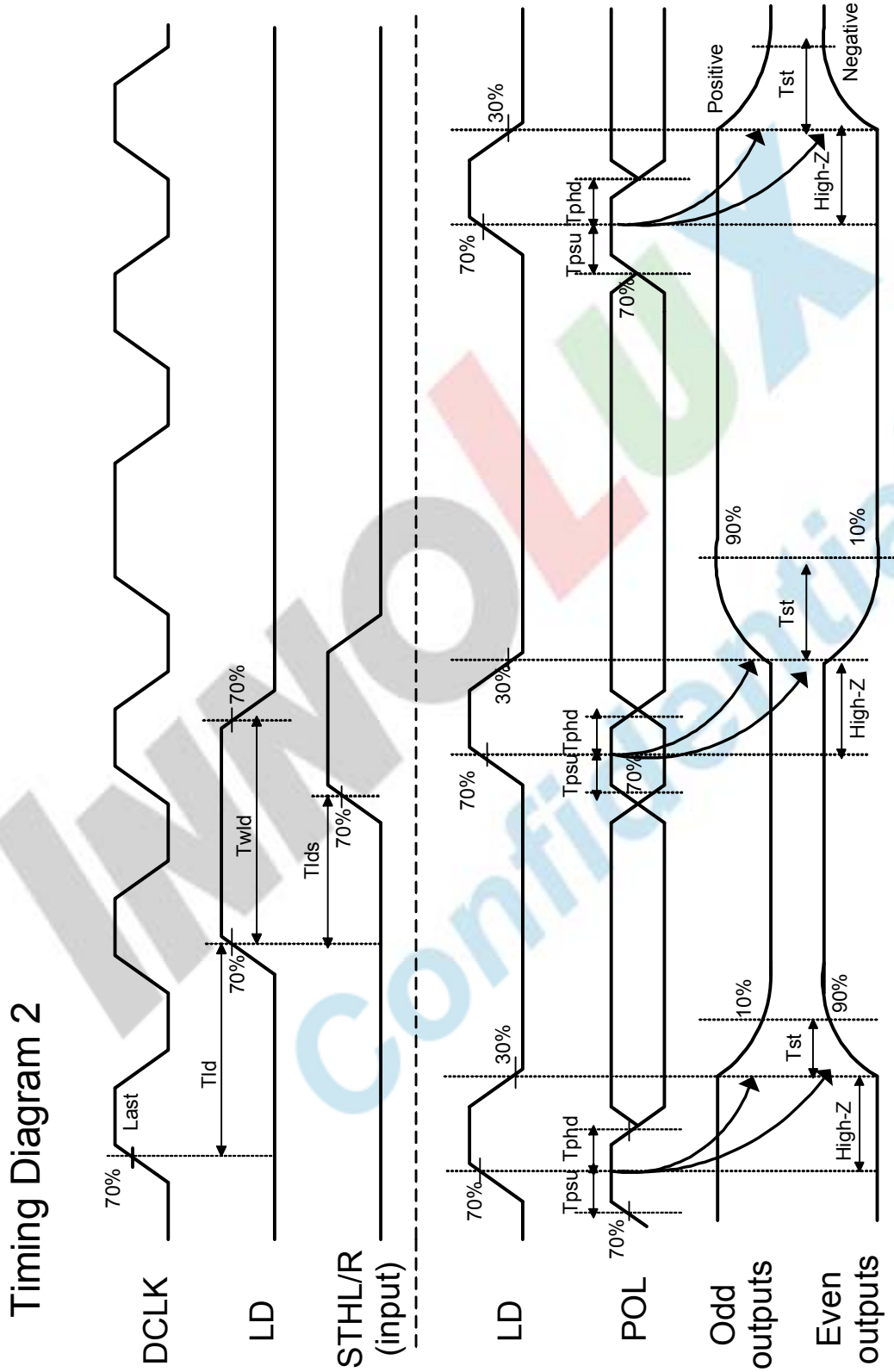


Fig.3-3 Horizontal timing 1

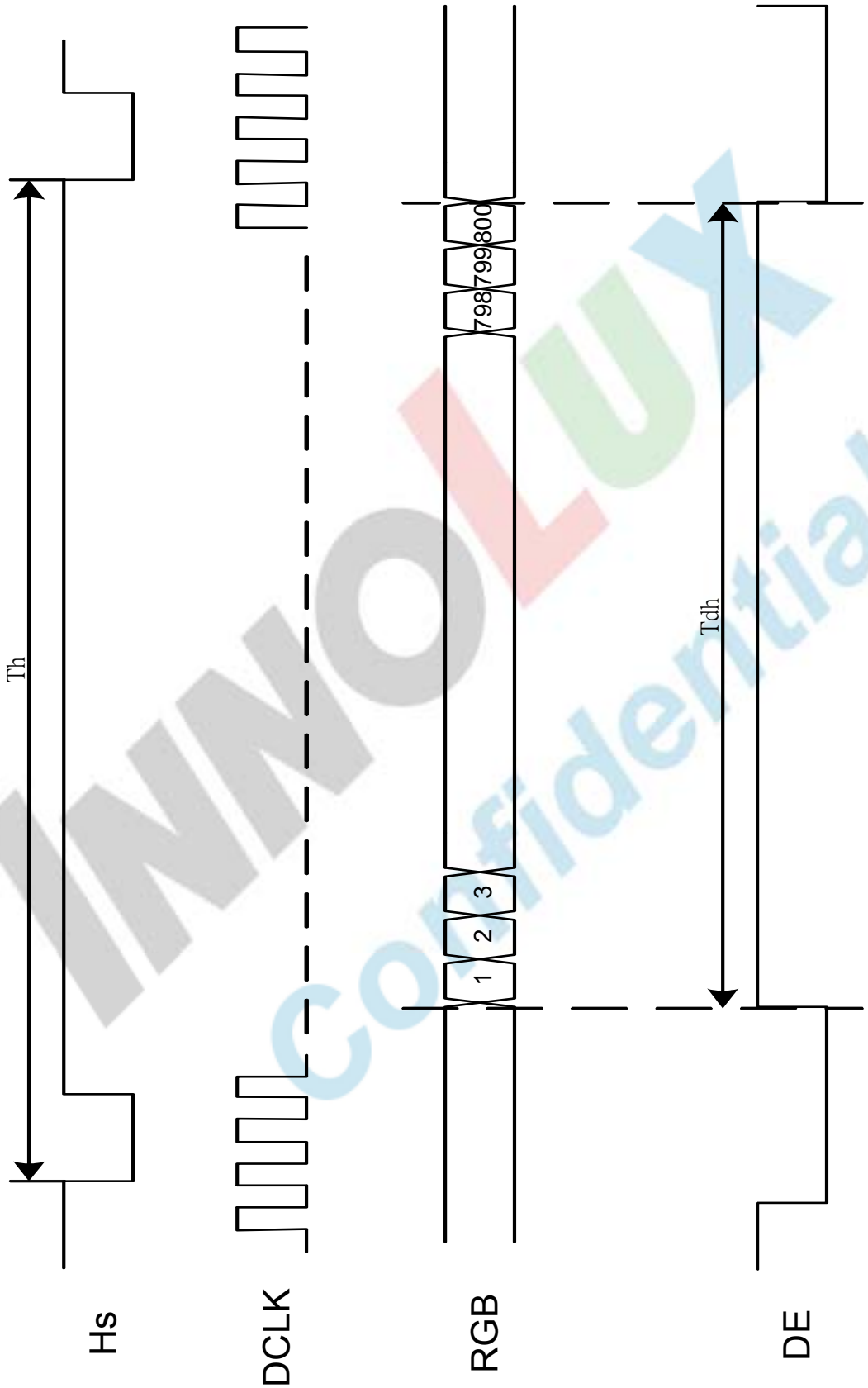


Fig.3-4 Horizontal timing 2

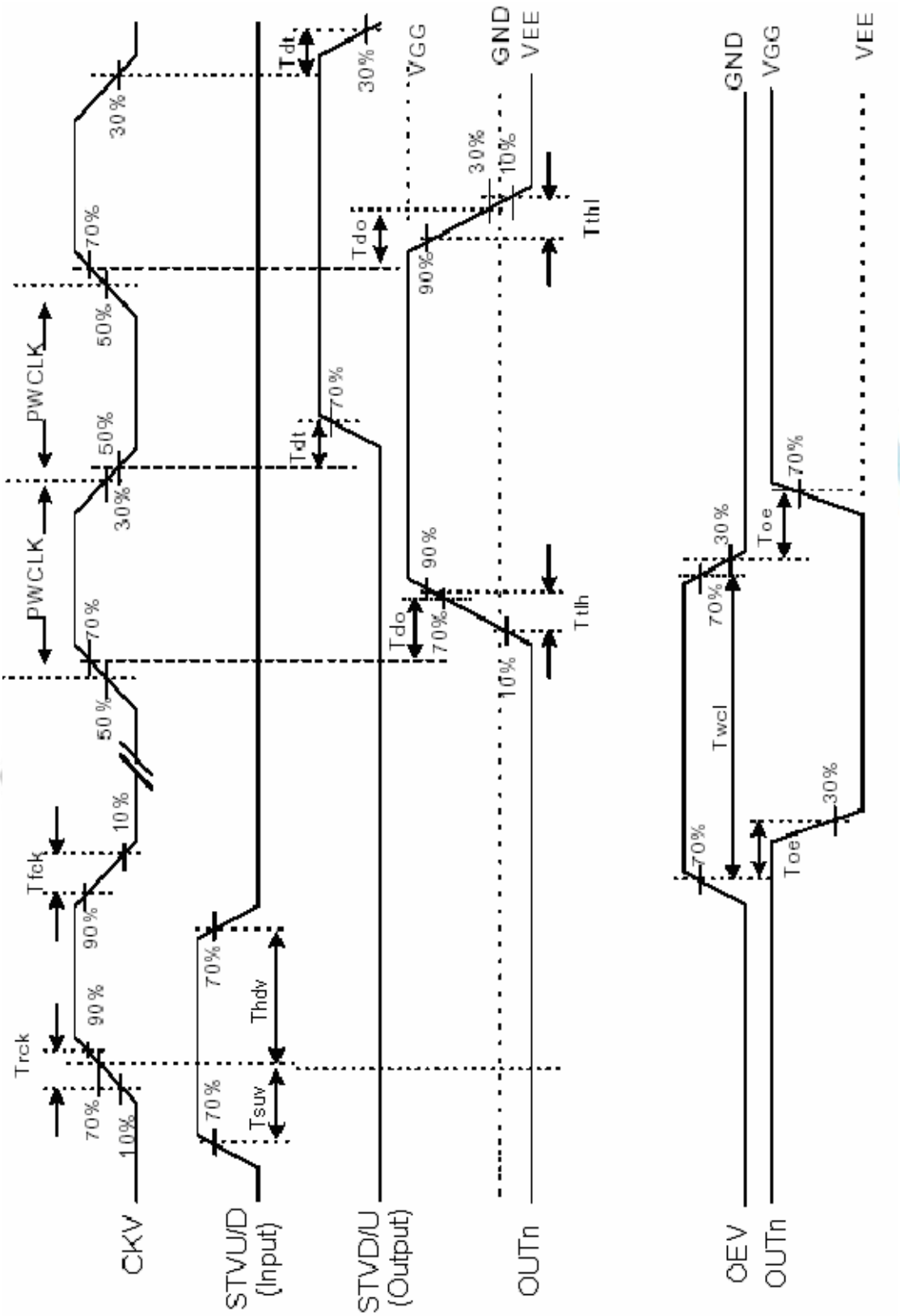


Fig.3-5 Vertical shift clock timing

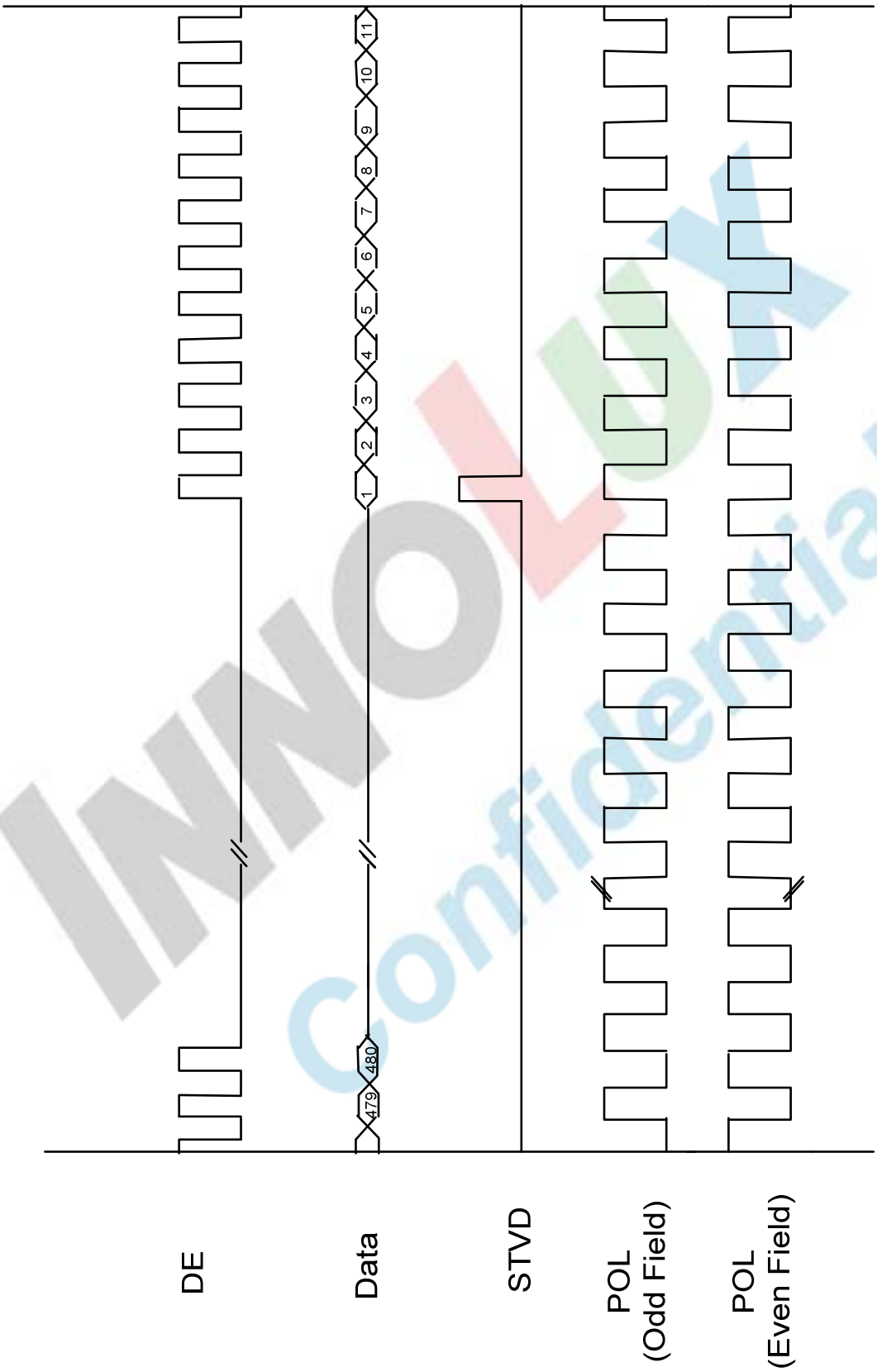


Fig.3-6 Vertical timing (from up to down)

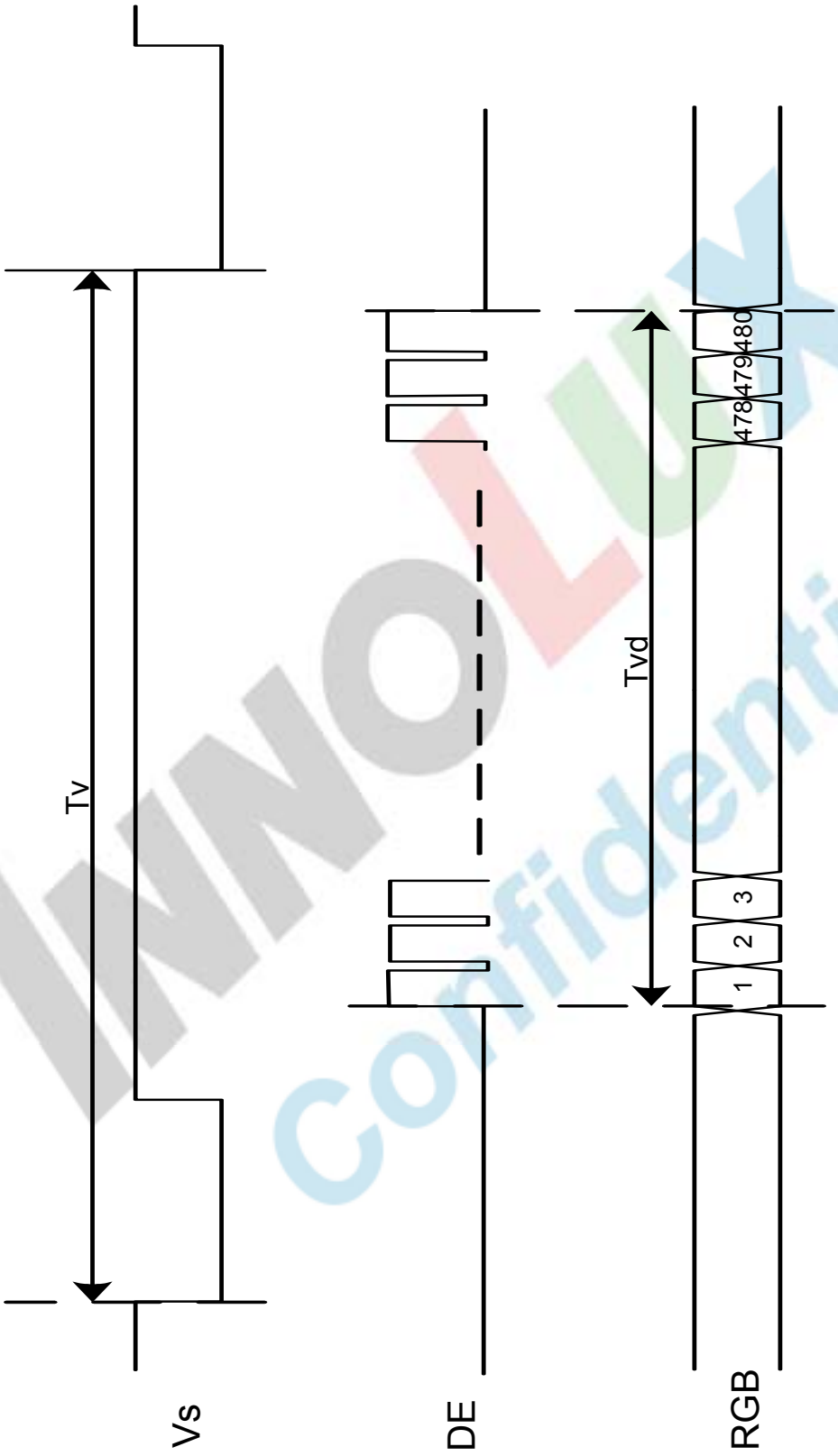


Fig.3-7 Vertical timing

4. Optical Specifications

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing angle (CR≥10)	θ_L	Φ=180°(9 o'clock)	55	65	-	degree	Note 1
	θ_R	Φ=0°(3 o'clock)	55	65	-		
	θ_T	Φ=90°(12 o'clock)	35	45	-		
	θ_B	Φ=270°(6 o'clock)	55	65	-		
Response time	T_{ON}	Normal θ=Φ=0°	-	15	30	msec	Note 3
	T_{OFF}		-	20	40	msec	Note 3
Contrast ratio	CR		250	300	-	-	Note 4
Color chromaticity	W_X		0.26	0.31	0.36	-	Note 2 Note 5 Note 6
	W_Y		0.28	0.33	0.38		
Luminance	L		200	250	-	cd/m^2	Note 6
Luminance uniformity	Y_U		70	75	-	%	Note 7

Test Conditions:

1. V_{CC}=3.3V, I_L*9=180mA (Backlight current), the ambient temperature is 25℃.
2. The test systems refer to Note 2.

Note 1: Definition of viewing angle range

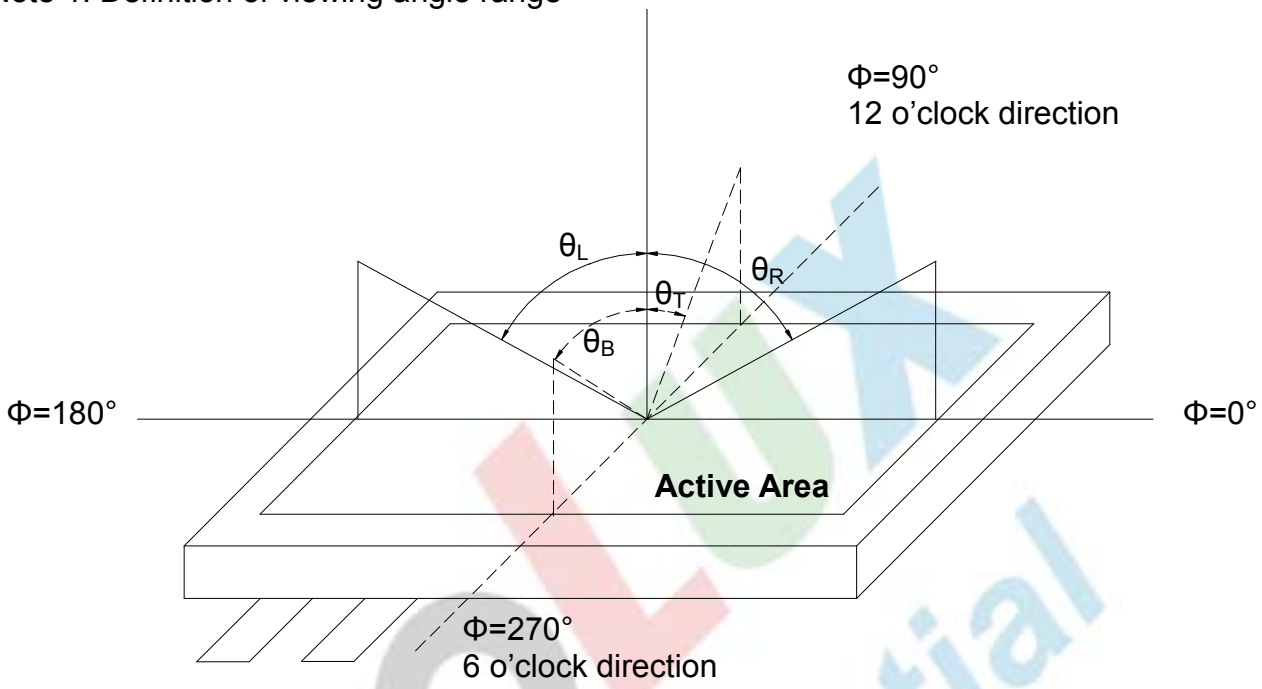


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. The optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

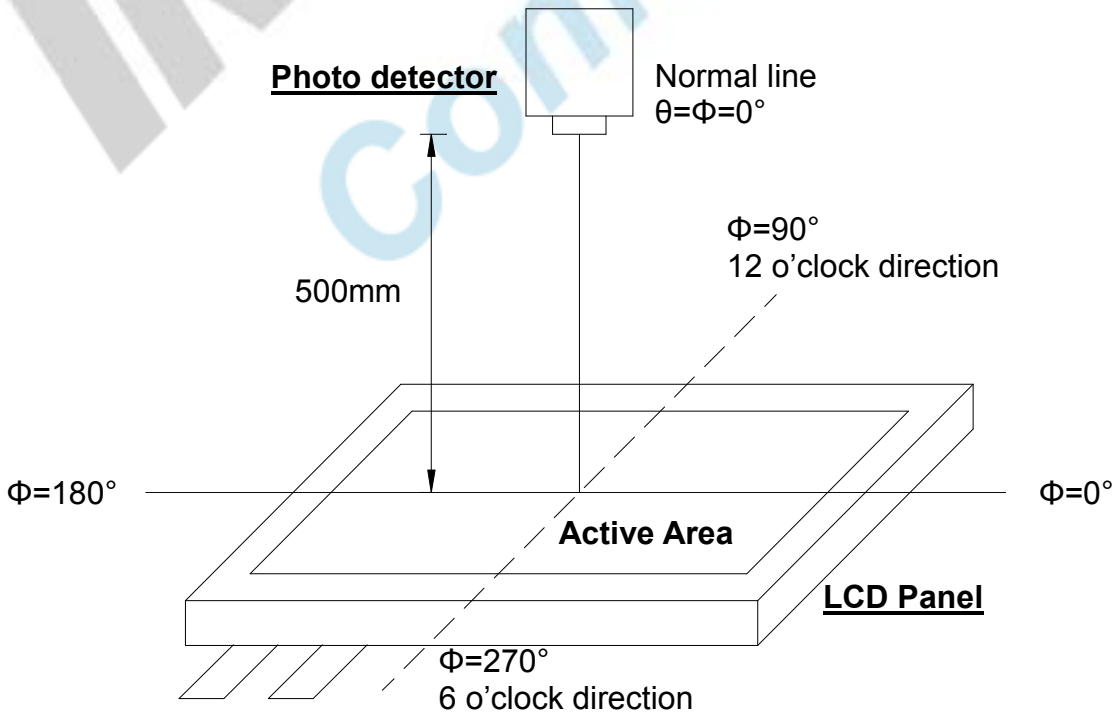


Fig. 4-2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

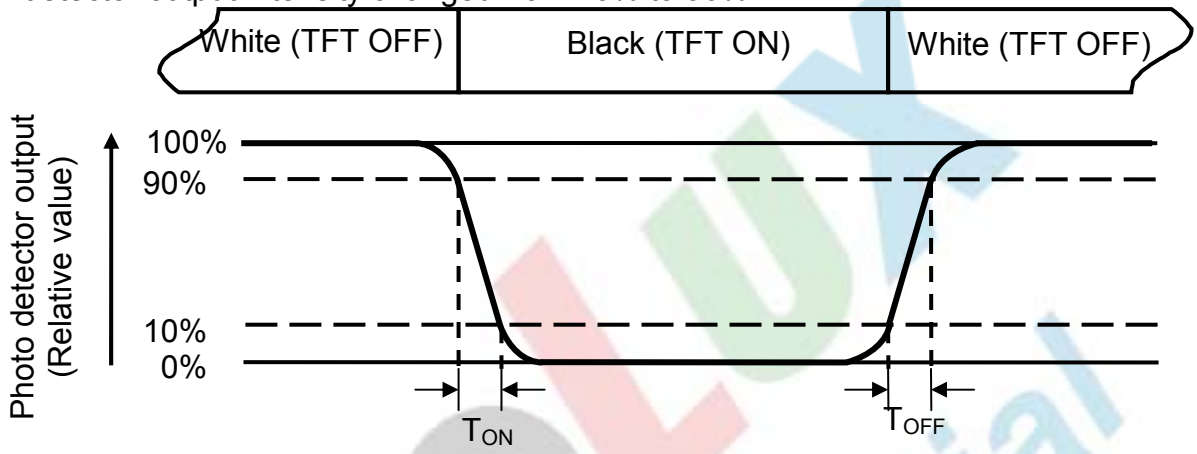


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of color chromaticity (CIE1931)
Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is I_L=20mA of which each LED module is 3 LED serial.

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$Luminance\ \ Uniformity\ (Yu) = \frac{B_{min}}{B_{max}}$$

L-------Active area length W----- Active area width

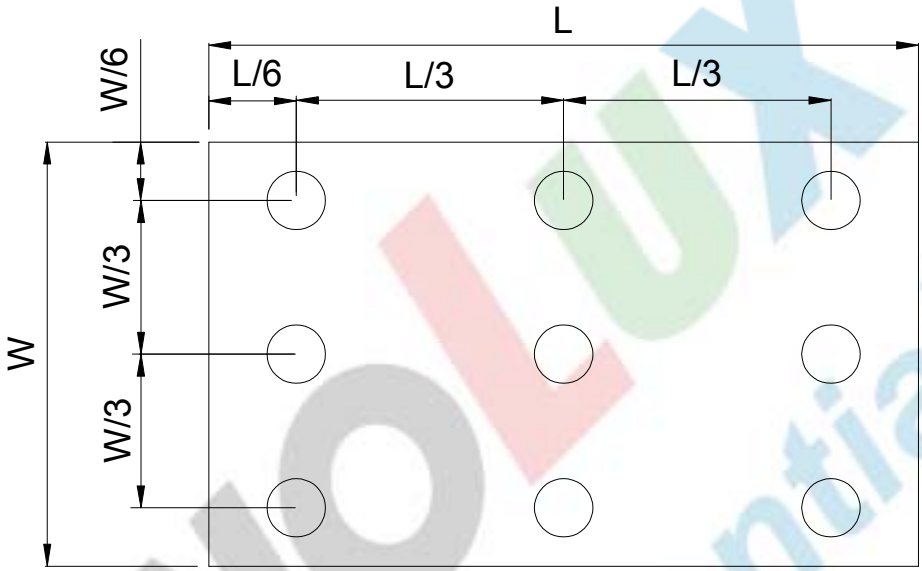


Fig. 4-4 Definition of measuring points

B_{max}: The measured maximum luminance of all measurement position.
B_{min}: The measured minimum luminance of all measurement position.

5. Reliability Test Items

(Note3)

Item	Test Conditions	Remark
High Temperature Storage	Ta = 85℃ 240 hrs	Note 1,Note 4
Low Temperature Storage	Ta = -30℃ 240hrs	Note 1,Note 4
High Temperature Operation	Ts = 85℃ 240hrs	Note 2,Note 4
Low Temperature Operation	Ta = -30℃ 240hrs	Note 1,Note 4
Operate at High Temperature and Humidity	+60℃, 90%RH max. 240 hrs	Note 4
Thermal Shock	-30℃/30 min ~ +85℃/30 min for a total 100 cycles, Start with cold temperature and end with high temperature	Note 4
Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X. Y. Z. (6 hours for total)	
Mechanical Shock	100G 6ms,±X, ±Y, ±Z 3 times for each direction	
Package Vibration Test	Random Vibration : 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y. Z. (6 hours for total)	
Package Drop Test	Height:60 cm 1 corner, 3 edges, 6 surfaces	
Electro Static Discharge	± 2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.
Note 2: Ts is the temperature of panel's surface.
Note 3: In the standard condition, there shall be no practical problem that may affect the display function.
Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

6. General Precautions

6.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

6.2. Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

6.3. Static Electricity

1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

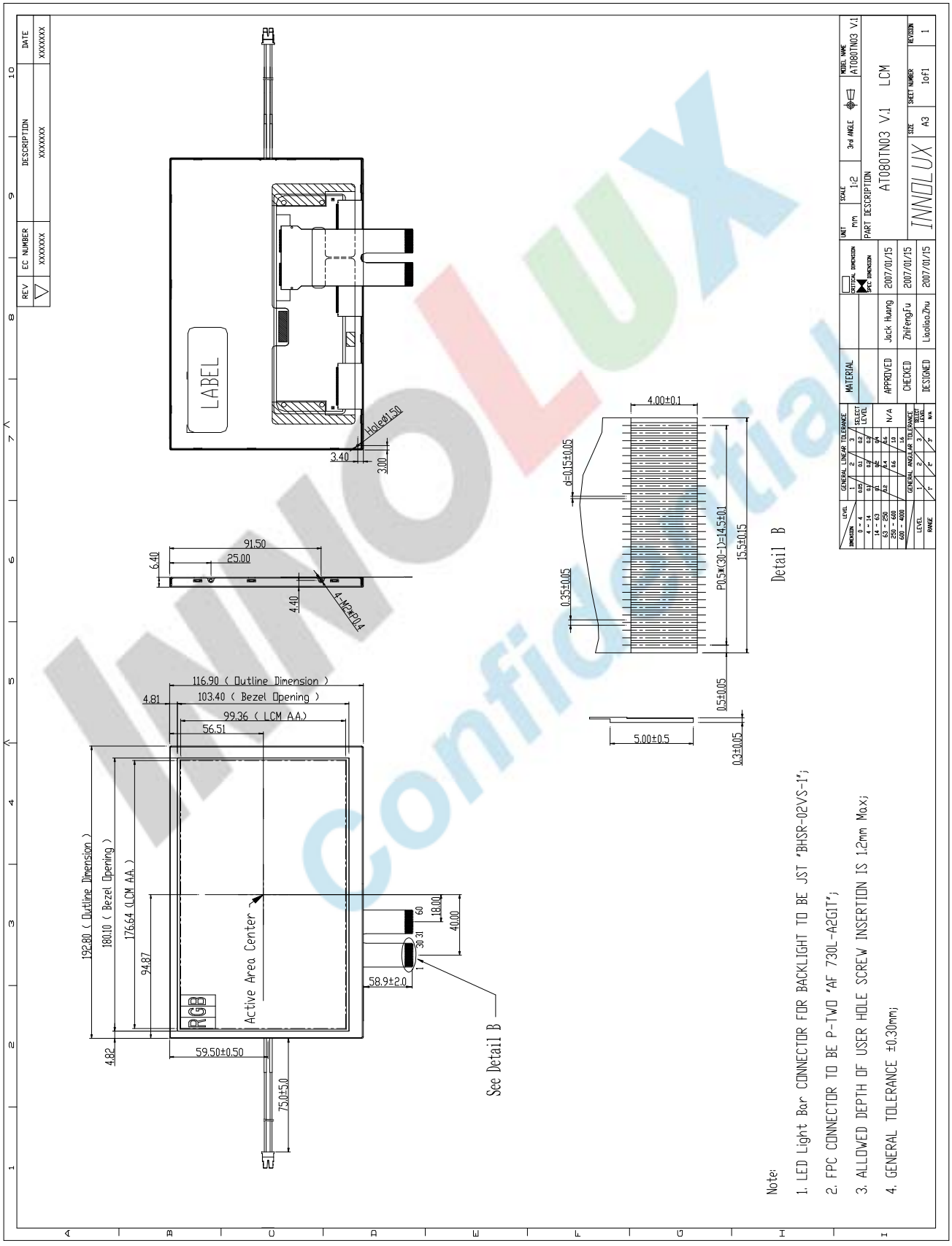
6.4. Storage

1. Store the module in a dark room where must keep at 25±10℃ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

6.5. Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

7. Mechanical Drawing



8. Package Drawing

8.1 Packaging Material Table

No.	Item	Model (Material)	Dimensions(mm)	Unit Weight (kg)	Quantity	Remark
1	LCM Module	AT080TN03 V.1	192.8×116.9×6.4	0.245	30pcs	
2	Partition	BC Corrugated paper	512×349×226	1.161	1set	
3	Corrugated Bar	B Corrugated paper	349X199X52	0.738	4pcs	
4	Dust-Proof Bag	PE	700X530	0.060	1pcs	
5	A/S Bag	PE	205×195×0.2	0.003	30pcs	
6	Carton	Corrugated paper	530*355*255	1.100	1pcs	
7	Total weight	12.710Kg± 5%				

8.2 Packaging Quantity

Total LCM quantity in Carton: no. of Partition 2 Rows x quantity per Row 15 = 30

8.3 Packaging Drawing