

REV.	DESCRIPTION	DATE	DWN	AP'PD
3.0	Add pcb layput	2010.04.20	LYP	WYN
4	EDIT	2011.3.22	WYN	WYN

ELECTRICAL SPECFICATIONS@25°C:

Turn Ratio($\pm 3\%$): (3-4):(6,5-8,7):(2-1) =1:0.125:0.208

 $L (10KHz/0.1V): (3-4) 253uH \pm 10\%$.

LK(100KHz/0.1V): (3-4,OTHER PIN SHORT) 7.5uHMAX.

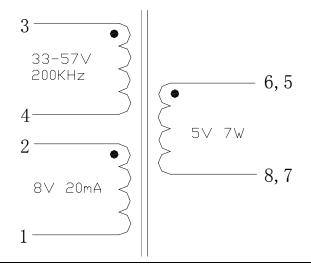
DCR: $(3-4)420m \Omega MAX$.

(2-1)115m Ω MAX.

 $(6,5-8,7)16m \Omega MAX.$

HI-POT:(3-4) TO (6,5-8,7) AC1500V/ 1mAMAX/2S (2-1) TO (6,5-8,7) AC1500V/ 1mAMAX/2S

Schematics





REF:PA1134NL

(15)

CA	YUAN DEAN SCIENTIFIC CO., LTD.				
TITLE	TRANSFORMER				
P/N	10SEP-2513SNL	REV.	4		
PAGE NO	1-1	DATE	2011.3.22		
AP'PD	WANG YN	DWN	WANG YN		

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