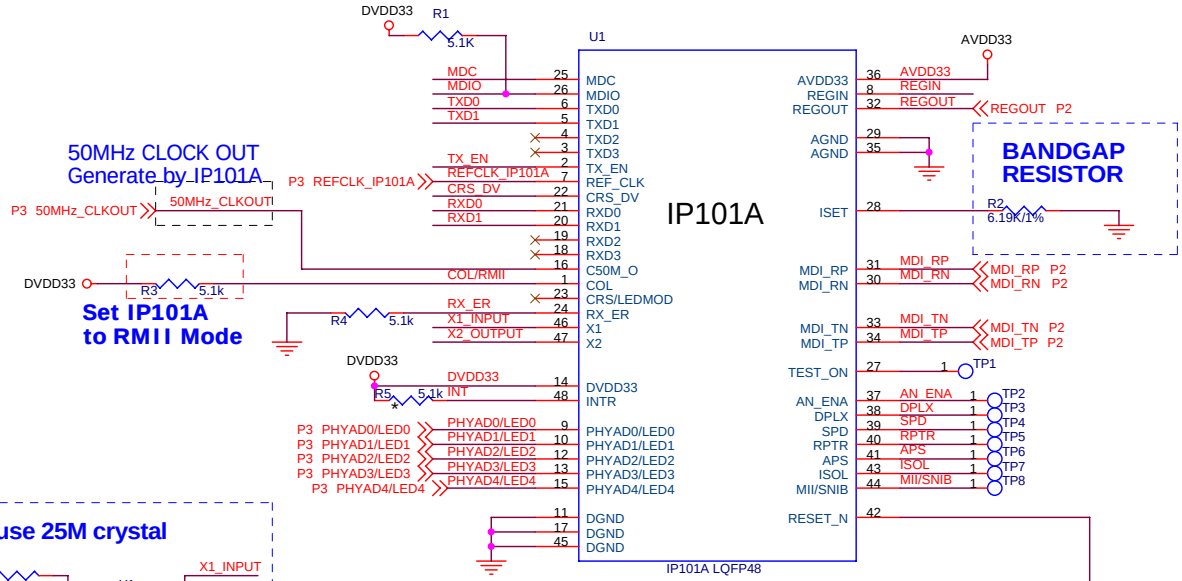
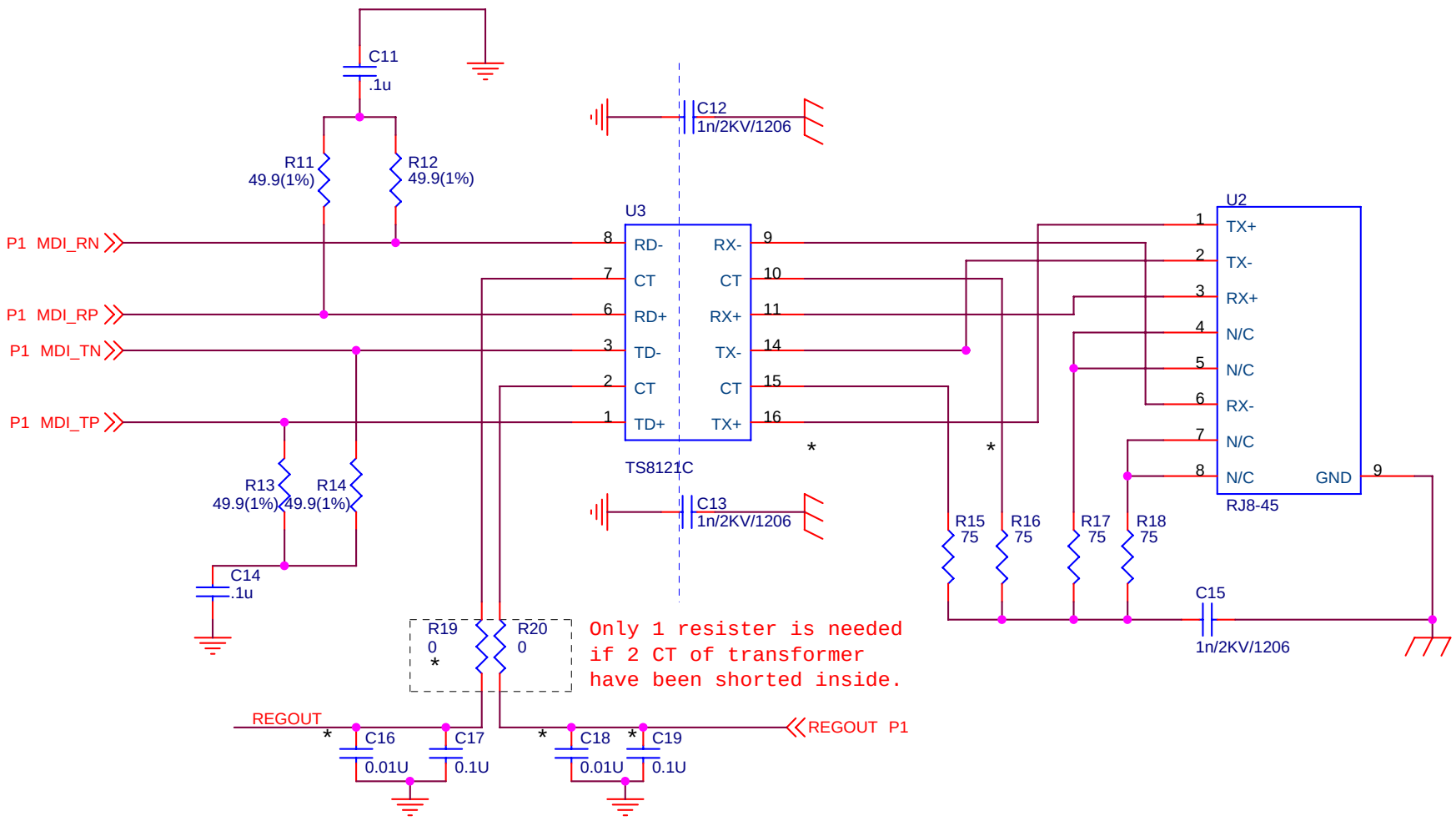


* : Optional ,but recommended



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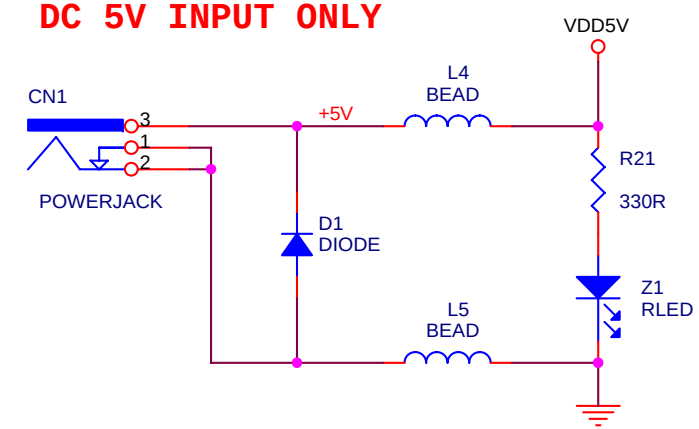
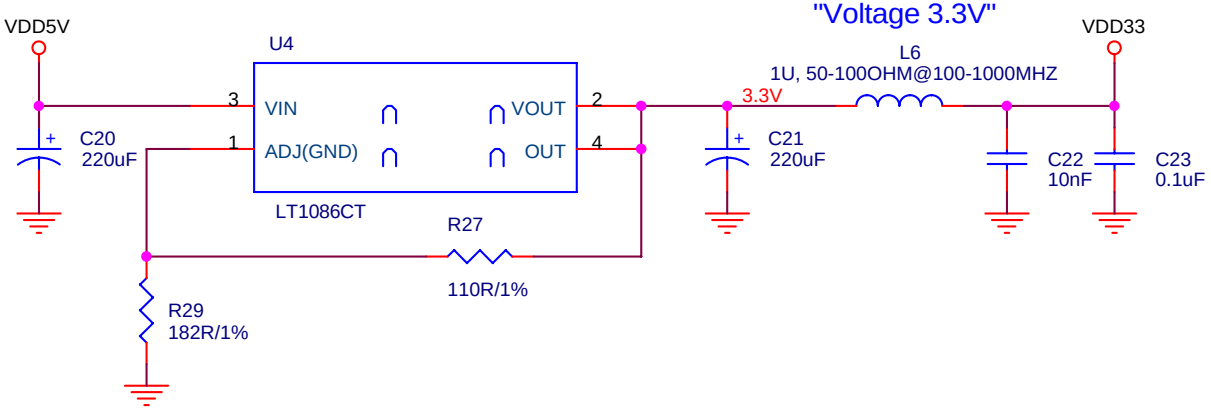
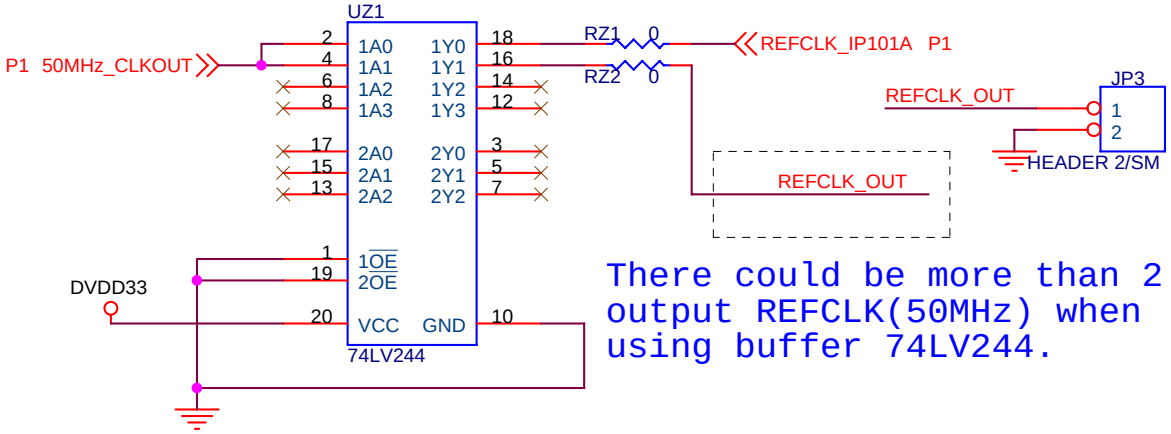
DC 5V INPUT ONLY





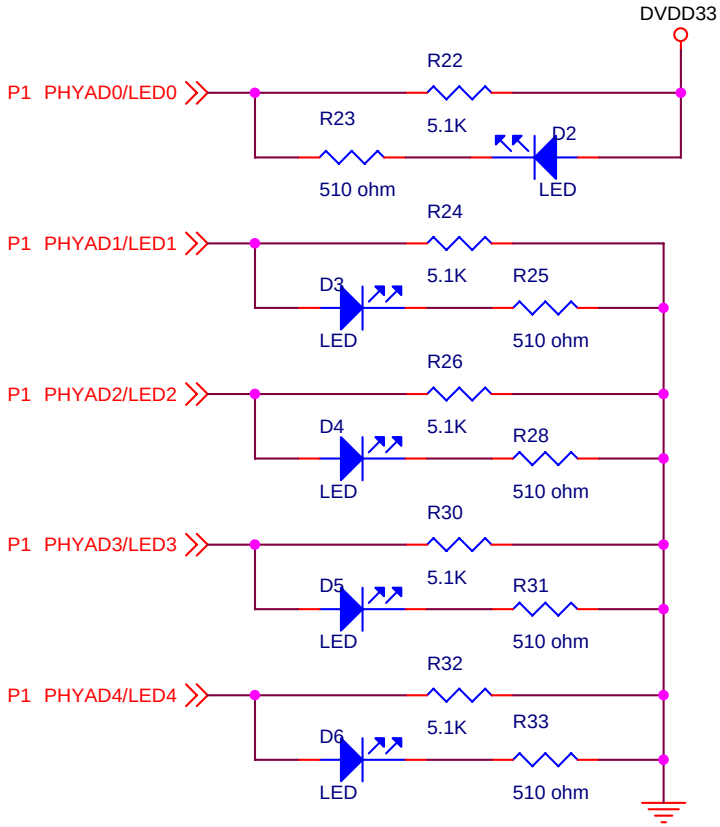
CLOCK generate by IP101A



There could be more than 2 output REFCLK(50MHz) when using buffer 74LV244.

LED and PHY address Configuration

This schematic sets PHY address to 00001b.



LED0	LED1	LED2	LED3	LED4
Link	Dupx	10Act	100Act	COL

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