

Stabilisation of the Arrival Time of a Relativistic Electron Beam to the 50 fs Level

J. Roberts,^{1,2,*} P. Skowronski,² P.N. Burrows,¹ G.B. Christian,¹ R. Corsini,² A. Ghigo,³ F. Marcellini,³ and C. Perry¹

¹*John Adams Institute (JAI), University of Oxford,*

Denys Wilkinson Building, Keble Road, Oxford, OX1 3RH, United Kingdom

²*The European Organization for Nuclear Research (CERN), Geneva 23, CH-1211, Switzerland*

³*Laboratori Nazionali di Frascati (LNFN), Via Enrico Fermi, 40, 00044 Frascati RM, Italy*

(Dated: May 9, 2017)

We report the results of a low-latency beam phase feed-forward system built to stabilise the arrival time of a relativistic electron beam. The system was operated at the Compact Linear Collider (CLIC) Test Facility (CTF3) at CERN where the beam arrival time was stabilised to approx. 50 fs. The system latency was 100 ns and the correction bandwidth > 23 MHz. The system meets the requirements for CLIC and could have applications at future electron-based free-electron lasers.

High-energy linear electron-positron colliders have been proposed as next-generation particle accelerators for exploring the subatomic world with extreme precision. They will provide sensitivity to new physics processes, beyond those described by the Standard Model (SM) of elementary particle interactions, at mass scales that can exceed the eventual reach of the CERN Large Hadron Collider (LHC) by more than an order of magnitude.

The Compact Linear Collider (CLIC) has been proposed [1] as a particle physics facility for the annihilation of electrons and positrons at centre-of-mass energies of up to 3 TeV. CLIC is the most technologically mature concept of a high-energy lepton collider for enabling direct searches for new physics processes in the multi-TeV energy regime. This energy reach, combined with high-luminosity of the electron-positron collisions, will also enable precise measurements of properties of the Higgs boson [2] and the top quark, and provide sensitivity to beyond-SM phenomena at mass scales of up to 10-100 TeV in some cases [3].

CLIC employs the novel concept of high power generation at radio-frequency (RF) by decelerating an electron drive beam and utilising that power to accelerate the main electron and positron beams to high energies. The drive-beam concept is shown schematically in Fig. 1; 50 deceleration sections are required for a 3 TeV electron-positron collider. At the decelerators the drive beam comprises a 240 ns-long pulse of 2.4 GeV electrons bunched with a frequency of c. 12 GHz; the pulse repetition rate is 50 Hz.

A major challenge is the synchronisation of the arrival of the drive and main beams at the power-extraction and transfer structures to better than 50 fs. This requirement limits the luminosity loss, resulting from subsequent mis-acceleration of the main beams, to less than 1% of the design value [4]. X-ray free-electron lasers also demand a high degree of beam arrival-time stability w.r.t. an externally-applied laser beam for the purpose of seeding of lasing by the electron beam. An RF phase and amplitude feedback utilising electro-optic beam arrival time monitors in this context was reported in [5].

We use the equivalent term longitudinal phase to refer

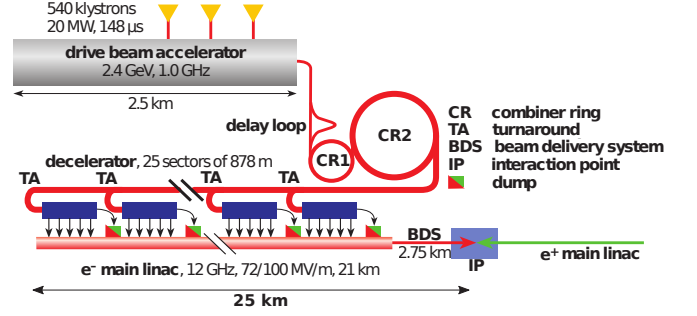


FIG. 1. Schematic of the CLIC drive-beam concept showing the electron acceleration complex [3].

to the beam time coordinate; 50 fs temporal stability is equivalent to 0.2° phase stability at 12 GHz. In the CLIC design the incoming drive-beam phase stability cannot be guaranteed to be better than 2° [1]. A correction mechanism to improve the phase stability by an order of magnitude is therefore required and must be applied to the full drive beam pulse with a bandwidth exceeding 17.5 MHz [4].

This is implemented via a ‘phase feed-forward’ (PFF) system which measures the incoming beam phase and provides a correction to the same beam pulse after it has traversed the turnaround loop (TA in Fig. 1). One PFF system will be installed in each deceleration section. The correction is provided by electromagnetic kickers in a 4-bend chicane: bunches arriving early (late) in time have their path through the chicane lengthened (shortened) respectively. A particular challenge is that the PFF latency must be shorter than the beam flight time of approx. 250 ns around the turnaround loop.

We describe a prototype PFF system (Fig. 2) that implements this novel concept at the CLIC Test Facility (CTF3) at CERN. CTF3 provides a 135 MeV electron beam bunched at 3 GHz frequency with a beam-pulse length of $1.2 \mu\text{s}$ and a pulse repetition rate of 0.8 Hz [1].

The incoming beam phase is measured in two upstream phase monitors (ϕ_1, ϕ_2). While the beam transits the turnaround loop a phase-correction signal is evaluated

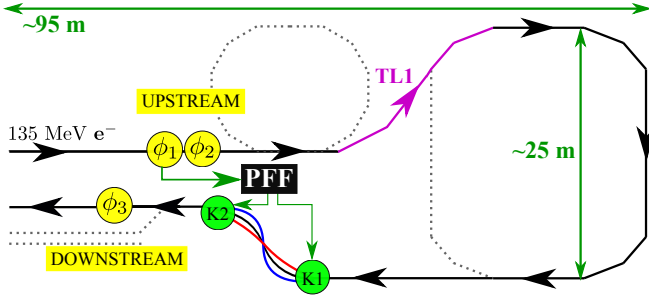


FIG. 2. Schematic of the CTF3 PFF prototype, showing the phase monitors (ϕ_1 , ϕ_2 and ϕ_3) and kickers (K_1 and K_2). The black box PFF represents the calculation and output of the correction, including the phase monitor signal processing electronics, feedforward controller and kicker amplifiers. Dashed lines indicate beam lines that are not used.

TABLE I. Requirements for the CLIC PFF system, and the respective CTF3 parameters; performance achieved with the prototype system is indicated by *.

	CLIC	CTF3	
Drive beam energy	2400	135	MeV
No. PFF systems	50	1	
Kickers per PFF chicane	16	2	
Power of kicker amplifiers	500	20*	kW
Angular deflection per kicker	± 94	$\pm 560^*$	μrad
Correction range	± 10	$\pm 6^*$	$^\circ$
Correction bandwidth	> 17.5	$> 23^*$	MHz
Phase monitor resolution	< 0.14	0.12*	$^\circ$
Initial phase jitter	2.0	0.9	$^\circ$
Corrected phase jitter	0.2	0.2*	$^\circ$

and fed to fast, high-power amplifiers; these drive two electromagnetic kickers (K_1, K_2) which are used to alter the beam transit time in a four-bend chicane. A downstream phase monitor (ϕ_3) is used to measure the effect of the correction.

The beam time of flight between ϕ_1 and K_1 is around 380 ns. The total cable delay for the PFF correction signals is shorter, around 250 ns. The correction can therefore be applied to the same bunch measured at ϕ_1 , providing the hardware latency is less than 130 ns. Significant hardware challenges include the resolution and bandwidth of the phase monitors, and the power, latency and bandwidth of the kicker amplifiers. A low latency digitiser/feedforward controller is also required.

The requirements of the CLIC system and their corresponding CTF3 values are listed in Table I. The main differences result from the different drive beam energies. Higher power amplifiers (500 kW rather than 20 kW) are required for CLIC, which may be achieved by combining the output of multiple modules similar to those built for CTF3. CLIC also requires the synchronisation of multiple PFF systems distributed along the 50 km facility, which is not addressed here.

The phase monitors [6] are cylindrical cavities with

an aperture of 23 mm and a length of 19 cm. Small ridges (notch filters) in the cavity create an effective volume with a resonant frequency of 12 GHz. The resonant electromagnetic field induced by the beam traversing the cavity contains a beam-position-independent monopole mode and an unwanted position-dependent dipole mode. The effect of the latter is removed by summing the outputs from an opposing pair of feedthroughs, on the top and bottom of the cavity, via a RF hybrid. To extract the beam phase the output from each hybrid is mixed with a 12 GHz reference signal derived from a 3 GHz source which is time-locked to the CTF3 master oscillator and serves all three phase monitors. A linear response to input beam phase was measured over the range $\pm 70^\circ$ [7]. By comparing the signals from ϕ_1 and ϕ_2 we have measured a phase resolution of 0.12° , i.e. about 30 fs [8].

The phase signals are digitised in the feedforward controller (FC) board [8], which is used to calculate and output the amplifier drive signals, and to control the correction timing. It consists of nine 14-bit analogue to digital converters clocked at 357 MHz, a field programmable gate array, and four digital to analogue converters.

The kicker amplifiers [8] consist of one central control module and two drive and terminator modules (one per kicker). The control module distributes power and input signals to the drive modules. The 20 kW drive modules consist of low-voltage Si FETs driving high-voltage SiC FETs; an input voltage range of ± 2 V corresponds to an output range of ± 700 V. The response is linear to within 3% for input voltages between ± 1.2 V, and the output bandwidth is 47 MHz for small signal variations of up to 20% of the maximum. For larger signal variations the bandwidth is slew-rate limited.

The two electromagnetic stripline kickers [9] are 1 m in length and have an internal aperture of 40 mm between two strips placed along their horizontal walls. They are designed to give a response within a few ns of the input signal. The strips have tapered ends to reduce beam coupling impedance. Opposite polarity voltages of up to 700 V applied to the strips at the downstream end produce a horizontal deflection of up to 560 μrad for the 135 MeV beam.

The measured total latency of the phase monitor signal processing, the FC calculation, and amplifier response was approximately 100 ns. Therefore the output from the FC was delayed by an additional 30 ns to synchronise the correction at the kicker with the beam arrival [8].

The PFF operation placed severe constraints on the setting of the magnetic lattice in both the beamline between the upstream phase monitors and the correction chicane and in the chicane itself. The beam transfer matrix coefficient R_{52} between the two kickers characterises the change in path length through the chicane relative to the deflection applied at the first kicker. With an R_{52} value of 0.74 m/rad [8] the expected maximum path length change for operation of the PFF system, corre-

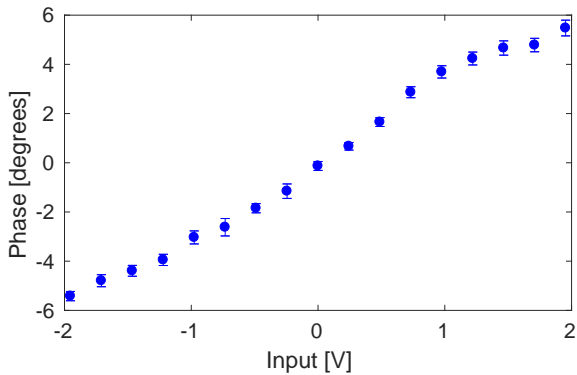


FIG. 3. Measured downstream beam phase vs. kicker amplifier input voltage. Standard errors are shown.

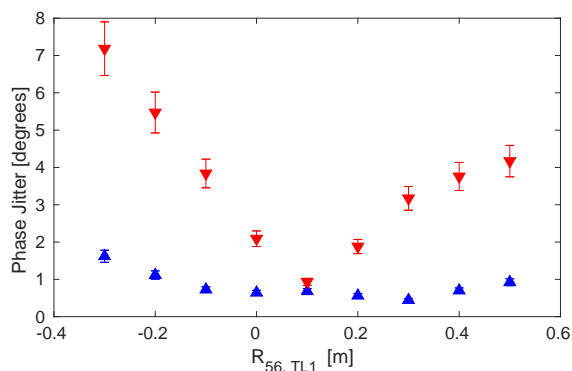


FIG. 4. Measured downstream (red) and upstream (blue) phase jitter vs. TL1 R_{56} value. Standard errors are shown.

sponding to the maximum deflection of $\pm 560 \mu\text{rad}$ from each kicker, is about $\pm 400 \mu\text{m}$, equivalent to $\pm 6^\circ$ in phase (Fig. 3). PFF operation also should not change the beam trajectory at the exit of the chicane. Therefore the chicane magnets were set so that the second kicker cancels the transverse orbit deviation created by the first [8].

A further challenge to PFF operation was obtaining a high correlation between the upstream and uncorrected downstream phases measured at ϕ_1 and ϕ_3 respectively. A correlation coefficient of at least 97% is required to reduce a typical incoming phase jitter of 0.8° to the target of 0.2° [8]. The maximum measurable correlation depends on both the phase monitor resolution and any additional phase jitter introduced in the beamlines between ϕ_1 and ϕ_3 . The monitor resolution of 0.12° limits the maximum upstream-downstream phase correlation to 98% in typical conditions, and places a theoretical limit of 0.17° on the measurable corrected downstream phase jitter. The dominant source of uncorrelated downstream phase jitter arises from beam energy jitter that is transformed into phase jitter.

To first order the phase-energy dependence can be described via the beam transfer matrix coefficient R_{56} :

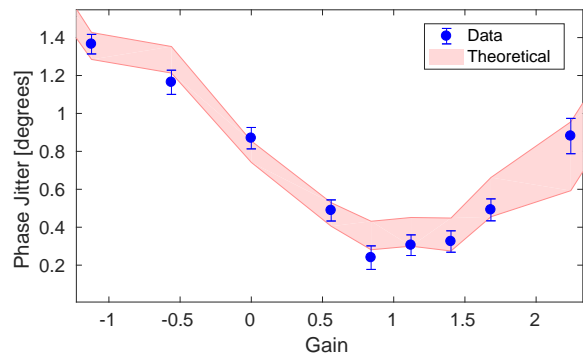


FIG. 5. Measured corrected beam phase jitter at ϕ_3 vs. PFF gain; standard errors are shown (points). The theoretically-achievable performance is shown by the red shaded region (see text).

$\phi_3 = \phi_1 + R_{56}(\Delta p/p)$, where $\Delta p/p$ is the particle's relative energy error. The optimal condition is $R_{56} = 0$. This was achieved by tuning the R_{56} value in the 'TL1' transfer line (Fig. 2) so as to compensate for non-zero R_{56} in the other beamline sections. With $R_{56, \text{TL1}} = 10 \text{ cm}$ the downstream phase jitter is reduced to the same level as the upstream jitter (Fig. 4). However, a large second-order phase-energy dependence remained uncorrected, resulting in a degradation in upstream-downstream phase correlation for large drifts in beam energy.

PFF performance is controlled by a gain parameter. Theoretically the best gain, in appropriate units, should be roughly unity, but in practice the gain can be chosen to achieve optimal performance for real beam conditions. A representative gain scan is shown in Fig. 5. The optimal system gain is typically in the range 0.8–1.2. Also shown in Fig. 5 is a theoretical prediction of the corrected phase jitter given the initial beam phase jitter at ϕ_1 and ϕ_3 , and the upstream-downstream phase correlation. The simulation reproduces the data.

In order to meet CLIC requirements (Table I) the PFF correction bandwidth should be at least 17.5 MHz so as to allow correction within the 240ns-long drive-beam pulse. This function was tested with the prototype, which was used to remove phase variations within a portion of the $1.2 \mu\text{s}$ beam pulse (Fig. 6). It is an operational feature at CTF3 that there is a roughly parabolic phase sag of 40° , resulting from the upstream RF pulse compression scheme [1]. Hence approximately a 440 ns portion of the pulse is within the $\pm 6^\circ$ dynamic range of the PFF system, and can be corrected to zero nominal phase. This time duration for the full correction exceeds the CLIC drive-beam pulse length of 240ns and in any case the CLIC design avoids such a large phase sag [1].

Fig. 6 shows the effect of the PFF system on the intra-pulse phase variations. The PFF system was operated in interleaved mode, with the correction applied to alternating pulses only. This allows the initial ('PFF Off')

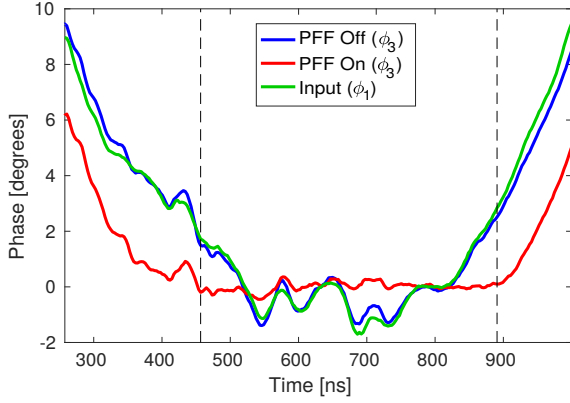


FIG. 6. Phase vs. time within the central portion of the beam pulse; the incoming phase measured in ϕ_1 (green), and the downstream phase measured in ϕ_3 with PFF off (blue) and PFF on (red). Each trace is the average over a 30 minute dataset. The vertical dashed lines mark the time interval corresponding to the PFF dynamic range.

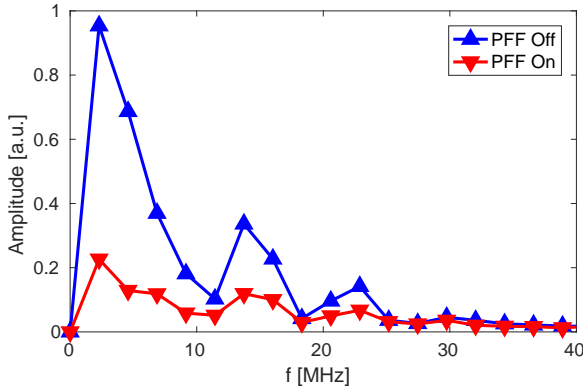


FIG. 7. Amplitude of phase errors at different frequencies (f) with the PFF system off (blue) and on (red).

and corrected ('PFF On') downstream phase to be measured concurrently at ϕ_3 . The ϕ_1 (PFF input) phase is also shown for comparison. Vertical dashed lines mark the 440 ns portion of the pulse where full correction is possible, this range is used in the following analyses.

The PFF system flattens the phase, and almost all variations are removed. Residual offsets are still present where there are small uncorrelated differences between the initial phase at ϕ_1 and ϕ_3 . The average intra-pulse phase variation (rms) over the dataset is reduced from $0.960 \pm 0.003^\circ$ (PFF off), to $0.285 \pm 0.004^\circ$ (PFF on).

A Fourier-Transform (FFT) method was used to characterise the PFF on/off datasets. The FFT amplitude is shown vs. frequency in Fig. 7. It can be seen that phase errors are corrected by up to a factor of 5 for frequencies up to 23 MHz, above which they are smaller than the monitor resolution and not measurable. This is consistent with an expected system bandwidth of around

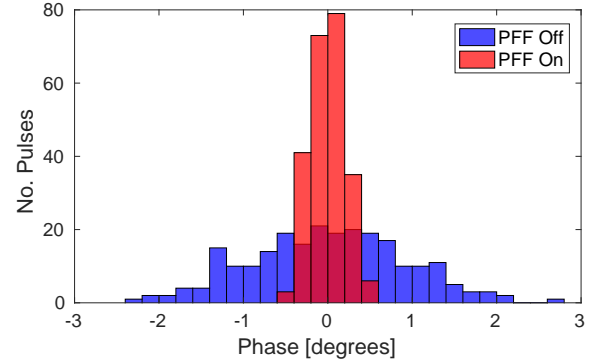


FIG. 8. Distribution of the mean downstream phase with the PFF system off (blue) and on (red).

30 MHz.

As well as removing intra-pulse phase variations the PFF system simultaneously corrects any pulse-to-pulse jitter. Fig 8 shows the effect of the PFF system on the mean phase for a dataset of around ten minutes duration. The pulse-to-pulse phase jitter is reduced from $0.92 \pm 0.04^\circ$ to $0.20 \pm 0.01^\circ$, meeting CLIC-level phase stability. The system acts to remove all correlation between the upstream and downstream phase, reducing an initial correlation of $96 \pm 2\%$ to $0 \pm 7\%$ for this dataset. Given the incoming upstream phase jitter and measured upstream-downstream correlation, the performance is consistent with the theoretically predicted correction of $0.26 \pm 0.06^\circ$.

Typically this level of corrected phase stability could not be maintained for longer time periods due to drifts in the operation of the CTF3 RF system, which led to a degradation in the upstream-downstream phase correlation as well as mean phase drifts beyond the PFF correction range. Nevertheless a mean phase stability of 0.30° was achieved in datasets taken over periods as long as 20 minutes. With suitable upstream RF feedbacks to keep the beam phase within the correction range, and a reduction of the higher order phase-energy dependence in the magnetic lattice, the system is capable of achieving CLIC-level phase stability continuously.

The system was further tested by varying the incoming mean beam phase systematically by up to 3° ; such a variation is comparable to the expected conditions in the CLIC design (Table I). This is illustrated in (Fig. 9). The system removed the induced phase variations and achieved more than a factor-5 reduction in the downstream phase jitter, correcting from $1.71 \pm 0.07^\circ$ to $0.32 \pm 0.01^\circ$.

In conclusion, we have built, deployed and tested a prototype drive-beam phase feedforward system for CLIC. The system incorporates high-resolution phase monitors, an advanced signal-processor and feedforward controller, low-latency, high-power, high-bandwidth amplifiers, and state-of-the-art electromagnetic kickers. The phase-

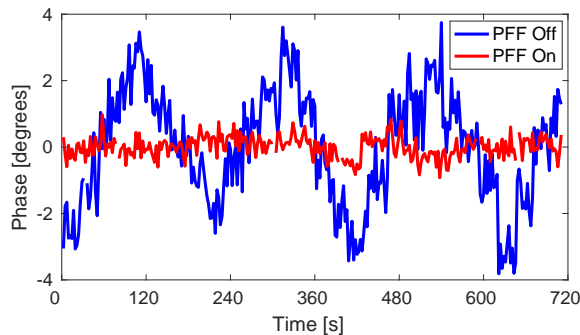


FIG. 9. Mean downstream phase vs. time with the PFF system off (blue) and on (red) subject to large additional phase variations added to the incoming phase (see text).

monitor resolution was measured to be $0.12^\circ \simeq 30$ fs. The overall system latency, including the hardware and signal transit times, was measured to be approx. 350 ns, which is less than beam time of flight between the input phase monitor and the correction chicane. Therefore, the feed-forward phase correction was applied downstream to the same beam bunches initially measured upstream. The system was used to stabilise the pulse-to-pulse phase jit-

ter to $0.20 \pm 0.01^\circ \simeq 50$ fs, and simultaneously corrects intra-pulse phase variations at frequencies up to 23 MHz.

We thank Alessandro Zolla and Giancarlo Sensoloni (INFN Frascati) for their work on the mechanical design of the phase monitors and kickers, Alexandra Andersson, Luca Timeo and Stephane Rey (CERN) for their work on the phase monitor electronics. We thank the operations team of CTF3 for their outstanding support. We acknowledge the UK Science and Technology Facilities Council for their financial support for this work.

* Corresponding author Jack.Roberts@cern.ch

- [1] M. Aichele *et al.*, “CLIC conceptual design report,” CERN-2012-007 (2012).
- [2] H. Abramowicz *et al.*, arXiv:1608.07538 [hep-ex] (2016).
- [3] CLIC and CLICdp collaborations, arXiv:1608.07537 [physics.acc-ph] (2016).
- [4] A. Gerbershagen *et al.*, Phys. Rev. AB **18**, 041003 (2015).
- [5] F. Löhl *et al.*, Phys. Rev. Let. **104**, 144801 (2010).
- [6] F. Marcellini *et al.*, EuCARD-REP-2013-023 (2014).
- [7] P. Skowronski *et al.*, in *Proceedings of IPAC13* (Shanghai, China, 2013) pp. 2097–2099.
- [8] J. Roberts, DPhil thesis, University of Oxford (2016).
- [9] A. Ghigo *et al.*, in *Proceedings of IPAC2011* (San Sebastian, Spain, 2011) pp. 1000–1002.