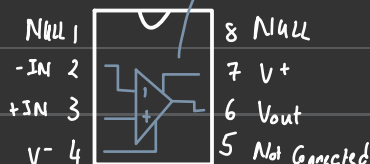
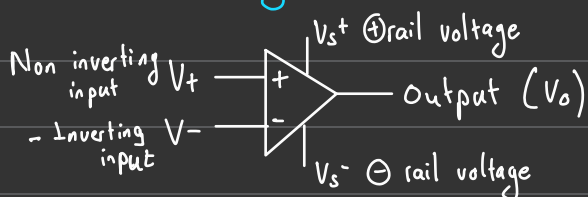


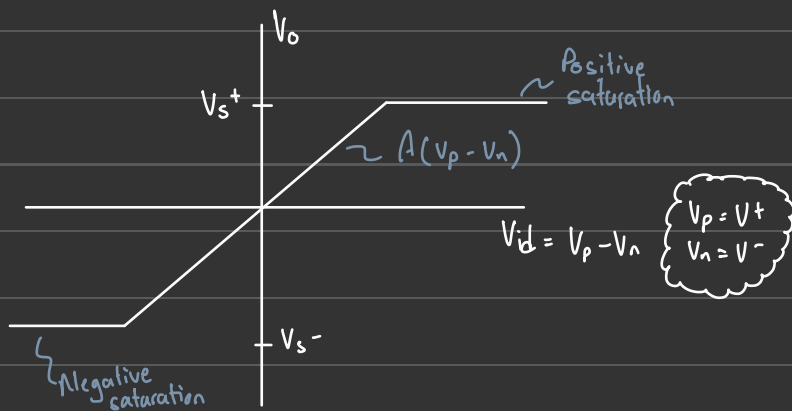
# Operational Amplifiers:



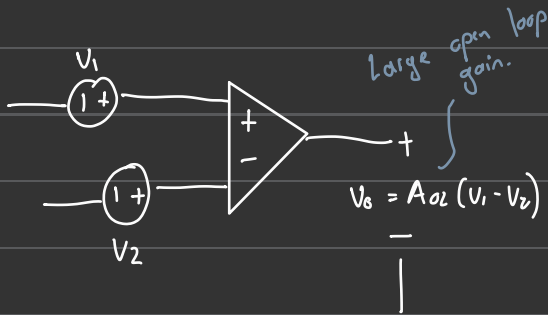
Symbol:



Plotting output  $V$  vs input differential  $V$



# Ideal Op Amp



$$V_{icm} = \frac{1}{2} (V_1 + V_2)$$

Avg. between  $V_+$  and  $V_-$

$$V_{id} = V^+ - V^-$$
$$= V_1 - V_2$$

## Assumptions

Infinite input impedance

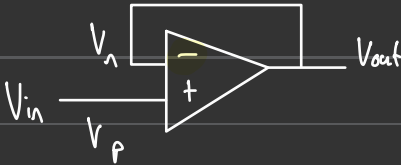
Infinite differential gain

Zero common-mode gain

Zero output impedance

Infinite bandwidth

## Negative feedback



$$V_o = A(V_{in} - V_o)$$

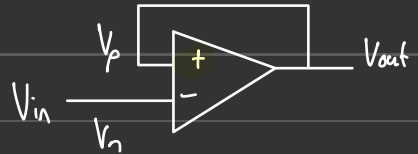
$$V_{in} < V_o \Rightarrow V_o \downarrow$$

$$V_{in} > V_o \Rightarrow V_o \uparrow$$

$V_o$  tends to  $V_{in}$

$$\Rightarrow V_n = V_p$$

## Positive feedback



$$V_o = A(V_o - V_{in})$$

•  $V_{in} < V_o \Rightarrow V_o \nearrow \text{Rail}$

•  $V_{in} > V_o \Rightarrow V_o \searrow \text{Rail}$

## Method

- 1) Confirm negative feedback  
↳ Looking at feedback loop.
- 2) Assume  $V_n = V_p$  and  $i_n = i_p = 0A$  (bc.  $\infty$  input impedance)
- 3) Use standard circuit analysis to determine values. ( $A_v, R_i, R_{out}...$ )  
↳ KCL, KVL.... (usually KCL @  $V_n$ )
- 4) Test that the op-amp is in between the linear region.

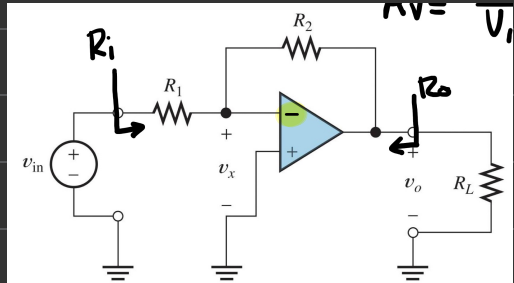
## Inverting Amplifiers

From steps above:

$$\text{Gain: } A_v = \frac{V_o}{V_{in}} = -\frac{R_2}{R_1}$$

$$\text{Input } R: R_i = R_1$$

$$\text{Output } R: R_o = 0 \Omega$$



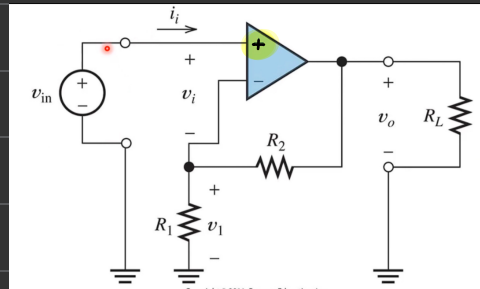
↳  $V_{in}$  goes to  $\ominus$  side

## Non-inverting Amplifiers

$$\text{Gain: } A_v = 1 + \frac{R_2}{R_1}$$

$$R_i = \infty$$

$$R_o = 0 \Omega$$



↳  $V_{in}$  goes to  $\oplus$  side