

**Date Released:**

Friday, March 18, 2022

**Due Date:**

Monday, March 28, 2022

## Assignment - I

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**Read the Instructions Carefully**

**Design:**

Consider a CPU with a 32 bit address bus and an 16-bit data bus.

How do you interface 8 memory chips (4K x 8 bit) with it?

The design should eliminate all memory-folding issues.

**NB:**

1. Use an A4 sheet for the assignment.
2. Submit the assignment to Room 321, Caesar Building.