Microprocessors

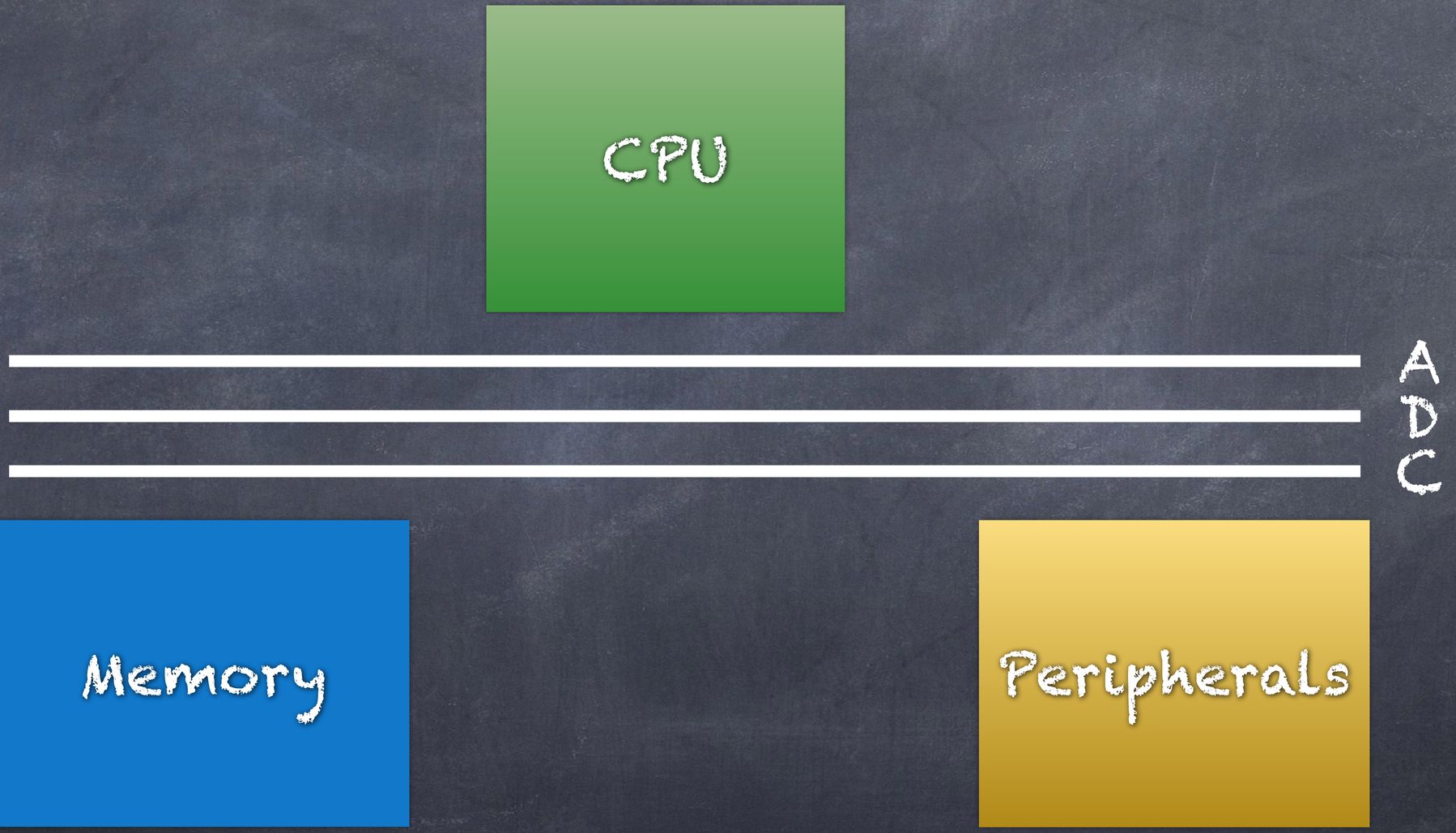
COE 381

WEEK 2: BASIC COMPUTER ORGANIZATION

Justice Owusu Agyemang, Department of Telecommunications Engineering, KNUST

Microprocessors & Microcontrollers

Basic Computer Organization



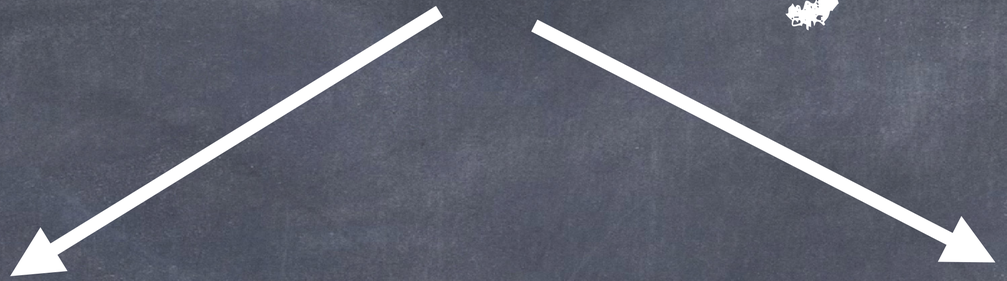
Memory Interfacing Problem

Example (Design):

Consider a CPU with a 16 bit address bus and an 8-bit data bus.

How do you interface 8 memory chips (2K x 4 bit) with it?

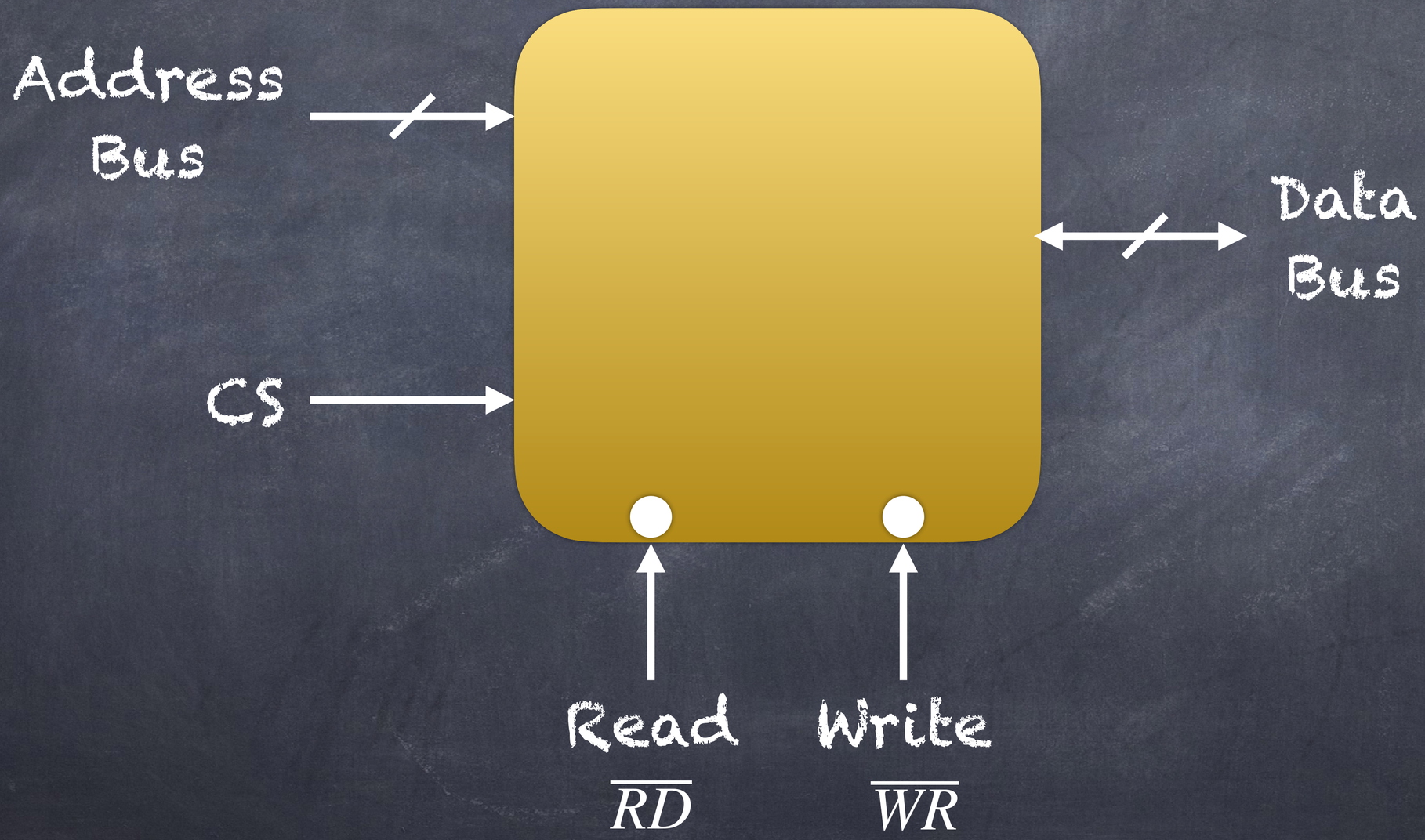
Memory



RAM

ROM

Memory



Memory Interfacing: ROM & RAM

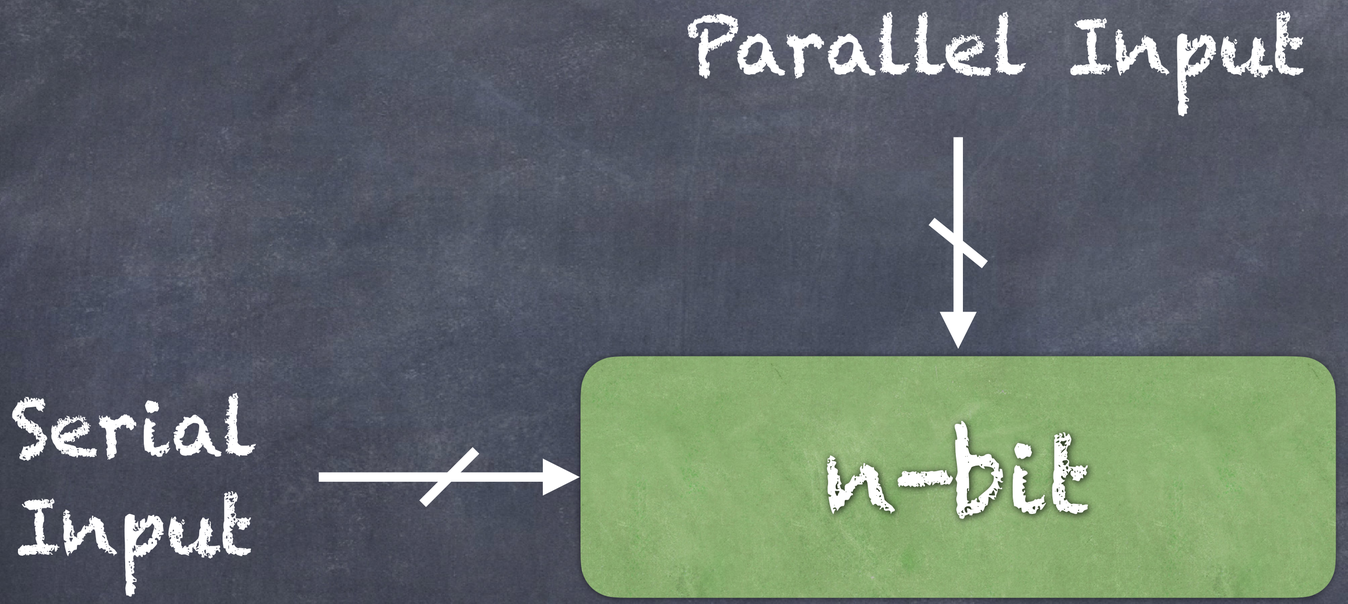
Example (Design):

8KB ROM, 16KB RAM.
CPU => 16 bit Address Bus, 8 bit Data Bus.

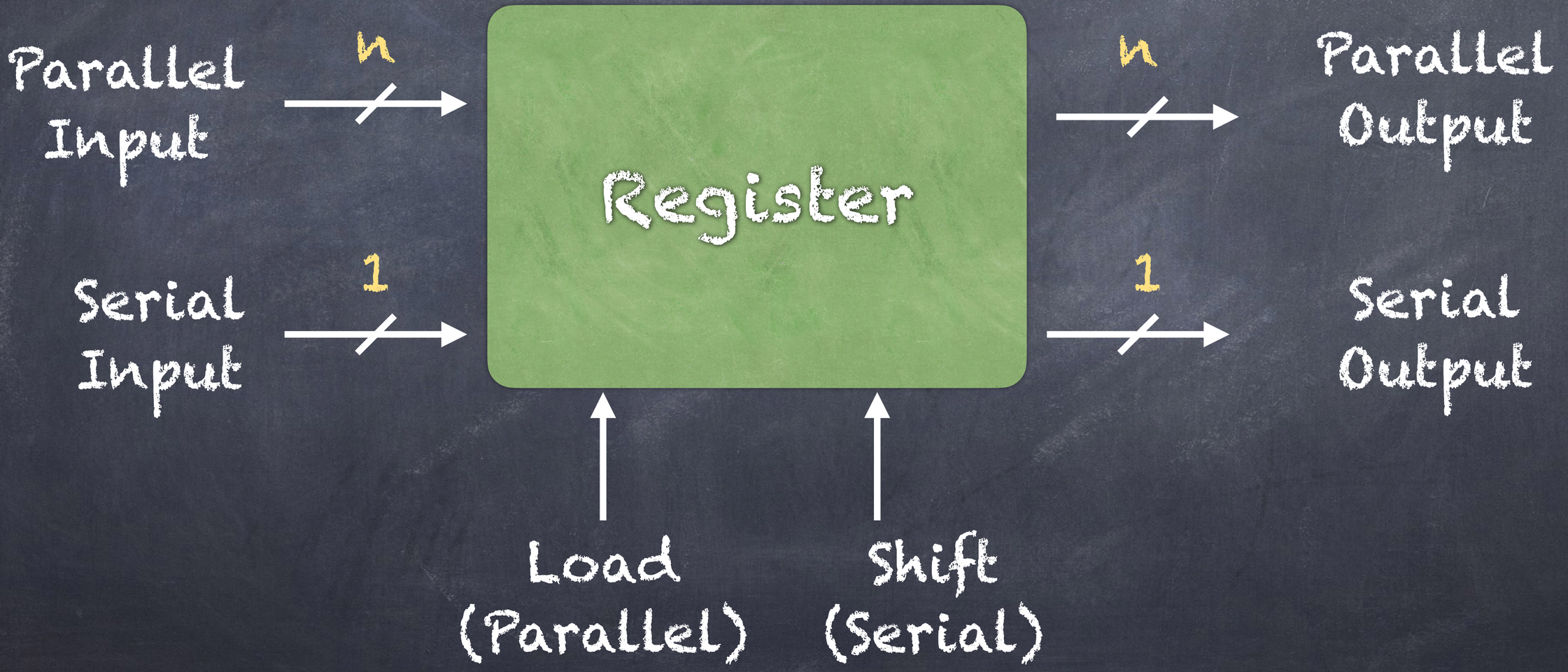
ROM Starting Address = 0000H
RAM Starting Address = 8000H

ROM/RAM Size = 4KB x 8 bit

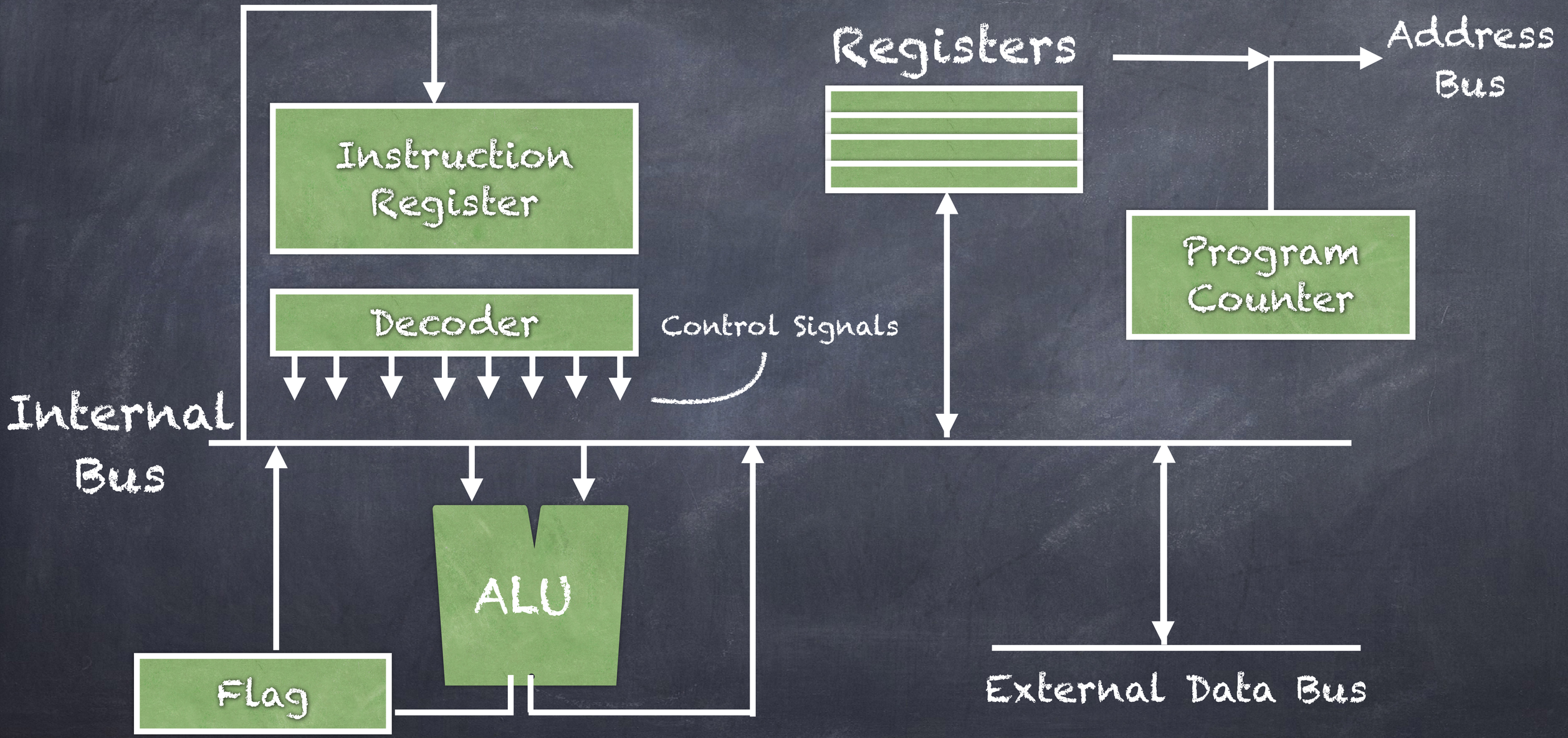
Register



Register

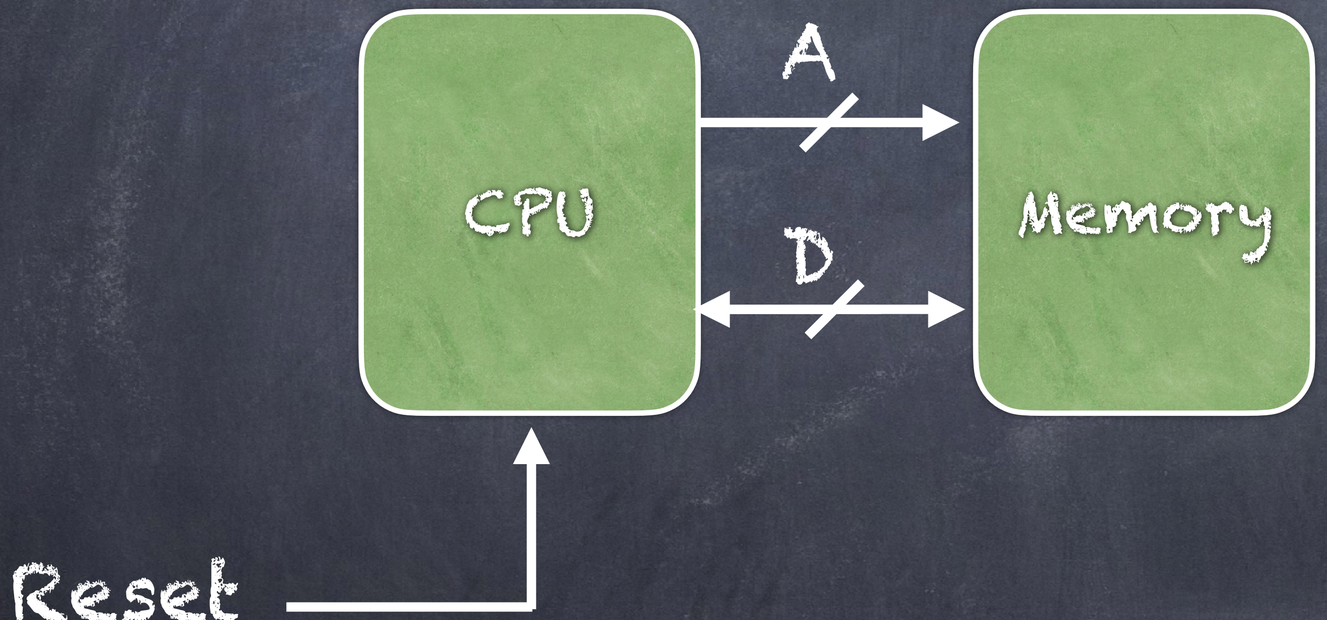

Parallel Input
n
Serial Input
1
Register
n
Parallel Output
1
Serial Output
Load (Parallel)
Shift (Serial)

Processor Internals

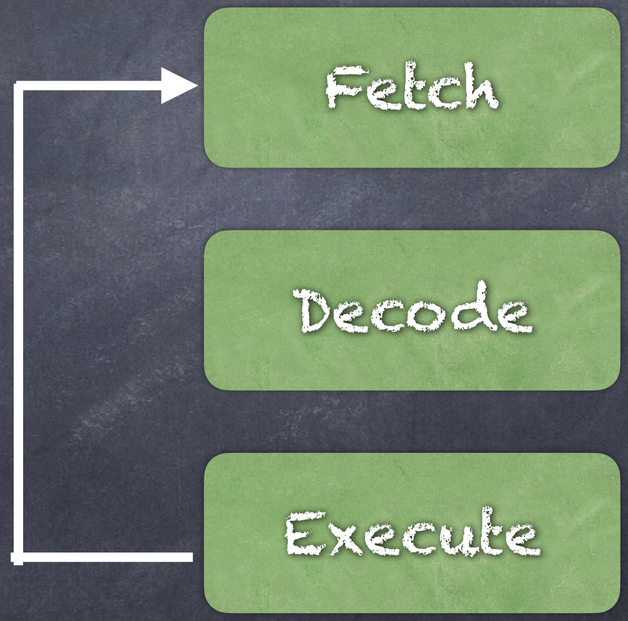


Processor Internals

How the Control Part works

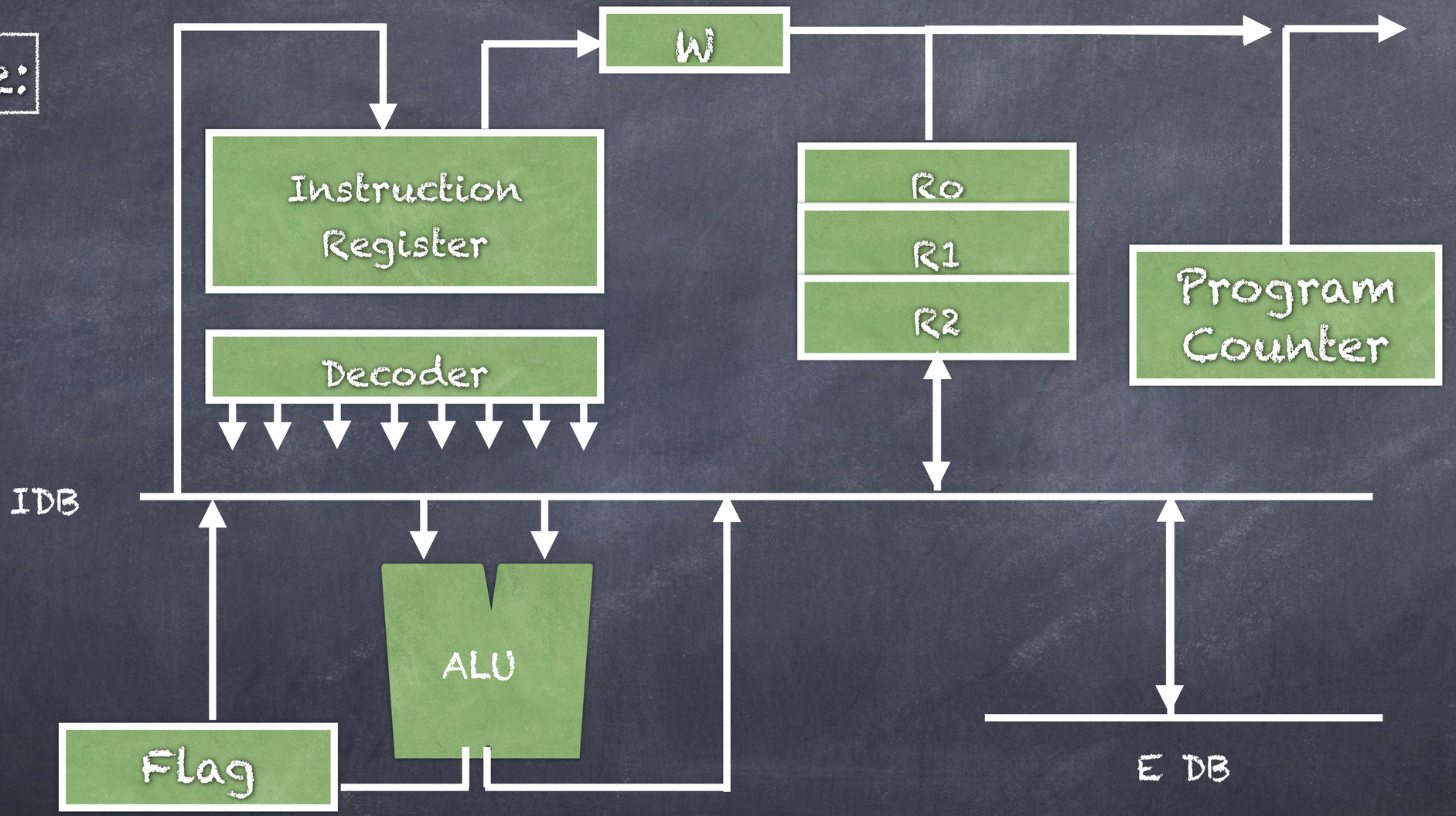


CPU Execution Cycle



Processor Internals

Example:



8085 MICROPROCESSOR

Next...

- 8085 Microprocessor