

VCA

in



CV

$\beta$



out

VCA

in



CV

$\beta$



out

VCA

in



CV

$\beta$



out

VCA

in



CV

$\beta$



out

VCA

in



CV

$\beta$



out

VCA

in



CV

$\beta$

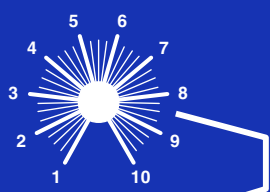


out

MIDI

aux

gate



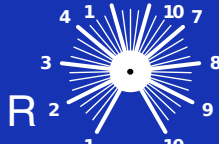
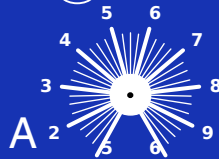
vel

1V/oct

AD/R

trig

gate

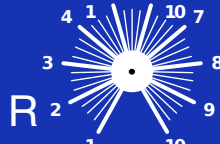
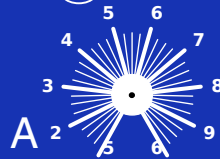


out

AD/R

trig

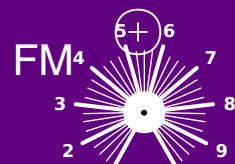
gate



out

ПОЛИВОКС

input



BP

LP