

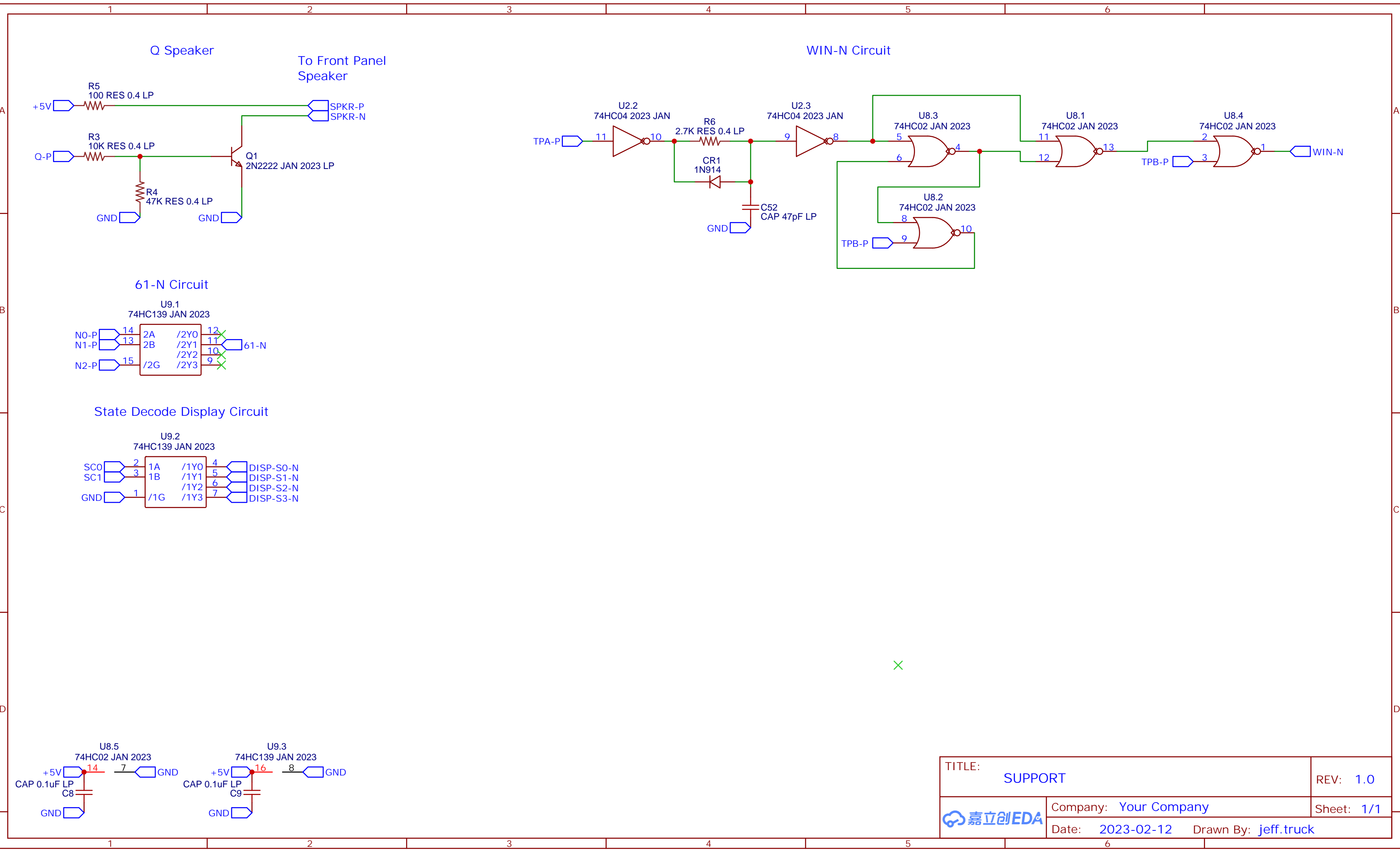
Nano Interface Device

Address Bus Interface

Data Bus Interface

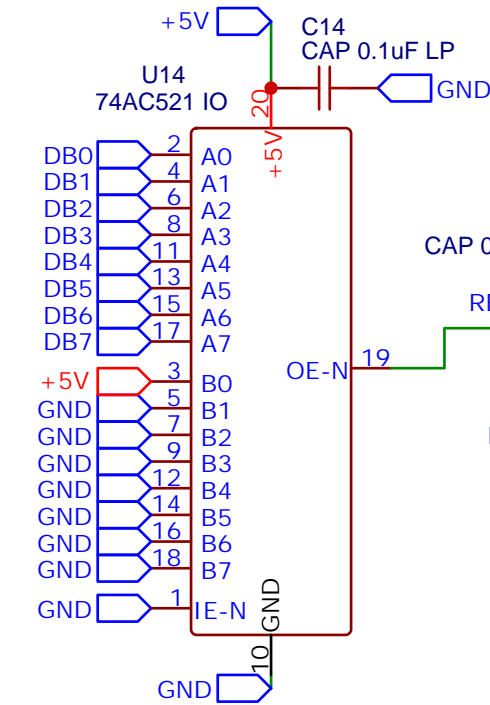
Human / Nano Run Reset Control Arbitration

CPU / Nano Signal Combine

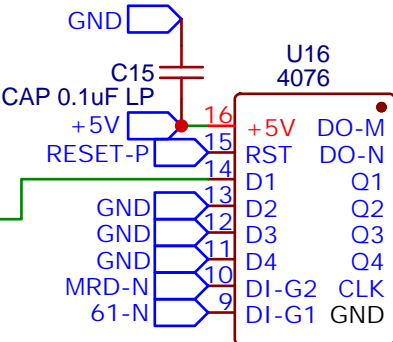


TITLE: SUPPORT		REV: 1.0
嘉立创EDA	Company: Your Company	Sheet: 1/1
	Date: 2023-02-12	Drawn By: jeff.truck

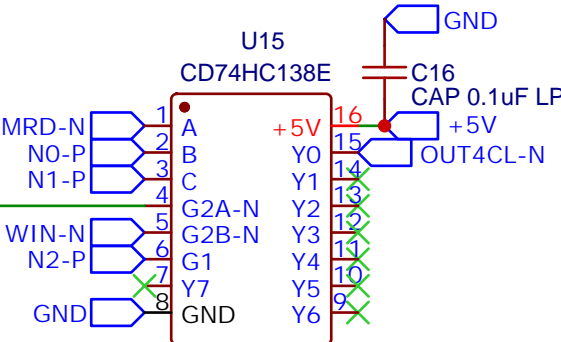
IO 2 LEVEL GROUP SELECT
SYSTEM IO ID = 1



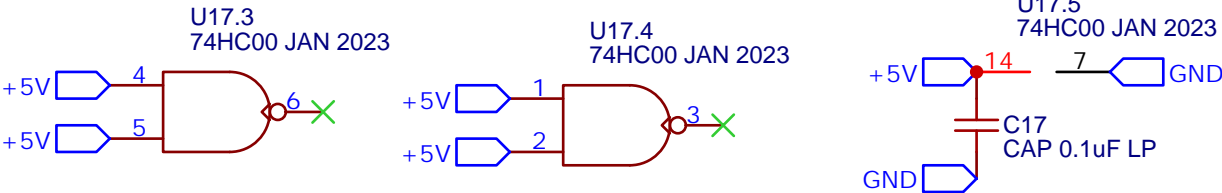
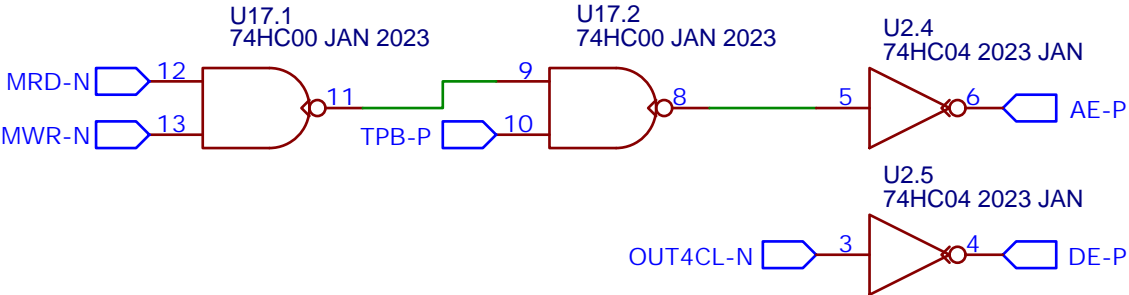
BANK IO ENABLE LATCH



IO COMMAND DECODE



ADDRESS AND DATA DISPLAY ENABLE CIRCUITS



SIGNAL DRIVERS / INTERFACE

