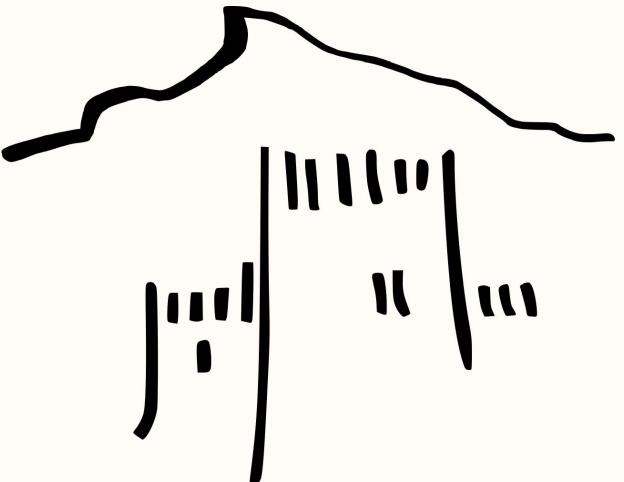
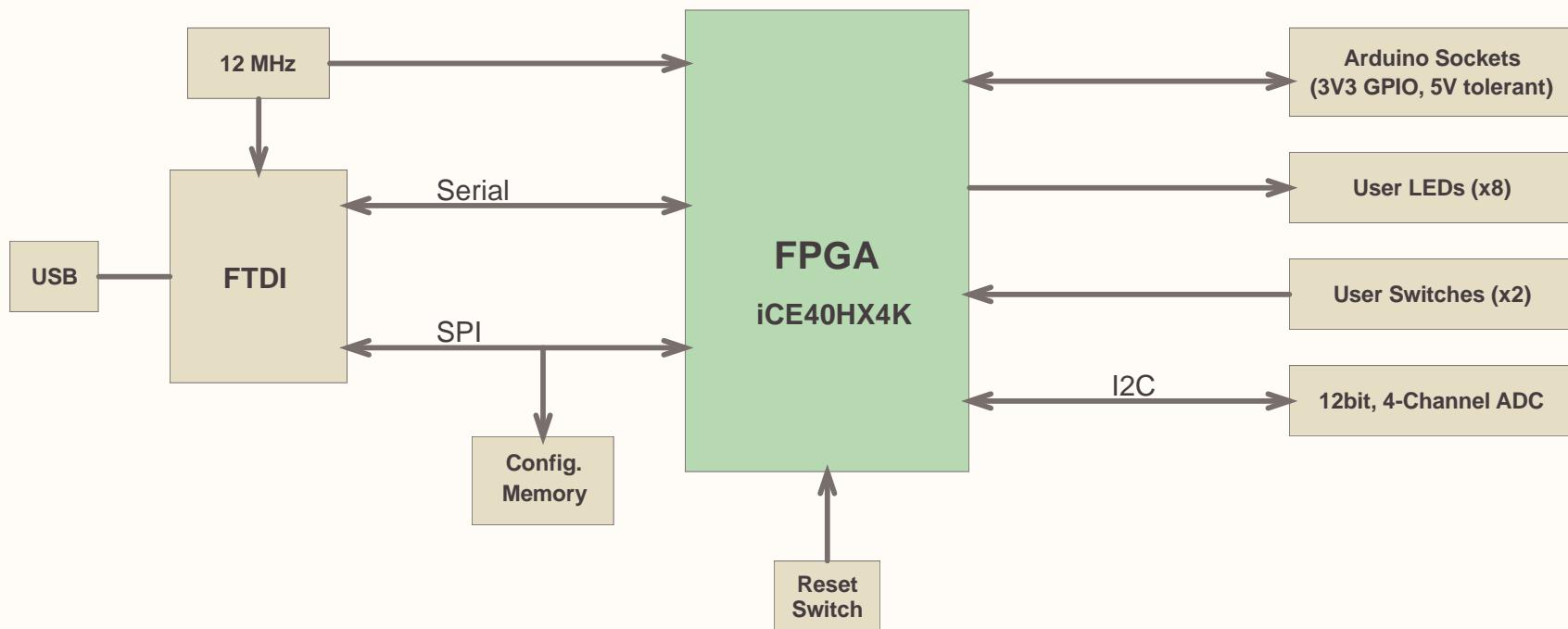


Alhambra II

iCE40 HX4K Open FPGA Board

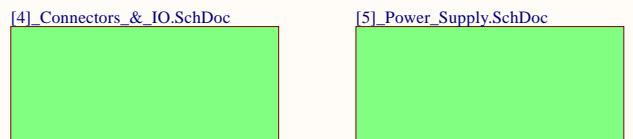
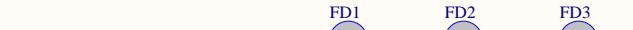
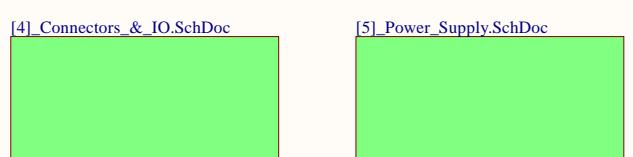
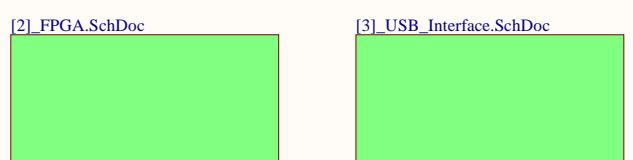
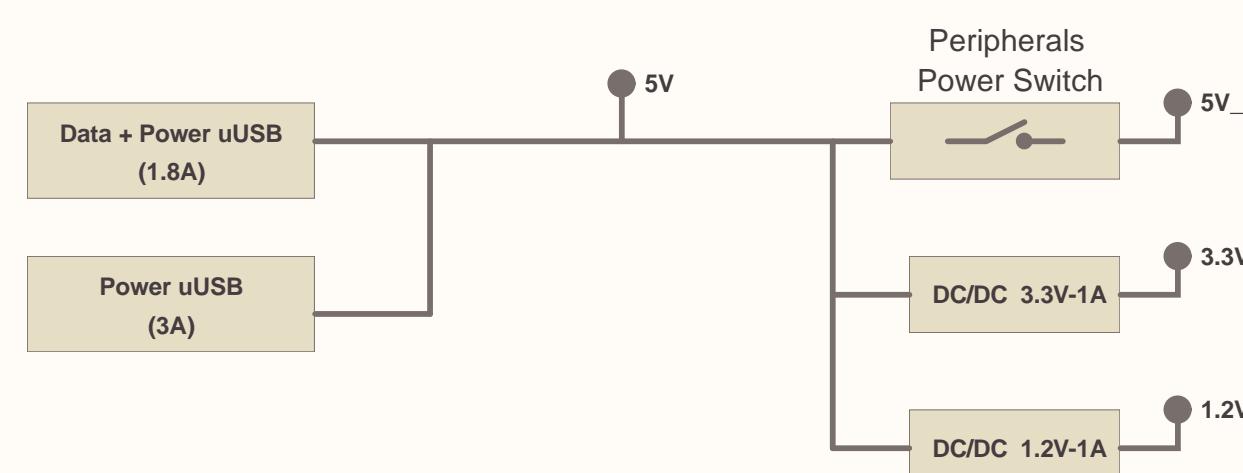


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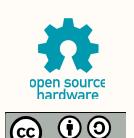


Revision History

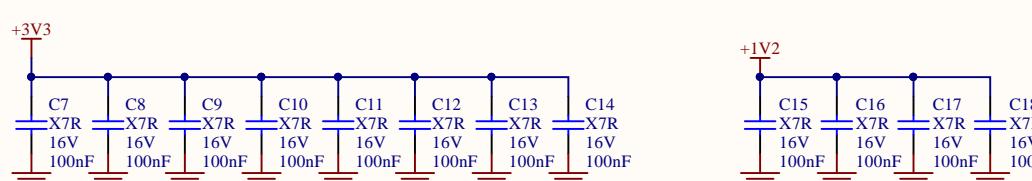
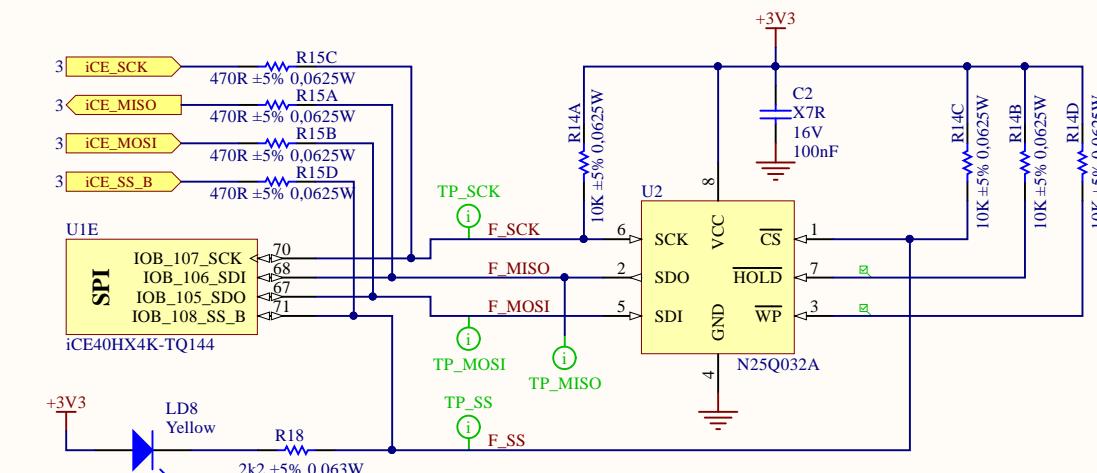
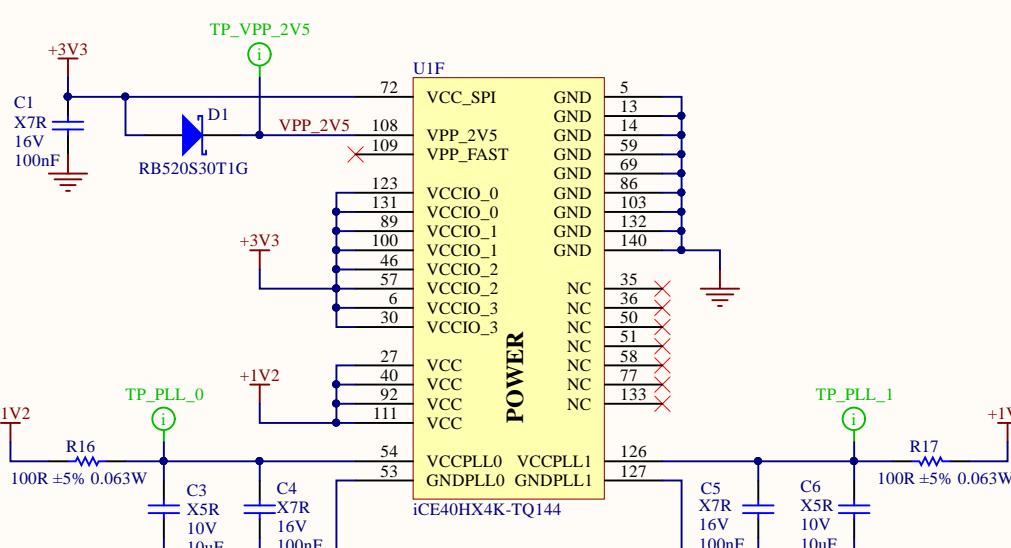
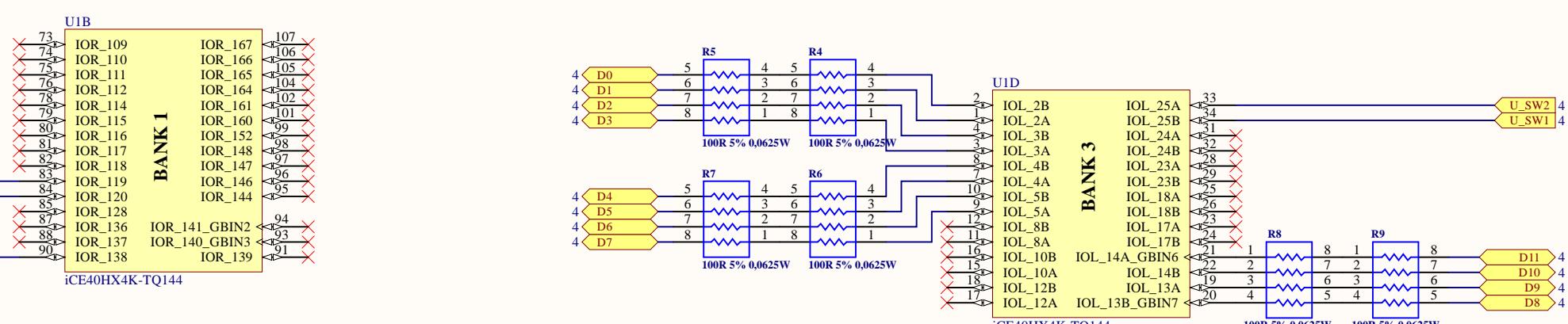
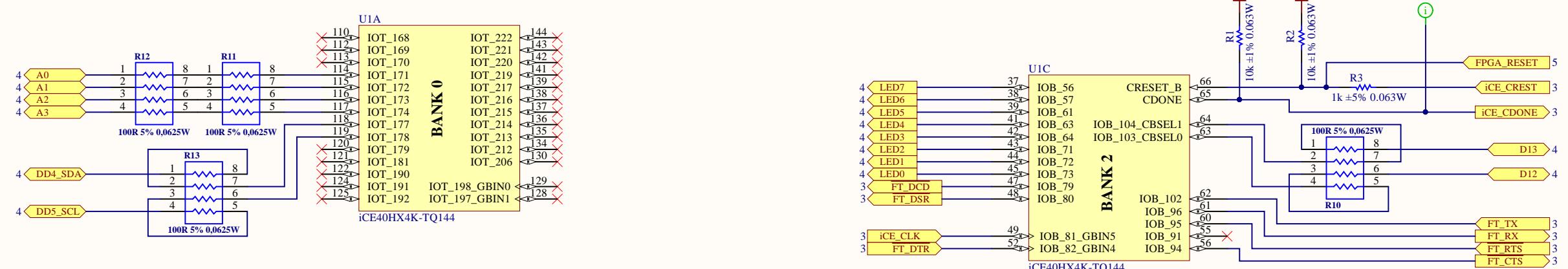
- 2018-03-23: Prototype Rev1.0 (Date Code 1812)
- 2018-06-11: Production release, v1.0A (Date Code 1824)
 - Added resistor for C50 discharge
 - Some minor changes



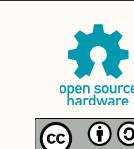
Alhambra II
Open FPGA Board



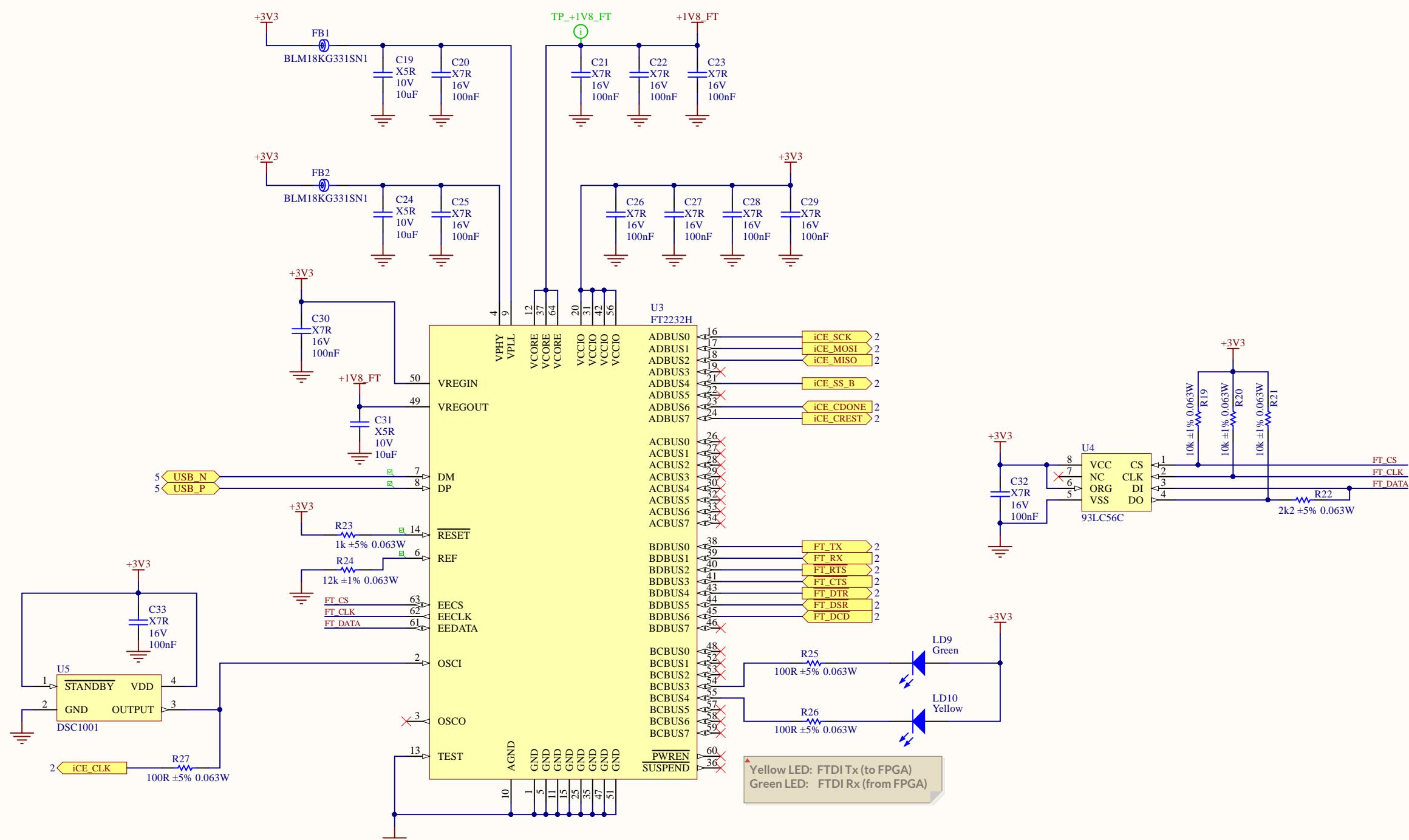
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Project	Alhambra II.PjPrjPcb	Version:	1.0A
Author:	Eladio Delgado Mingorance	Date:	15/11/2018
		Time:	9:32:10
		Variation:	[No Variations]



Alhambra II Open FPGA Board



Sheet	FPGA	2 / 5	A3
Project	Alhambra II.PjPrjPcb		
Author:	Eladio Delgado Mingorance	Version:	1.0A
Date:	15/11/2018	Time:	9:32:11
		Variation:	[No Variations]

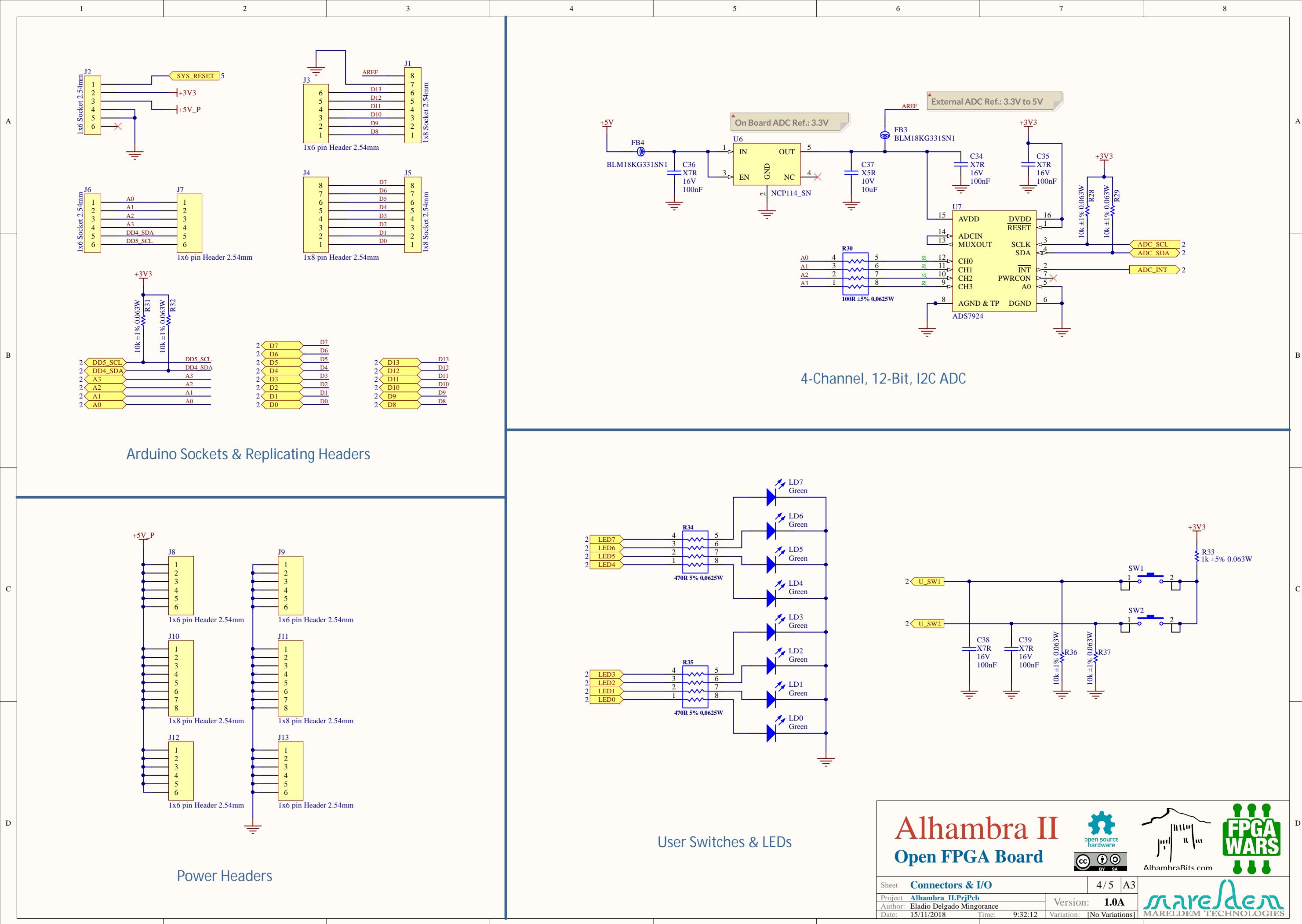


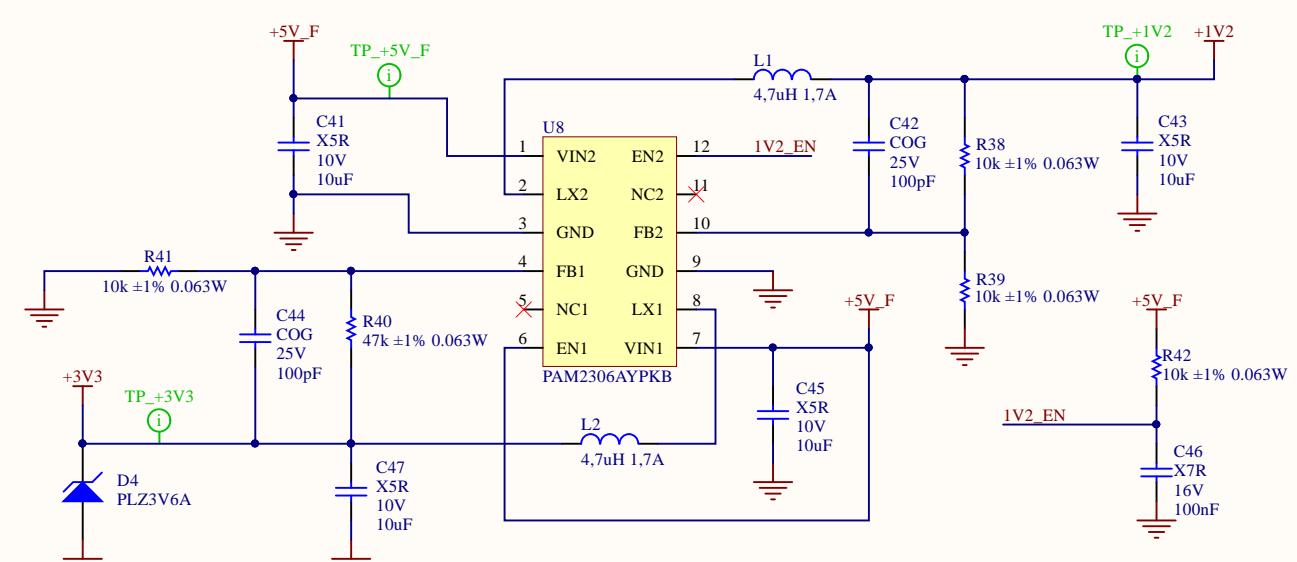
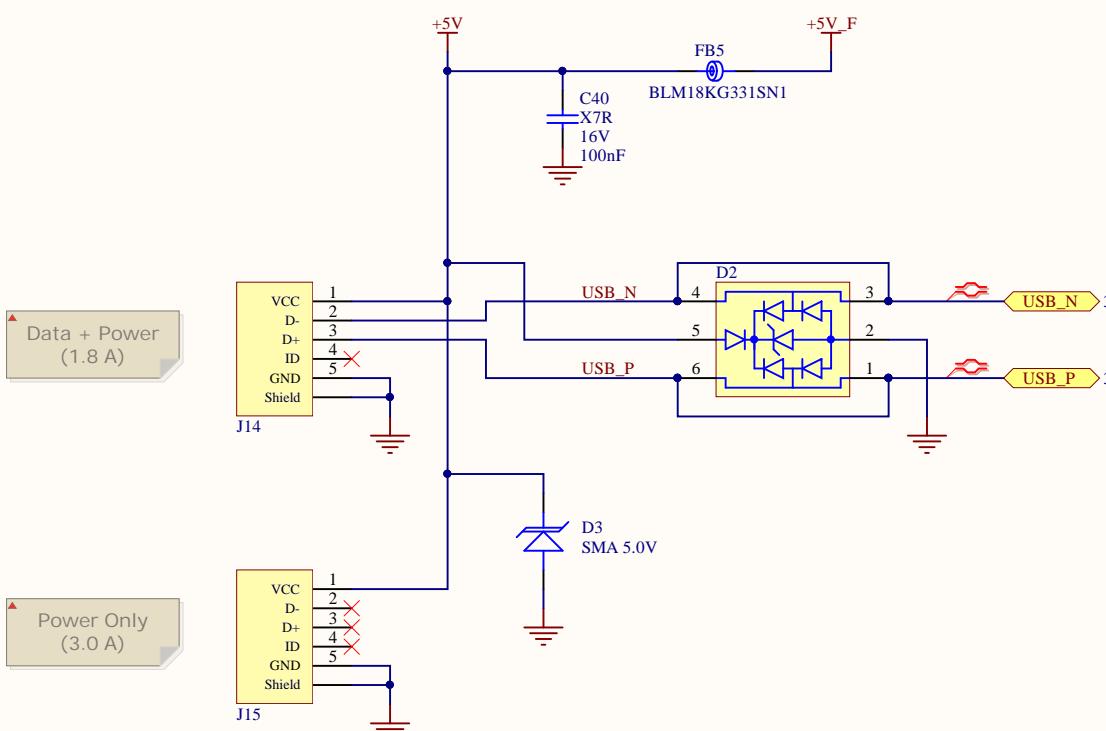
Alhambra II Open FPGA Board

Sheet **USB Interface**

3 / 5 | A3

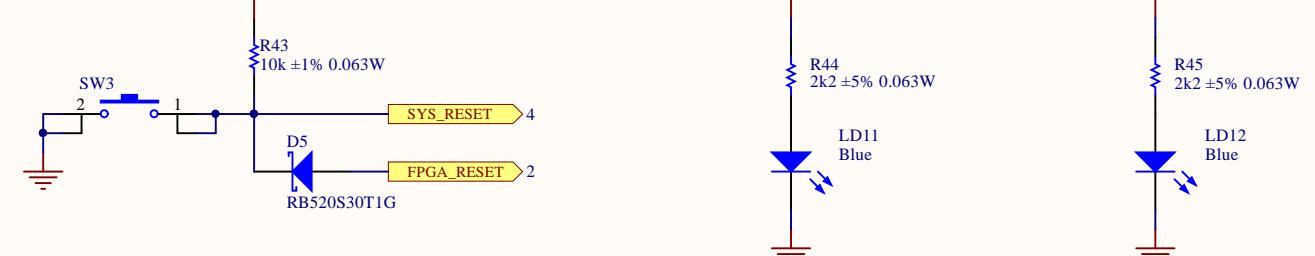
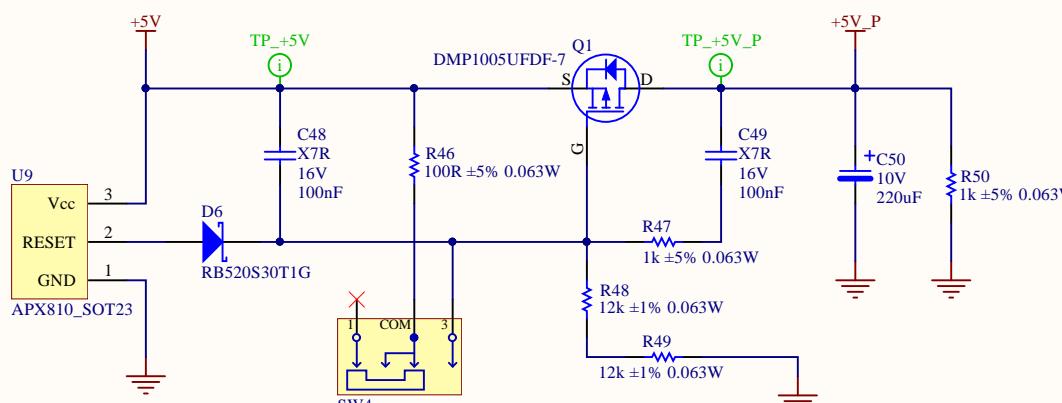
Project **Alhambra II.PjPcb**Version: **1.0A**Author: **Eladio Delgado Mingorance**Date: **15/11/2018**Time: **9:32:12**Variation: **[No Variations]**
MAREDEM TECHNOLOGIES





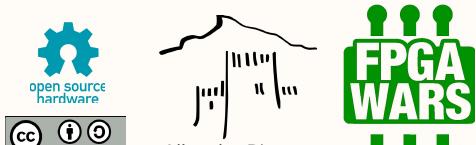
USB Connectors and Power Input

Power Supply 3.3V and 1.2V Rails



Peripherals Power Switch

Alhambra II Open FPGA Board



Sheet	Power Supply	5 / 5	A3
Project	Alhambra II.PrtPcb		
Author	Eladio Delgado Mingorance	Version	1.0A
Date	15/11/2018	Time	9:32:13
		Variation	[No Variations]