

Lecture 16: Dynamic ILP and real systems

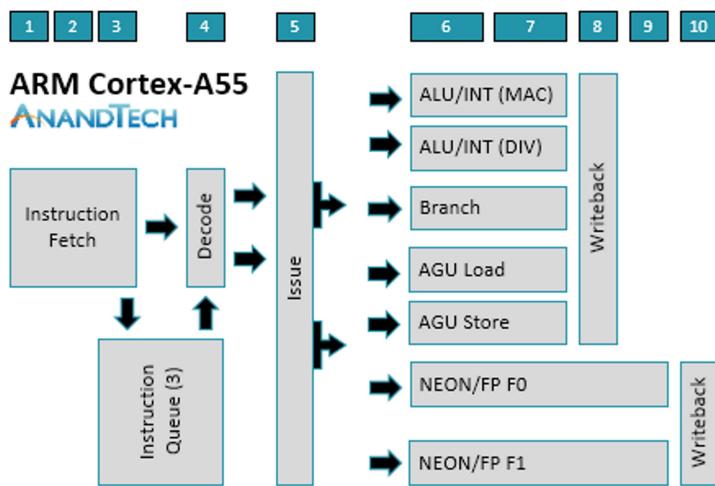
Wednesday, February 13, 2019 8:17 AM

Outline

- Dynamic ILP
- Real in-order designs
- Real out-of-order designs

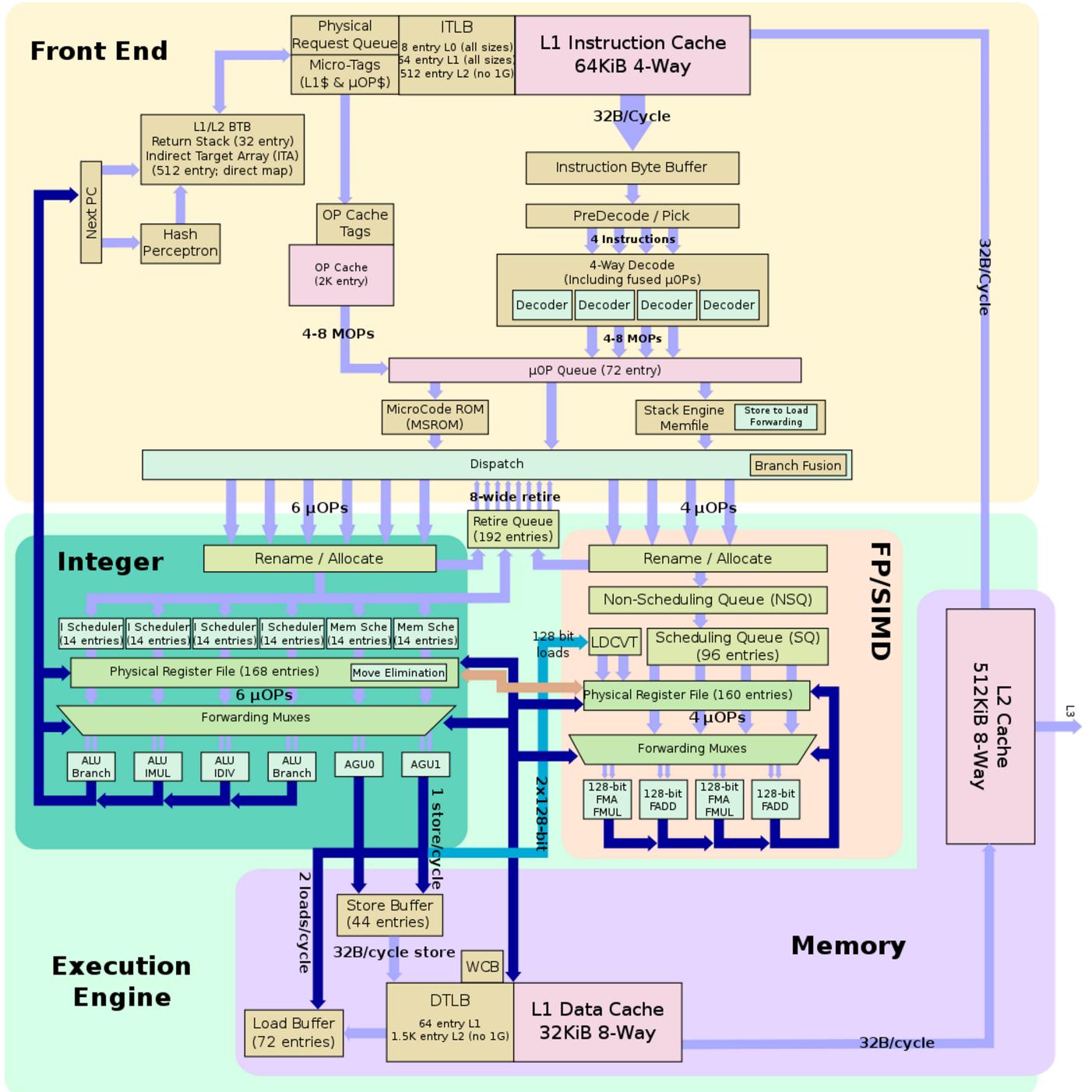
Dynamic ILP

A real pipeline (ARM A55)



An out-of-order design (AMD Ryzen)

From wikichip.com



Real out-of-order: Intel Skylake

From wikichip.com

