EECE 144 Fall 2011

Lab Report #3Section 4 9/21/2011

Submitted by:

| Signature | Printed Name | Date |
|-----------|------------------|--------------|
| | Jeremiah Mahler | Sep 21, 2011 |
| | Marvanee Johnson | Sep 21, 2011 |

$1\quad {\bf Description/Objectives}$

2 Procedure

(Equations 1)

$$ac + a'b + ab'c' (1)$$

| ac + a'b + ab'c' | | | |
|------------------|---|---|---------|
| a | b | c | z (out) |
| 0 | 0 | 0 | 0 |
| 0 | 0 | 1 | 0 |
| 0 | 1 | 0 | 1 |
| 0 | 1 | 1 | 1 |
| 1 | 0 | 0 | 1 |
| 1 | 0 | 1 | 1 |
| 1 | 1 | 0 | 0 |
| 1 | 1 | 1 | 1 |

Figure 1: Truth table of outputs for the function ac + a'b + ab'c'.

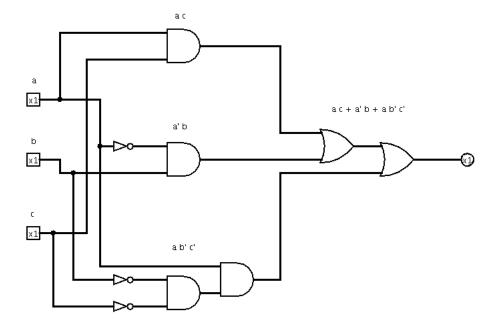


Figure 2: Logic circuit ac + a'b + ab'c' defined in Logisim.

- 3 Observations
- 4 Conclusion
- 5 References
- [1] Logisim, "Logisim, a graphical tool for designing and simulating logic circuits." http://ozark.hendrix.edu/~burch/logisim/, 2011.