Intan_StreamData_128ch_parallelLoop_noEventStruct.vi
C:\INTSY\LVs_Intsy_128ch_w_flex\Intan_StreamData_128ch_parallelLoop_noEventStruct.vi
Last modified on 7/9/2019 at 1:09 AM
Printed on 7/9/2019 at 1:18 PM

Cchans 0 64
Dchans 0 96
C+D chans 0 64
A+B+C+D chans 0 0

Achans 0 0   Bchans 1 33   A+B chans 10 18

Intan-Teensy Bioelectric Module: Data Stream and Visualization
128 chan + ADXL

CircBufRead/WriteCorrect

| Display and Save File | Teensy Comms | VISA resource status | LowLevelDebug | ChanChangeDebug |

ChanSelect
A chans (0-31)

Amplifier Chans
B26
B27
B28
B29
B30
B31
C0
C1
C2
C3
C4
C5
C6
C7
C8
C9
C10
C11
C12
C13
C14
C15
C16
C17
C18
C19
C20
C21
C22
C23
C24
C25
C26
C27
C28
C29

Accel/Vdd/Vflex
ax
ay
az
Vdd
Vflex

Update Channels
Update Stream Chans

Filter settings

LP Filter On/Off   HP Filter On/Off   Bandstop On/Off

LP Cutoff (Hz) 40   HP Cutoff (Hz) 0.5   fl Cutoff (Hz) 48
fh Cutoff (Hz) 52

FS (Hz) 100   DT_scale (sec) 0.01   Data Streaming Frequency

Display Settings

DisplayScale (uV/div) 3200   ChartDisplayLength (s) 5

offset factor 1   Display Update interval (ms) 100

Center Traces OFF   Buffer Read Interval (ms) 100

Save Binary Data
save aux file

Save status
NOT SAVING DATA

STOP SERIAL DATA STREAM   Finish Logging to SD

Vdd amp (V) 3.3

| Buffer Status (write) | VISA read iter | Ring Buffer Size | % buffer full |
| OK 0 | 2749 | Samples per Channel 640000 | 100 |

display iter 2770   Channels 0

Buffer Status (read) OK 0

buffer underflow
buffer overflow
non integer read

Ring Buffer Status

Time (sec)

ax
ay
az
Vflex
Vdd

Acceleration/Vflex (V)

Time (sec)