

Intan\_mainControlPanel\_128ch.vi

C:\INTSY\LVs\_Intsy\_128ch\_w\_flex\Intan\_mainControlPanel\_128ch.vi

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# Intan-Teensy Bioelectric Module: configure hardware and serial port Ambulatory 128 chan

Configure all settings, then click run arrow.

When config is complete, click the purple button to start data stream.

Separate VI will pop open to display signals

Teensy response

ampA: Sending dummy commands to complete ADC calibration....FINISHED!

ampB: Sending dummy commands to complete ADC

Config Result

USB connection verified

Begin and configuring SPI bus now...Finished!

Begin and configuring SPI1 bus now...Finished!

amp A verifying SPI link now.

Reading and printing contents of registers 40-44. Check link is verified. Should spell out INTAN below:

amp B verifying SPI link now.

Reading and printing contents of registers 40-44. Check link is verified. Should spell out INTAN below:

INTAN

amp C verifying SPI link now.

Reading and printing contents of registers 40-44. Check link is verified. Should spell out INTAN below:

INTAN

amp D verifying SPI link now.

Reading and printing contents of registers 40-44. Check link is verified. Should spell out INTAN below:

INTAN

amp A get info now.

Chip ID: RHD2132

Number of amplifiers: 32

Unipolar configuration = common reference

Die revision: 2

amp B get info now.

Chip ID: RHD2132

Number of amplifiers: 32

Unipolar configuration = common reference

Die revision: 2

amp C get info now.

Chip ID: RHD2132

Number of amplifiers: 32

Unipolar configuration = common reference

Die revision: 2

amp D get info now.

Chip ID: RHD2132

Number of amplifiers: 32

Unipolar configuration = common reference

Die revision: 2

amp A configuring registers 0-7 now.

Desired configuration of registers 0-7:

0: 11011110

1: 11000000

2: 101000

3: 10

4: 0

5: 0

6: 0

7: 0

Finished register configuration

VISA resource name

COM15

Serial Port Type

USB connection

Save to SD card

SD File Name Suffix  
(2 digits)

97

Experiment Duration (hr)

1

FileSize(MB)

99

ADC Sampling

Desired Fs (Hz)

100

ADC fs (Hz)

100

DT samp (us)

10000

Serial Streaming

NumMaxStreamingChans

128

Hardware Filter Settings

R\_L source

OFF-chip

Lower cutoff

0.10 Hz

12

Upper cutoff

100 Hz

12

Intan Amp Config

ConfigReg [0:7, aPwr]

Config Hardware Filter

ADC calib

Save Raw Data

Save Display Data

Raw data file path

HeaderFilePath

Click to Start Data Stream