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// GPIO5 -- SX1278's SCK
// GPIO19 -- SX1278's MISnO
#define SCK
#define MISO
                   19
                   23 // GPIO27 -- SX1278's MOSI
#define MOSI
#define SS
                 5 // GPIO18 -- SX1278's CS
                 25 // GPIO14 -- SX1278's RESET
14 // GPIO26 -- SX1278's IRQ(Interrupt Request)
#define RST
#define ADC_PIN ADC1_CHANNEL_0 //ADC 1 canal 0 GPIO36 VP #define DAC_PIN 26
#define PTT_PIN 0
 MOSI: GPIO_23/VSPI_MOSI
MISO: GPIO_19/VSPI_MISO SCK: GPIO_18/VSPI_SCK
 Board specific:
 SS/CS/NSS: GPIO_5
RST/RESET: GPIO_25
 DIOO/IRQ: GPIO_14
```