CHAPTER 3

Multidimensional grids and data

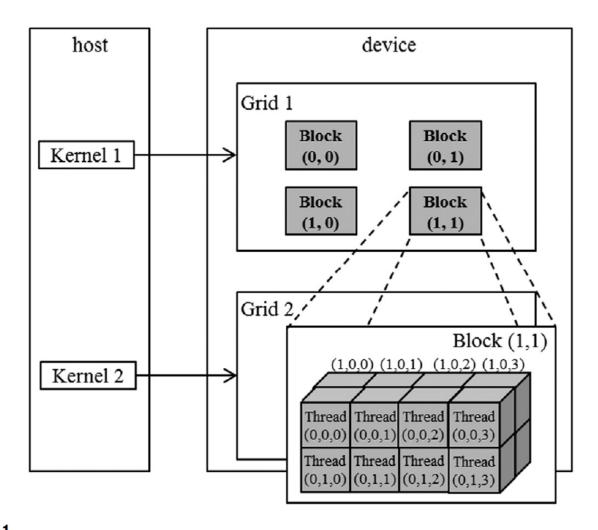


FIGURE 3.1

A multidimensional example of CUDA grid organization.

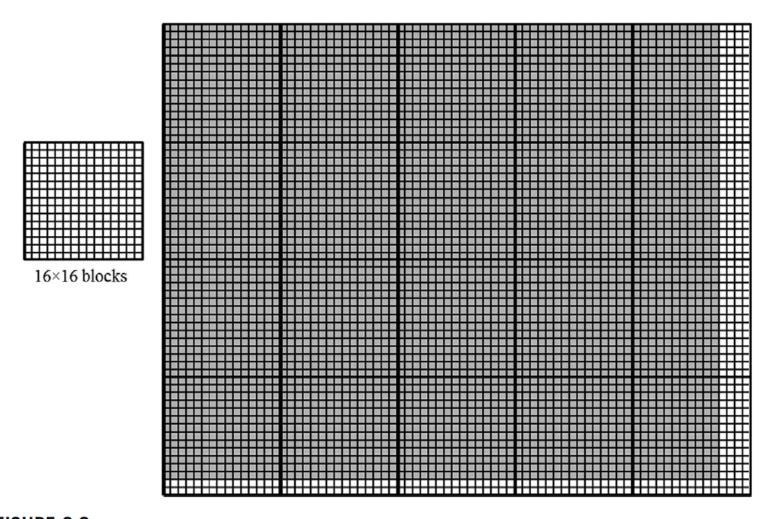
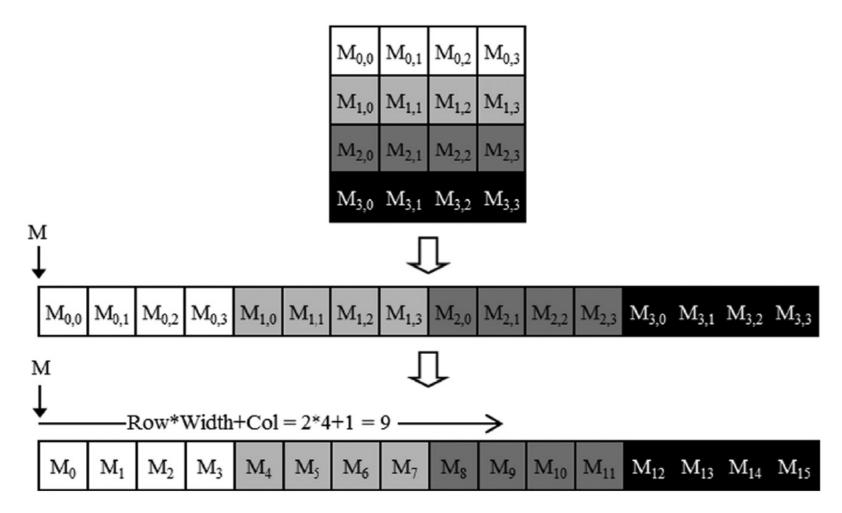


FIGURE 3.2

Using a 2D thread grid to process a 62×76 picture P.



Row-major layout for a 2D C array. The result is an equivalent 1D array accessed by an index expression j*Width+i for an element that is in the jth row and ith column of an array of Width elements in each row.

```
// The input image is encoded as unsigned chars [0, 255]
01
02
    // Each pixel is 3 consecutive chars for the 3 channels (RGB)
0.3
      global
04
    void colortoGrayscaleConvertion(unsigned char * Pout,
05
                    unsigned char * Pin, int width, int height) {
06
        int col = blockIdx.x*blockDim.x + threadIdx.x;
07
        int row = blockIdx.y*blockDim.y + threadIdx.y;
0.8
        if (col < width && row < height) {
09
            // Get 1D offset for the grayscale image
            int grayOffset = row*width + col;
10
11
            // One can think of the RGB image having CHANNEL
12
            // times more columns than the gray scale image
13
            int rgbOffset = grayOffset*CHANNELS;
14
            unsigned char r = Pin[rgbOffset ]; // Red value
15
            unsigned char g = Pin[rgbOffset + 1]; // Green value
16
            unsigned char b = Pin[rgbOffset + 2]; // Blue value
17
            // Perform the rescaling and store it
18
            // We multiply by floating point constants
            Pout[grayOffset] = 0.21f*r + 0.71f*g + 0.07f*b;
19
20
21
```

Source code of colorToGrayscaleConversion with 2D thread mapping to data.

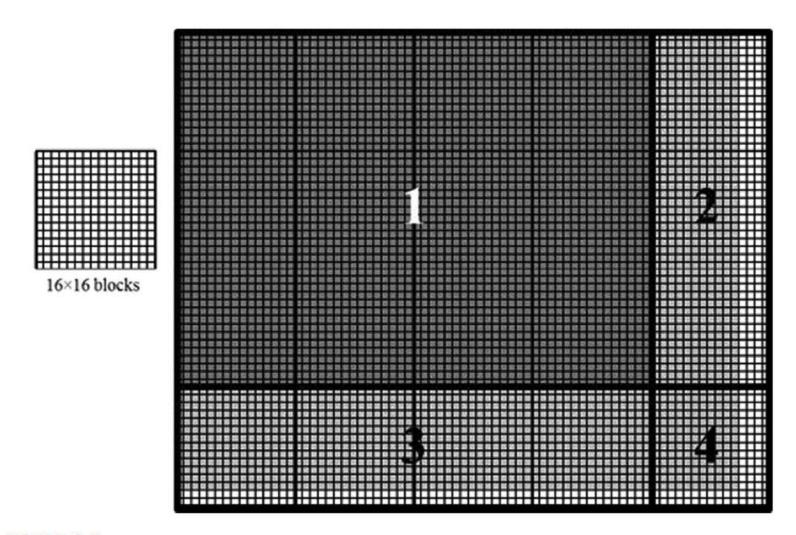


FIGURE 3.5

Covering a 76×62 picture with 16×16 blocks.

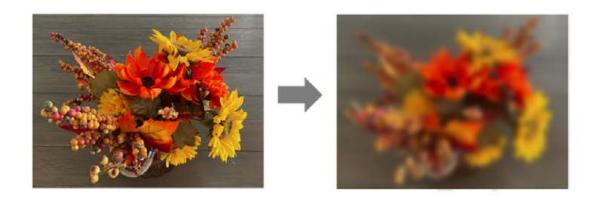
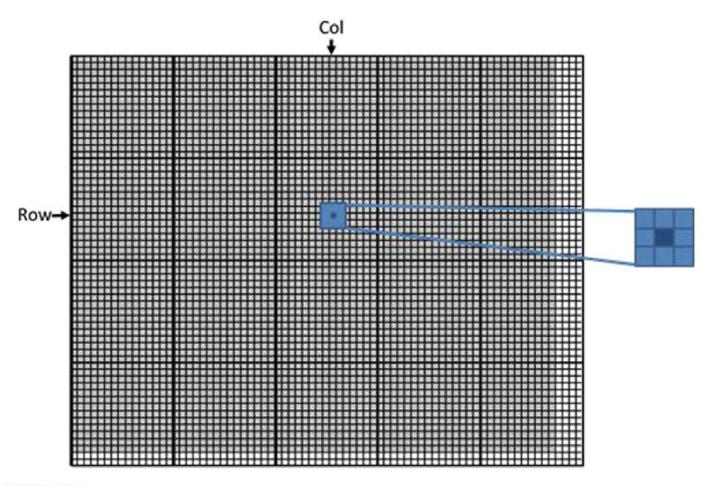


FIGURE 3.6

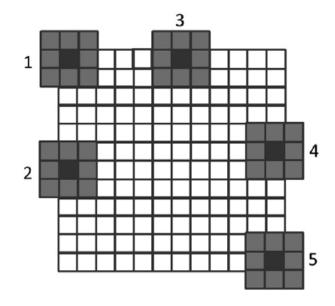
An original image (left) and a blurred version (right).



Each output pixel is the average of a patch of surrounding pixels and itself in the input image.

```
01
      global
02
    void blurKernel (unsigned char *in, unsigned char *out, int w, int h) {
0.3
      int col = blockIdx.x*blockDim.x + threadIdx.x;
04
      int row = blockIdx.y*blockDim.y + threadIdx.y;
0.5
      if(col < w && row < h) {
06
        int pixVal = 0;
07
        int pixels = 0;
09
            // Get average of the surrounding BLUR SIZE x BLUR SIZE box
10
        for (int blurRow=-BLUR SIZE; blurRow<BLUR SIZE+1; ++blurRow) {
11
           for(int blurCol=-BLUR SIZE; blurCol<BLUR SIZE+1; ++blurCol){</pre>
12
              int curRow = row + blurRow;
13
              int curCol = col + blurCol;
14
                    // Verify we have a valid image pixel
15
               if(curRow>=0 && curRow<h && curCol>=0 && curCol<w) {
                   pixVal += in[curRow*w + curCol];
16
17
                   ++pixels; // Keep track of number of pixels in the avg
18
19
20
21
            // Write our new pixel value out
22
            out[row*w + col] = (unsigned char) (pixVal/pixels);
23
24
```

An image blur kernel.



Handling boundary conditions for pixels near the edges of the image.

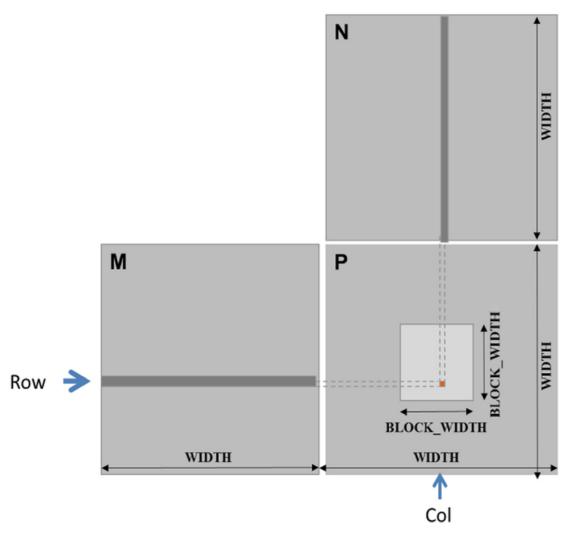


FIGURE 3.10

Matrix multiplication using multiple blocks by tiling P.

```
01
        global void MatrixMulKernel(float* M, float* N,
02
                                       float* P, int Width) {
03
          int row = blockIdx.y*blockDim.y+threadIdx.y;
          int col = blockIdx.x*blockDim.x+threadIdx.x;
04
05
          if ((row < Width) && (col < Width)) {
06
              float Pvalue = 0;
07
              for (int k = 0; k < Width; ++k) {
0.8
                  Pvalue += M[row*Width+k]*N[k*Width+col];
09
10
              P[row*Width+col] = Pvalue;
11
12
```

A matrix multiplication kernel using one thread to compute one P element.

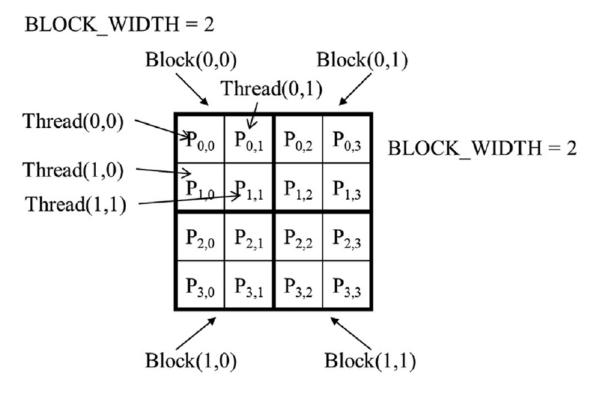


FIGURE 3.12

A small execution example of matrixMulKernel.

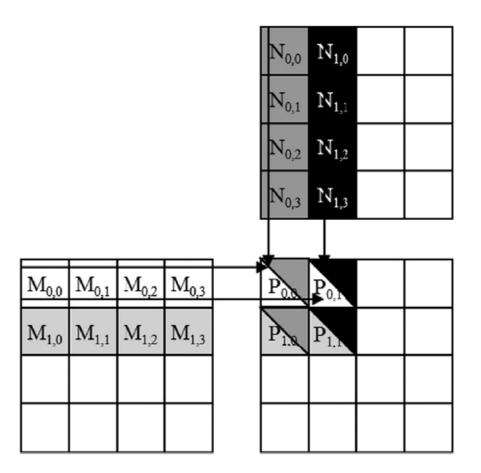


FIGURE 3.13

Matrix multiplication actions of one thread block.

```
dim3 dimGrid(32, 1, 1);
dim3 dimBlock(128, 1, 1);
vecAddKernel<<<dimGrid, dimBlock>>>(...);
```

```
dim3 dog(32, 1, 1);
dim3 cat(128, 1, 1);
vecAddKernel<<<dog, cat>>>(...);
```

```
dim3 dimGrid(ceil(n/256.0), 1, 1);
dim3 dimBlock(256, 1, 1);
vecAddKernel<<<dimGrid, dimBlock>>>(...);
```

vecAddKernel<<<ceil(n/256.0), 256>>>(...);

```
dim3 dimGrid(2, 2, 1);
dim3 dimBlock(4, 2, 2);
KernelFunction<<<dimGrid, dimBlock>>>(...);
```

col = blockIdx.x*blockDim.x + threadIdx.x

int plane = blockIdx.z*blockDim.z + threadIdx.z

row = blockIdx.y*blockDim.y + threadIdx.y

col = blockIdx.x*blockDim.x + threadIdx.x

```
01
       global void foo kernel(float* a, float* b, unsigned int M,
unsigned int N) {
02
          unsigned int row = blockIdx.y*blockDim.y + threadIdx.y;
03
          unsigned int col = blockIdx.x*blockDim.x + threadIdx.x;
          if(row < M && col < N) {
04
05
              b[row*N + col] = a[row*N + col]/2.1f + 4.8f;
06
07
0.8
      void foo(float* a d, float* b d) {
09
          unsigned int M = 150;
10
          unsigned int N = 300;
11
          dim3 bd(16, 32);
12
          \dim 3 \operatorname{gd}((N-1)/16+1, (M-1)/32+1);
13
          foo kernel <<< gd, bd >>>(a d, b d, M, N);
14
```