

EE108B Lab Report Guidelines

Below are the general guidelines. The handout for each lab may have additional guidelines, so be sure to read those, too.

All EE108B reports **must** be submitted in PDF format. You can find PDF writers on all the lab machines or via ghostscript on the Sweet Hall machines or under Linux. Under Mac OS X you can choose "Save as PDF" from the print dialog. The files should be in the format of **lastname1_lastname2_lab#.pdf**, please!

Contents. The reports should be thorough enough to demonstrate what you did and what you learned and to allow another person to replicate your work. You should not spend much time explaining the problem you are solving as it is defined in the lab itself. (That is, you should say, "I implemented Conway's game of life," and not spend a page describing the details of the game, but you must describe how you did your memory access to calculate the next state, for example.) Remember: someone is going to have to read this so the point is NOT to make it long. Make it short and sweet while covering the key points and features of your design and why you chose them. **WE WILL NOT READ ANY PORTION OF YOUR REPORT BEYOND THE FIRST 3 PAGES.** Your code will be examined, but it will be impossible to understand if your description of your design is not clear.

1. Title page - name, project, date (this does not count toward the 3 page limit)
2. Introduction - what you did
3. Design - how you did it: hierarchy and design decisions; descriptions of FSMs and how they interact go here, e.g., block diagrams and flow charts. List any design aids, e.g., custom scripts or other tools, how you used them and how they work. Basically whatever you think it takes to make it clear to the TA how your design works and interacts.
4. Results - how it worked out
5. Conclusions - what did you think of it? Was it a good/bad design/implementation? **Why?** What would you do differently next time? Any comments on the lab assignment itself are appreciated.

Additional Items to Submit:

6. Simulations - show **KEY** simulations and scripts. You **MUST** annotate the simulations so the TA can see what is happening and why this is important. This should directly relate to the design above and show why it was a good/bad one.
7. Implementation - your Verilog/assembly code. All of it, with enough comments so the TA can read it and understand it. Specific modules should be referenced from the Design section above. Do not include code in the PDF itself.

When submitting electronically, combine the following items into a single zip archive:

- Your report PDF
- A folder of simulation PDFs (if the lab has a simulation component)
- Your source code (MIPS or Verilog files)

The starter code distribution should run correctly when your submitted source files replace the original versions. Test this to make sure you know exactly what you modified.