

$I(dq)$ is in the range of 125mA to 250mA, to even 500mA!

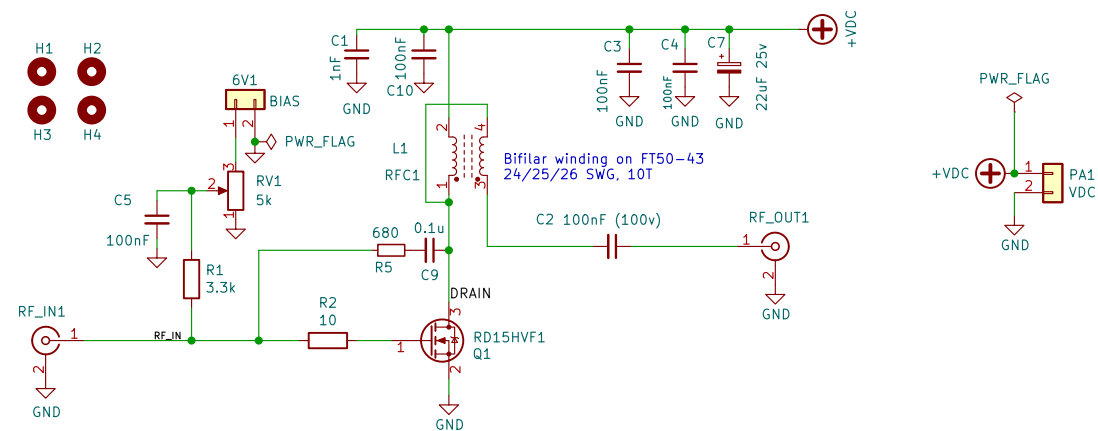
Ideally $VDC == 12.5v$ for RD15HVF1.

The 100nF + 680R negative feedback circuit is calculated for RD15HVF1.

Negative feedback is compulsory so that $I(dq)$ can be properly set, without the MOSFET trying to amplify spurious signals when the DC bias is increased.

Motivation: IRF510 is awesome @ 24v but we want to operate at 13.8v with higher efficiency at 21 MHz and 28 MHz in the field.

(VU2ASH) The PCB layout should be linear in PA as the signal proceeds, to avoid input-output feedback especially at higher frequency. It improves stability.



Robust Class-C / Class-D Single-ended RD15HVF1 powered HF PA		
Uses ideas from VU2ASH, HAMBREWERS, QRP Labs, Tom (AK2B), VK3PE, and G6LBQ		
Author: Dhiru Kholia (VU3CER)		
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