Motivation: We want higher power and efficiency at 21 MHz and 28 MHz. (VU2ASH) The PCB layout should be linear in PA as the signal proceeds, to avoid input—output feedback especially at higher frequency. It improves stability. PWR_FLAG GND L1 Bifilar winding on FT50-43 24/25/26 SWG, 10T RFC1 RF_OUT1 C2 100nF (100v) 1-0 COLLECTOR RF_IN1 Q1 NTE-224 D1 1N4148 GND GND Robust Class-C / Class-D Single-ended TRANSISTOR powered HF PA Uses ideas from VU2ASH, HAMBREWERS, QRP Labs, Tom (AK2B), VK3PE, and G6LBQ Author: Dhiru Kholia (VU3CER) Sheet: / File: Finals-Transistor.kicad_sch Title: Easy-PA-With-Pluggable-LPF Date: 2022-03-18 Rev: v0.01 KiCad E.D.A. kicad 6.0.4-6f826c9f35~116~ubuntu21.10.1 ld: 1/1