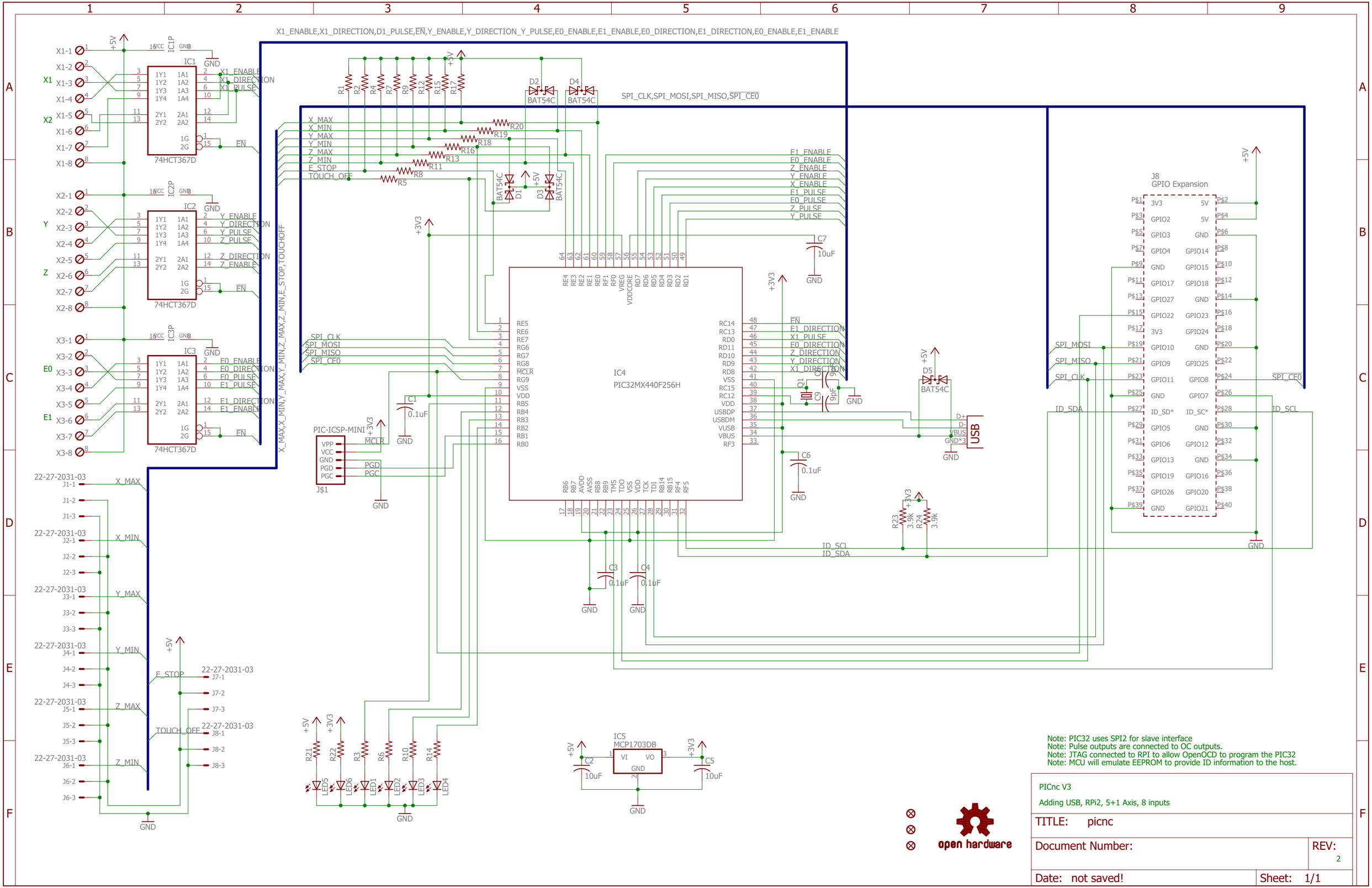
X1_ENABLE,X1_DIRECTION,D1_PULSE,EN,Y_ENABLE,Y_DIRECTION_Y_PULSE,E0_ENABLE,E1_ENABLE,E0_DIRECTION,E1_DIRECTION,E0_ENABLE,E1_ENABLE
SPI_CLK,SPI_MOSI,SPI_MISO,SPI_CE0
X_MAX,X_MIN,Y_MAX,Y_MIN,Z_MAX,Z_MIN,E_STOP,TOUCHOFF
IC1
74HCT367D
IC2
74HCT367D
IC3
74HCT367D
IC4
PIC32MX440F256H
IC5
MCP1703DB
J8
GPIO Expansion
USB
PIC-ICSP-MINI
J$1
22-27-2031-03
Note: PIC32 uses SPI2 for slave interface
Note: Pulse outputs are connected to OC outputs.
Note: JTAG connected to RPI to allow OpenOCD to program the PIC32
Note: MCU will emulate EEPROM to provide ID information to the host.
PICnc V3
Adding USB, RPi2, 5+1 Axis, 8 inputs
TITLE: picnc
Document Number:
REV: 2
Date: not saved!
Sheet: 1/1
open hardware