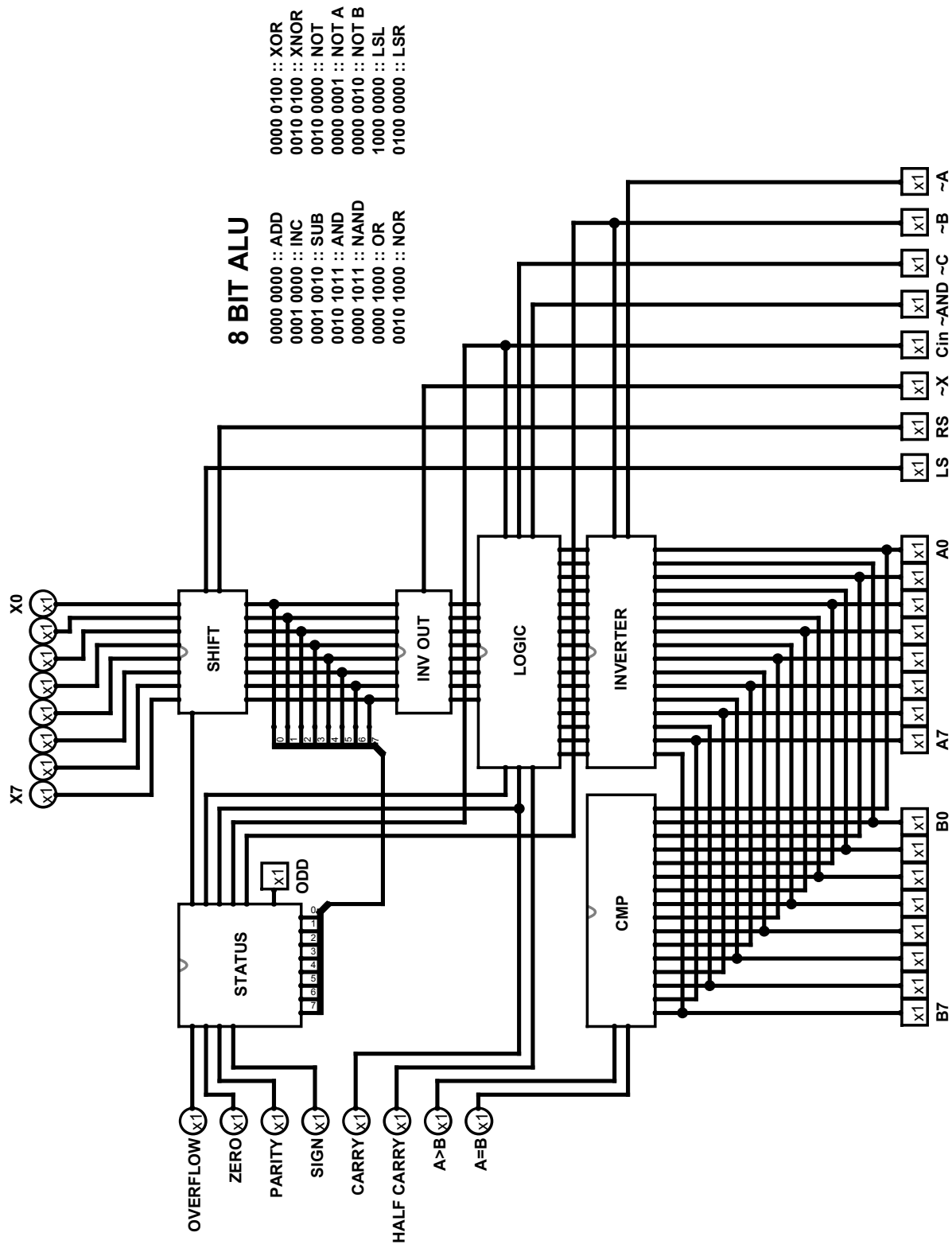
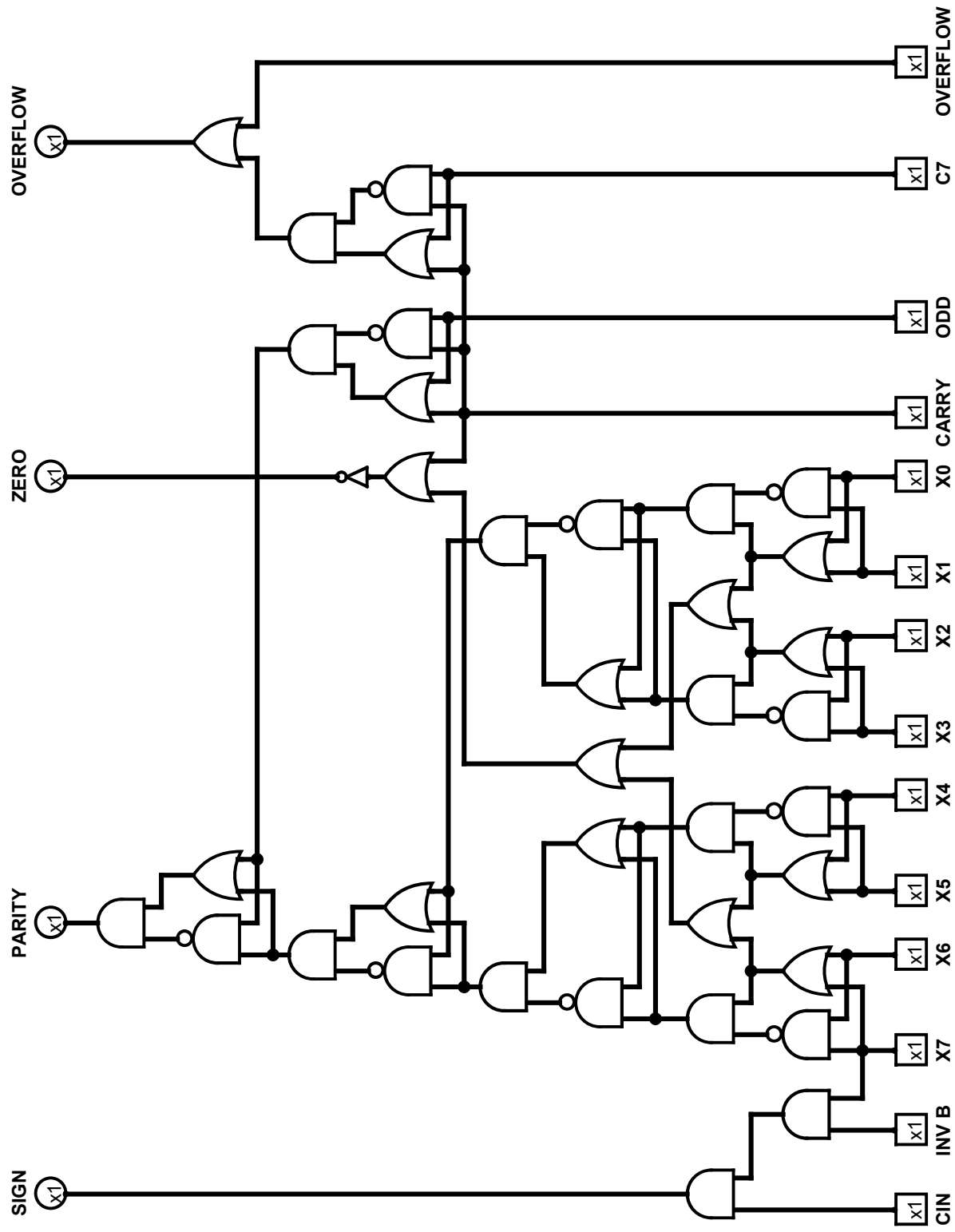


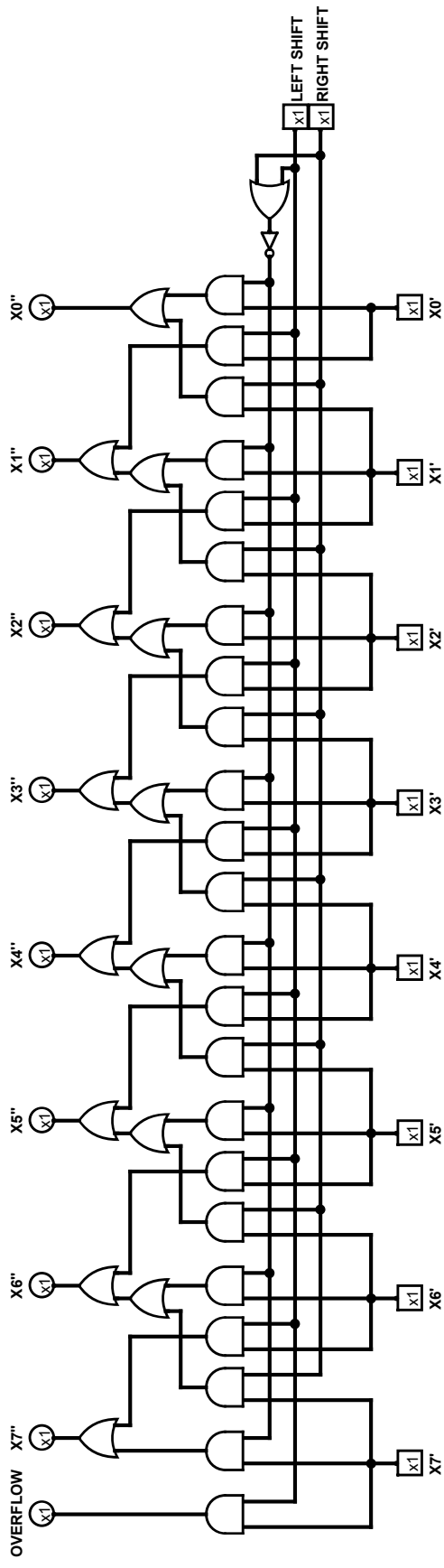
ALU (1 of 7)

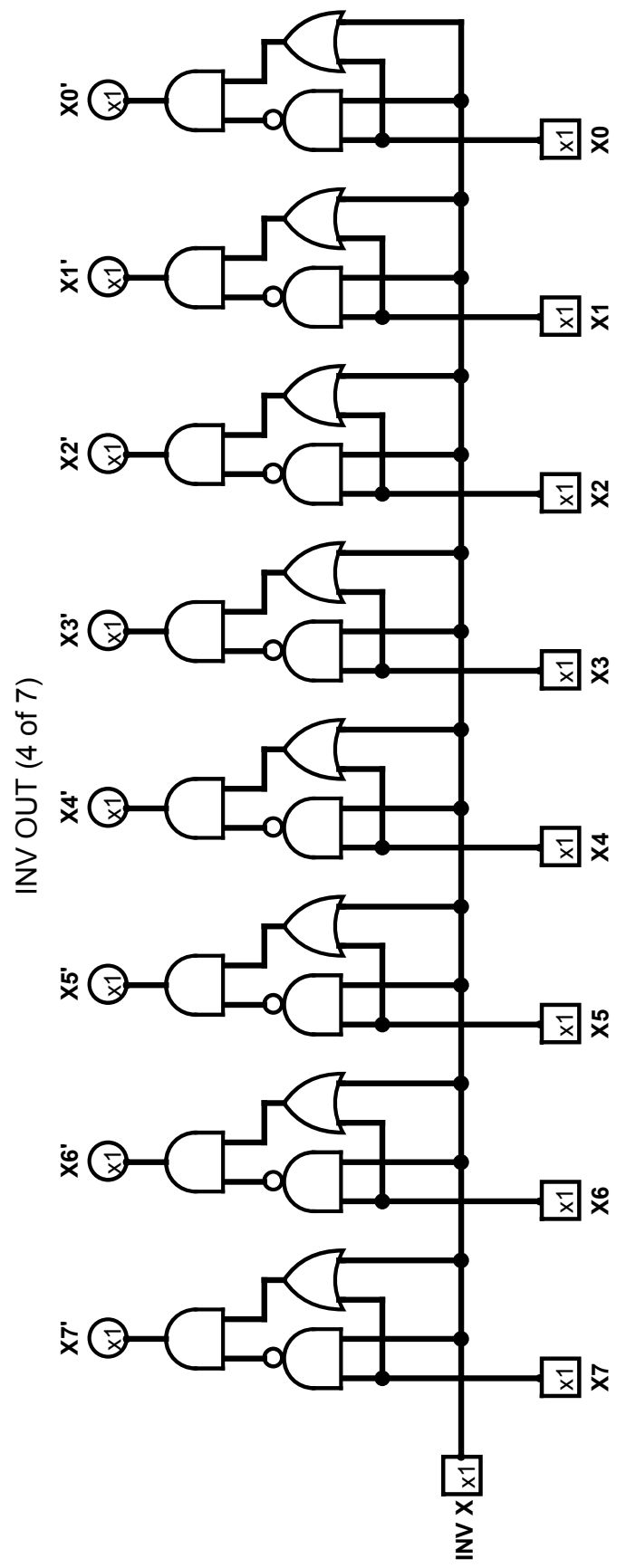


STATUS (2 of 7)

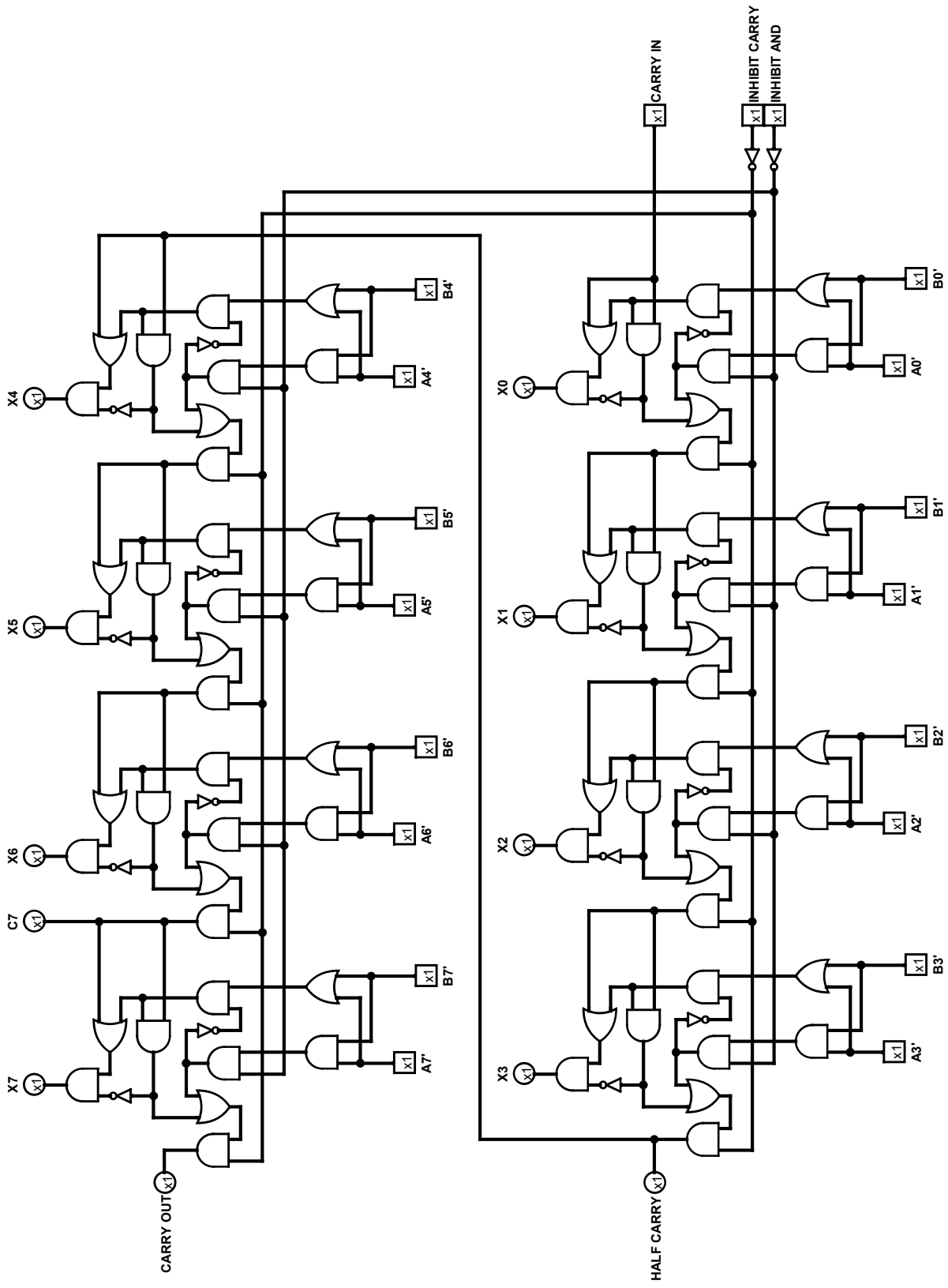


SHIFT (3 of 7)

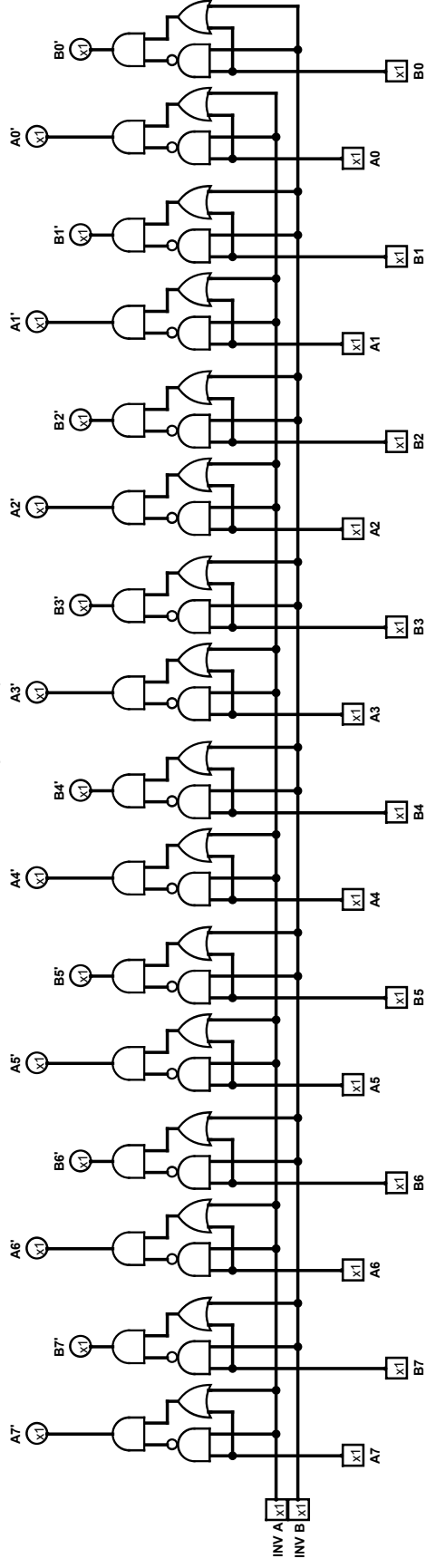




LOGIC (5 of 7)



INV A/B (6 of 7)



CMP (7 of 7)

