A\B	INA	Ca	Cb	Cc	Cd	Ct	IAR	P	PIPAR
INA	0	2 (RIM)	2 (RIM)	2 (RIM)	2 (RIM)	2 (RIM)	3 (AMR)		(time)
Ca	1	0	2 (RIM, *)	2 (RIM, *)	2 (RIM, *)	2 (RIM, *)	2 (CIM)	2 (CIM)	2 (CIM)
Cb	1	1	0	2 (RIM, *)	2 (RIM, *)	2 (RIM, *)	2 (CIM*)	2 (CIM*)	2 (CIM*)
Cc	1	1	1	0	2 (RIM, *)	2 (RIM, *)	2 (CIM*)	2 (CIM*)	2 (CIM*)
Cd	1	1	1	1	0	2 (RIM, *)	2 (CIM*)	2 (CIM*)	2 (CIM*)
Ct	1	1	1	1	1	0	2 (CIM*)	2 (CIM*)	2 (CIM*)
IAR	1	1	1	1	1	1	0	2 (AMR, time)	2 (time)
P	1	1	1	1	1	1	1	0	2 (AMR)
PIPAR	1	1	1	1	1	1	1	1	0

Node A is attempting allocation & will be eventual winner
Node B is "other"; state refers to Node B's idea of Node A's state, not Node B's own internal state during allocation

0 Proper operation: State machines synchronized
1 Normal transient: Node B has not received & processed message(s) yet

2 Invalid: State is in advance of A transitions during initial sequence, or has missed transitions in later sequences

3 Needs examination: No message indicates the alias has been dropped