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## **Acronyms**

**CIL** Common Intermediate Language

**CLI** Common Language Infrastructure

**CLS** Common Language Specification

**CTS** Common Type System

**IL** Intermediate Language

**ISO** International Organization for Standardization

**MSIL** Microsoft Intermediate Language

**VES** Virtual Execution System

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# 1 Introduction

## 1.1 The Common Language Infrastructure

*The Common Language Infrastructure (CLI)* is a specification developed by *Microsoft* and standardized by *ISO* and *ECMA International* [1, 2]. It describes executable code and an environment that allows numerous programming languages to be executed on various platforms.

The following four main aspects are covered by the Common Language Infrastructure [2]:

- ***The Common Type System (CTS)*** - a type system supporting types and operations found in many programming languages;
- ***Metadata*** - used for describing and referencing types defined by the CTS;
- ***The Common Language Specification (CLS)*** - an agreement between language designers and framework designers specifying a subset of the CTS and a set of usage conventions;
- ***The Virtual Execution System (VES)*** - responsible for loading and executing programs written for the CLI.

There are multiple implementations of the CLI, for instance: *.NET Framework*, *Shared Source Common Language Infrastructure Implementation*, *.NET Core* and *Mono*.

## 1.2 The Common Intermediate Language

*The Common Intermediate Language (CIL)* is also known as *the Microsoft Intermediate Language (MSIL)* or simply *the Intermediate Language (IL)* [3]. It is an object-oriented programming language that is a part of the CLI. Each language compatible with the CLI is compiled into the CIL. Moreover, the CIL is the actual language executed by the VES. The language is a reasonable compromise between user-friendly, high-level programming languages and low-level assemblers. Although the CIL is human-readable, it is still a stack-based language and writing programs manually is therefore quite difficult.

Technically, the CIL is just a set of over 200 instructions and it does not define a syntax for describing *the CLI metadata*. There is another syntax called *ILAsm* - an assembly language for the CIL [2]. However, many sources refer to the CIL and ILAsm interchangeably. Thus, *the CIL* means hereinafter the whole ILAsm syntax and *the CIL instruction set* refers to the correct meaning of the Common Intermediate Language.

---

```
1 .assembly extern mscorlib {}
2
3 .assembly HelloWorld {}
4
```

```

5  .method static public void main() cil managed
6  {
7      .entrypoint
8      .maxstack 1
9      ldstr "Hello world!"
10     call void [mscorlib]System.Console::WriteLine(string)
11     ret
12 }

```

---

Code listing 1.1: *Hello world* program.

The difference between ILAsm and the CIL can be easily understood by analysing the *Hello world* program shown in code listing 1.1. Lines 1-8 and 12 of the example contain the CLI metadata described in ILAsm syntax whereas lines 9-11 contain actual CIL instructions. The detailed meaning of each line can be explained as follows:

- Line 1 informs that an external assembly (`mscorlib`) should be loaded.
- Line 3 defines the assembly that should be created as the result of the compiler.
- Line 5 declares a static public method (`main`). It also defines that the method contains CIL code.
- Line 6 begins the body of the `main` method.
- Line 7 informs that the method should be used as the entry point of the program - it should be called by the compiler as the very first method.
- Line 8 specifies the maximal size of the evaluation stack associated with the method.
- Line 9 contains a `ldstr` instruction. It tells the compiler to put the *Hello world!* string onto the evaluation stack.
- Line 10 contains a `call` instruction. It tells the compiler to call the `WriteLine` method from the `System.Console` class defined in the `mscorlib` assembly.
- Line 11 contains a `ret` instruction. It tells the compiler to return the result of the method and to transfer the control to the next method on the call stack.
- Line 12 ends the body of the `main` method.

### 1.3 ### TO DO: ZALOZENIA, CEL PRACY I ZAKONCZENIE WSTEPU ###

## 2 The CIL instruction set

In order to understand how the CIL instructions work, this section explains the concept of the global state and provides code examples introducing several CIL instructions. As the whole set contains more than 200 instructions, further information on each of them can be found in [2] and [1].

### 2.1 The global state

The CLI can manage multiple threads of control at the same time. A single thread of control can be thought as a call stack consisting of multiple method states. Although all the threads of control are rather independent, they still can access multiple managed heaps allocated in the shared memory space [2]. This concept is presented in figure 1.

A single method state includes several items required for the VES to execute the method [2], inter alia:

- an instruction pointer - it determines the next instruction that should be executed within the method;
- an evaluation stack - it stores evaluation values;
- a method information handle - it contains read-only method information;
- a local variable array;
- an argument array.

Generally, a single instruction can be understood as a description of how to manipulate the corresponding method state - there are only a few exceptions to this rule. Some instructions only require access to the evaluation stack while some do need to change other parts of the global state. To better understand these differences, the examples below present a couple of instructions and describe what effect they have on the global state.

### 2.2 The evaluation stack

As described above, a method state contains an evaluation stack. While a rich set of data types can be represented in memory, only a very limited subset of those types are supported by the CLI on an evaluation stack. This subset consists of the following data types [2]:

- `int32`,
- `int64`,
- `native int`,
- `F` (a floating-point number),



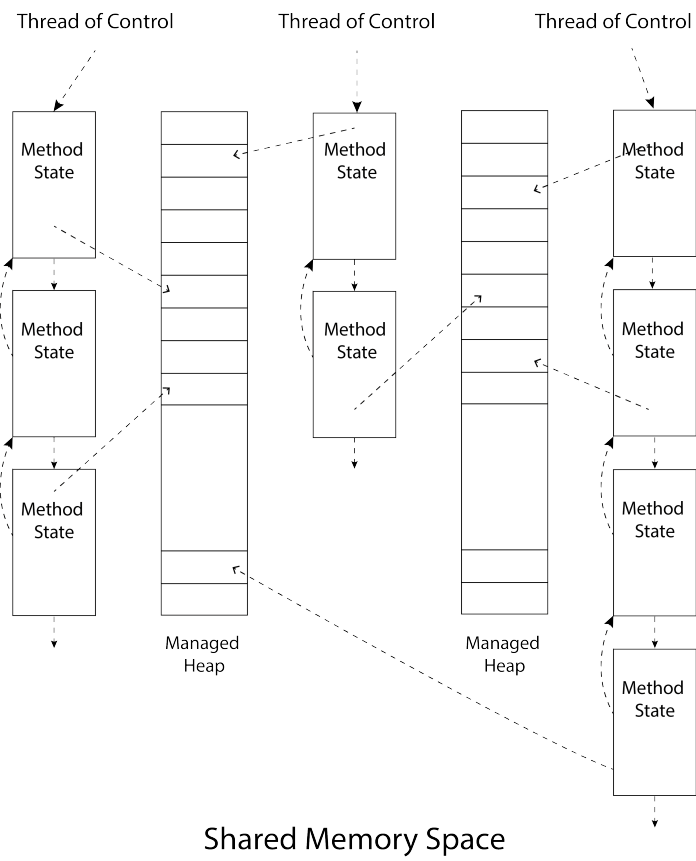


Figure 1: The global state concept.

- 0 (an object reference),
- & (a managed pointer),
- `native unsigned int` (also an unmanaged pointer),
- a user-defined value type.

## 2.3 Stack transformations

A stack transformation can be thought as a sequence of pop and / or push operations performed on an evaluation stack. The majority of the CIL instructions uses such a sequence to get some values from the corresponding stack or to push some values onto it. In order to describe and visualise the effect of an instruction, the following syntax is used:

- $S$  denotes an evaluation stack;
- $S \rightarrow S \cdot v_1 \cdot v_2 \cdot \dots \cdot v_n$  denotes a stack transformation which is equivalent to pushing the values  $v_1, v_2, \dots, v_n$  onto the stack  $S$ ;
- $S \cdot u_1 \cdot u_2 \cdot \dots \cdot u_n \rightarrow S$  denotes a stack transformation which is equivalent to popping the values  $u_1, u_2, \dots, u_n$  from the stack  $S$ ;
- $S \cdot u_1 \cdot u_2 \cdot \dots \cdot u_n \rightarrow S \cdot v_1 \cdot v_2 \cdot \dots \cdot v_m$  denotes a stack transformation which is equivalent to popping values  $u_1, u_2, \dots, u_n$  from the stack  $S$  and then pushing the values  $v_1, v_2, \dots, v_m$  onto it.

## 2.4 Stack values

To illustrate the difference between various stack values, the following syntax represents the value  $v$  of the stack type `type`:

$$v_{\text{type}}$$

. For instance:

- $0_{\text{int32}}$  represents 0 of type `int32`.

## 2.5 Examples

### 2.5.1 `ldc.i4.0`

The `ldc.i4.0` instruction pushes 0 onto the evaluation stack as `int32`. The corresponding stack transformation can be presented as follows [2]:

$$S \rightarrow S \cdot 0_{\text{int32}}$$

The program shown in code listing 2.1 uses the instruction to push 0 onto the stack and then writes out the top stack value (0) using the `call` instruction.

---

```

1  .assembly extern mscorlib {}
2
3  .assembly HelloWorld {}
4
5  .method static public void main() cil managed
6  {
7      .entrypoint
8      .maxstack 1
9      ldc.i4.0
10     call void [mscorlib]System.Console::WriteLine(int32)
11     ret
12 }

```

---

Code listing 2.1: Usage of the `ldc.i4.0` instruction.

The CIL provides a number of similar instructions to push other constant values onto the stack: `ldc.i4.1`, `ldc.i4.2`, `ldc.i4.3`, `ldc.i4.4`, `ldc.i4.5`, `ldc.i4.6`, `ldc.i4.7`, `ldc.i4.8` and `ldc.i4.m1` (or `ldc.i4.M1`) which pushes `-1` onto the stack.

### 2.5.2 `stloc.0` and `ldloc.0`

The `stloc.0` instruction pops a value  $v$  from the evaluation stack and moves it to the 0-th position in the local variable array. Its stack transformation can be represented as follows [2]:

$$S \cdot v \rightarrow S$$

Conversely, the `ldloc.0` instruction loads the 0-th local variable  $v$  onto the evaluation stack and its stack transformation is [2]:

$$S \rightarrow S \cdot v$$

The program shown in code listing 2.2 uses both instruction to store 7 in the 0-th local variable and then to load it onto the stack.

There are several similar instructions provided by the CIL: `stloc.1`, `stloc.2`, `stloc.3`, `stloc`, `stloc.s`, `ldloc.1`, `ldloc.2`, `ldloc.3`, `ldloc`, `ldloc.s`.

---

```

1  .assembly extern mscorlib {}
2
3  .assembly HelloWorld {}
4
5  .method static public void main() cil managed
6  {
7      .entrypoint
8      .maxstack 1
9      .locals init ([0] int32 n)
10     ldc.i4.7

```

---

```
11     stloc.0
12     ldloc.0
13     call void [mscorlib]System.Console::WriteLine(int32)
14     ret
15 }
```

---

Code listing 2.2: Usage of the `stloc.0` and `ldloc.0` instructions.

### 2.5.3 ### TO DO: PRZYKLADY UZYCIA INNYCH INSTRUKCJI ###

### 3 The semantics

As described in 2.1, an instruction can be thought as a description of how to manipulate the current global state. Examples of such informal descriptions can be found in 2.5. [2] presents further examples but it does not provide any formal way to describe the CIL instructions and their semantics. Thus, one of the goals of the thesis is to introduce a formal, mathematical model to represent the semantics of the CIL.

The semantics presented below is patterned upon the example of the CIL semantics presented in [4]. However, this thesis attempts to simplify it and to represent the semantics on a higher level of abstraction.

#### 3.1 The global state

The global state described in 2.1 can be represented as an ordered pair

$$\sigma = \langle \tau, \eta \rangle \quad (3.1)$$

where  $\sigma$  is a global state,  $\tau$  is a set of states of the corresponding threads of control and  $\eta$  is a set of states of the managed heaps.

The following formula could be potentially used to describe the semantics [5]:

$$\langle I, \sigma_1 \rangle \rightarrow \sigma_2. \quad (3.2)$$

It can be understood as follows: if the program is in the state  $\sigma_1$  and it executes the instruction  $I$ , the result state is  $\sigma_2$ . However, formula 3.2 treats the state and the current instruction as separate beings while the global state of the CIL contains the instruction pointer hence the formula can be simplified:

$$\sigma_1 \rightarrow \sigma_2. \quad (3.3)$$

.

#### 3.2 The execution state

Since the global state and its mathematical representation can be very complex, this section is to provide a simpler concept called hereinafter *the execution state*. As a single instruction can only change a state of a single thread of control, there is no need to include the whole global state in the semantics. The execution state is then a part of the global state that could be potentially changed by an instruction and can be represented as an ordered pair:

$$\langle \gamma, \eta \rangle \quad (3.4)$$

where  $\gamma$  is the corresponding thread of control (its state) and  $\eta$  is a set of states of the managed heaps.

The semantics can be now presented as formulas of the following shape:

$$\langle \gamma_1, \eta_1 \rangle \rightarrow \langle \gamma_2, \eta_2 \rangle. \quad (3.5)$$

The formula has an analogous meaning to formula 3.3 however it operates on the execution state while the rest of the global state remains unchanged. For transparency, the angle brackets can be now omitted.

### 3.3 The method state

As mentioned before, the state of a single thread of control can be understood as a call stack of method states. Thus a similar syntax to the one introduced in section 2.3 is used to illustrate the changes made on a call stack. A single method state can be then described using a tuple

$$\langle I, S, M, L, A \rangle \quad (3.6)$$

where:

- $I$  is the instruction pointer,
- $S$  is the evaluation stack,
- $M$  is the method information handle,
- $L$  is the local variable array,
- $A$  is the argument array.

### 3.4 The final syntax

Taken all together, the semantics of the CIL can be represented as a set of formulas of the following shape:

$$\gamma_1 \cdot \mu_{1,1} \cdot \dots \cdot \mu_{1,n}, \eta_1 \rightarrow \gamma_2 \cdot \mu_{2,1} \cdot \dots \cdot \mu_{2,m}, \eta_2 \quad (3.7)$$

where

- $\gamma_1, \gamma_2$  are call stacks,
- $\mu_{1,1}, \dots, \mu_{1,n}, \mu_{2,1}, \dots, \mu_{2,m}$  are method states,
- $\eta_1, \eta_2$  are sets of managed heaps.

For instance, the following describes a transformation which pushes 0 of type `int32` onto the evaluation stack of the top method state:

$$\gamma \cdot \langle I, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I, S \cdot 0_{\text{int32}}, M, L, A \rangle, \eta.$$

### 3.5 Instruction pointers

To show what actually instruction is pointed by the instruction pointer  $I$ , the following formula is used:

$$I \nearrow \text{instruction}. \quad (3.8)$$

No special mathematical syntax is used to describe various instruction types. For example, the following formula means that the instruction pointer  $I$  points to a `ldc.i4.0` instruction:

$$I \nearrow \text{ldc.i4.0}.$$

Additionally, according to the syntax presented in [4],  $I_n$  denotes hereinafter a pointer to the  $n$ -th instruction of a method.

### 3.6 Arrays

A single method state contains 2 different arrays: a local variable array and an argument array. An array  $A$  can be represented as a function associating a single value to each natural number that is less than the length of the array. In order to formalise the changes made on arrays, the following term is used:

$$A[n \mapsto v] \quad (3.9)$$

It can be understood as the array  $A$  with the  $n$ -th element set to the value  $v$ . Formally [4]:

$$(A[n \mapsto v])(i) = \begin{cases} v & \text{if } i = n, \\ A(i) & \text{otherwise.} \end{cases} \quad (3.10)$$

### 3.7 Examples

This section follows the informal descriptions contained in 2.5 and uses the conventions described above in order to specify the semantics of several CIL instructions which can be found in formulas 3.11-??.

$$\frac{I_n \nearrow \text{ldc.i4.0}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot 0_{\text{int32}}, M, L, A \rangle, \eta} \quad (3.11)$$

$$\frac{I_n \nearrow \text{ldc.i4.1}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot 1_{\text{int32}}, M, L, A \rangle, \eta} \quad (3.12)$$

$$\frac{I_n \nearrow \text{ldc.i4.2}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot 2_{\text{int32}}, M, L, A \rangle, \eta} \quad (3.13)$$

$$\frac{I_n \nearrow \text{ldc.i4.3}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot 3_{\text{int32}}, M, L, A \rangle, \eta} \quad (3.14)$$

$$\frac{I_n \nearrow \text{ldc.i4.4}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot 4_{\text{int32}}, M, L, A \rangle, \eta} \quad (3.15)$$

$$\frac{I_n \nearrow \text{ldc.i4.5}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot 5_{\text{int32}}, M, L, A \rangle, \eta} \quad (3.16)$$

$$\frac{I_n \nearrow \text{ldc.i4.6}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot 6_{\text{int32}}, M, L, A \rangle, \eta} \quad (3.17)$$

$$\frac{I_n \nearrow \text{ldc.i4.7}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot 7_{\text{int32}}, M, L, A \rangle, \eta} \quad (3.18)$$

$$\frac{I_n \nearrow \text{ldc.i4.8}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot 8_{\text{int32}}, M, L, A \rangle, \eta} \quad (3.19)$$

$$\frac{I_n \nearrow \text{ldc.i4.m1}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot (-1)_{\text{int32}}, M, L, A \rangle, \eta} \quad (3.20)$$

$$\frac{I_n \nearrow \text{ldc.i4.M1}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot (-1)_{\text{int32}}, M, L, A \rangle, \eta} \quad (3.21)$$

$$\frac{I_n \nearrow \text{stloc.0}}{\gamma \cdot \langle I_n, S \cdot v, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S, M, L[0 \mapsto v], A \rangle, \eta} \quad (3.22)$$

$$\frac{I_n \nearrow \text{stloc.1}}{\gamma \cdot \langle I_n, S \cdot v, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S, M, L[1 \mapsto v], A \rangle, \eta} \quad (3.23)$$

$$\frac{I_n \nearrow \text{stloc.2}}{\gamma \cdot \langle I_n, S \cdot v, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S, M, L[2 \mapsto v], A \rangle, \eta} \quad (3.24)$$

$$\frac{I_n \nearrow \text{stloc.3}}{\gamma \cdot \langle I_n, S \cdot v, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S, M, L[3 \mapsto v], A \rangle, \eta} \quad (3.25)$$

$$\frac{I_n \nearrow \text{ldloc.0}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot L(0), M, L, A \rangle, \eta} \quad (3.26)$$

$$\frac{I_n \nearrow \text{ldloc.1}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot L(1), M, L, A \rangle, \eta} \quad (3.27)$$



$$\frac{I_n \nearrow \text{ldloc.2}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot L(2), M, L, A \rangle, \eta} \quad (3.28)$$

$$\frac{I_n \nearrow \text{ldloc.3}}{\gamma \cdot \langle I_n, S, M, L, A \rangle, \eta \rightarrow \gamma \cdot \langle I_{n+1}, S \cdot L(3), M, L, A \rangle, \eta} \quad (3.29)$$

### TO DO: SEMANTYKA INSTRUKCJI stloc.s, stloc, ldloc.s, ldloc  
###

## 4 The interpreter

The main part of this thesis is the interpreter which reads a CIL source code and is able to execute it.

### TO DO: KILKA ZDAN / AKAPITOW O TEORII INTERPRETACJI NA PODSTAWIE ZRODEL ###

### 4.1 The lexer and the parser

### TO DO: KILKA ZDAN O GRAMATYCE, TOKENIZACJI, I PAR-SOWANIU KODU DO AST ###

### 4.2 Technical background

In order to focus on the semantics of the CIL and not on data types, the interpreter has been written in C# which is itself compiled into the CIL so it uses the same type system and the same external assemblies.

The interpreter is a console application based on the **.NET Framework 4.7.2** so it can be executed in **Windows** operating system with the appropriate framework installed. The program can be run with the following command line argument:

- `-fileName fileName` - a path to a text file containing the source code to be interpreted.

The standard input, standard output and standard error streams are redirected to the console in order to allow the interpreted program to interact with the user. For instance, the program shown in code listing 1.1 calls the `Console.WriteLine` method to write out the *Hello world!* string to the standard output. The interpreter redirects the output therefore the string appears in the console. However, when an interpreter exception is caught, it is also written to the standard error stream.

### 4.3 Detection of non-supported features

Before interpreting the provided source code, the application checks if the code is supported by the interpreter. Once a non-supported feature is detected, one of the following exceptions is thrown and written out to the console:

- `InstructionNotSupportedException`,
- `FeatureNotSupportedException`.

### 4.4 The actual interpreter

In order to implement the interpreter, a couple of concepts described before needed to be modelled in C#. For example, the `CilExecutionState` class is responsible for the execution state explained in 3.4 and can be seen in listing 4.1. There are other classes which have been implemented by analogy, inter alia:

- CilControlState,
- CilMethodState,
- CilEvaluationStack.

---

```

1 public class CilExecutionState
2 {
3     public CilControlState ControlState { get; }
4
5     public CilManagedMemory ManagedMemory { get; }
6
7     public CilExecutionState(CilControlState controlState,
8                             CilManagedMemory managedMemory)
9     {
10         ControlState = controlState;
11         ManagedMemory = managedMemory;
12     }
13 }
```

---

Code listing 4.1: The CilExecutionState class.

#### 4.4.1 The visitor

Since the semantics presented in 3 bases on transformations of the execution state, the same concept has been used to implement the interpreter. A single transformation of the state always depends on the current instruction pointer so the process of interpreting can be thought as visiting various instructions presented in the abstract syntax tree. To implement such a behaviour, a design pattern called *Visitor* has been used. The pattern allows to write a new operation for each element of an object structure [6].

**CilInstructionsVisitor** shown in listing 4.2 is an abstract class which implements the Visitor design pattern. In order to make its implementation reusable, the class does not decide what instruction should be visited next. Instead, an abstract method **GetNextInstruction** is used. As the CIL instruction set contains around 200 instructions, the visitor itself has been split up into several smaller classes (called subvisitors) responsible for visiting instructions of different types. Each instruction has its corresponding abstract method in the subvisitors set that is called by the main **Visit** method once an instruction of the appropriate type is going to be visited.

Using such an abstract implementation allows to implement different types of visitors for the abstract syntax tree. For now, there is only one implementation - the **CilInterpreterInstructionsVisitor** class and its subvisitors. This implementation uses the execution state to implement the **GetNextInstruction** method. To be precise, the instruction pointer of the top method state is used to determine the next instruction to visit.

---

```

1 public abstract class CilInstructionsVisitor
2 {
3     protected abstract InstructionNoneVisitor
4         InstructionNoneVisitor { get; }
5
6     protected abstract InstructionMethodVisitor
7         InstructionMethodVisitor { get; }
8
9     protected abstract InstructionStringVisitor
10        InstructionStringVisitor { get; }
11
12    protected abstract InstructionIVisitor InstructionIVisitor
13        { get; }
14
15    protected abstract InstructionTypeVisitor
16        InstructionTypeVisitor { get; }
17
18    protected abstract InstructionVarVisitor
19        InstructionVarVisitor { get; }
20
21    protected abstract InstructionRVisitor InstructionRVisitor
22        { get; }
23
24    protected abstract InstructionBrVisitor
25        InstructionBrVisitor { get; }
26
27    protected abstract InstructionFieldVisitor
28        InstructionFieldVisitor { get; }
29
30    protected abstract InstructionI8Visitor
31        InstructionI8Visitor { get; }
32
33    protected abstract InstructionSwitchVisitor
34        InstructionSwitchVisitor { get; }
35
36    protected abstract InstructionTokVisitor
37        InstructionTokVisitor { get; }
38
39    public void Visit()
40    {
41        var nextInstruction = GetNextInstruction();
42        while (nextInstruction != null)
43        {
44            VisitInstruction(nextInstruction);
45            nextInstruction = GetNextInstruction();
46        }
47    }
48
49    protected abstract CilInstruction GetNextInstruction();

```

```

38
39 private void VisitInstruction(CilInstruction instruction)
40 {
41     if (instruction is CilInstructionNone instructionNone)
42         InstructionNoneVisitor.VisitInstructionNone(
43             instructionNone);
44     else if (instruction is CilInstructionMethod
45             instructionMethod)
46         InstructionMethodVisitor.VisitInstructionMethod(
47             instructionMethod);
48     else if (instruction is CilInstructionString
49             instructionString)
50         InstructionStringVisitor.VisitInstructionString(
51             instructionString);
52     else if (instruction is CilInstructionI instructionI)
53         InstructionIVisitor.VisitInstructionI(instructionI);
54     else if (instruction is CilInstructionType
55             instructionType)
56         InstructionTypeVisitor.VisitInstructionType(
57             instructionType);
58     else if (instruction is CilInstructionVar instructionVar
59             )
60         InstructionVarVisitor.VisitInstructionVar(
61             instructionVar);
62     else if (instruction is CilInstructionR instructionR)
63         InstructionRVisitor.VisitInstructionR(instructionR);
64     else if (instruction is CilInstructionBr instructionBr)
65         InstructionBrVisitor.VisitInstructionBr(instructionBr)
66         ;
67     else if (instruction is CilInstructionField
68             instructionField)
69         InstructionFieldVisitor.VisitInstructionField(
70             instructionField);
71     else if (instruction is CilInstructionI8 instructionI8)
72         InstructionI8Visitor.VisitInstructionI8(instructionI8)
73         ;
74     else if (instruction is CilInstructionSwitch
75             instructionSwitch)
76         InstructionSwitchVisitor.VisitInstructionSwitch(
77             instructionSwitch);
78     else if (instruction is CilInstructionTok instructionTok
79             )
80         InstructionTokVisitor.VisitInstructionTok(
81             instructionTok);
82     else
83         throw new ArgumentException($"CIL instruction cannot
84             be recognized: '{instruction.ToString()}'");
85 }
86 }

```

---

Code listing 4.2: The `CilInstructionsVisitor` class.

## 4.5 Examples

Having the abstract visitors structure described in the previous section, implementing the interpreter is equivalent to completing the implementation of all of the abstract methods of the subvisitors. The implementation itself is based on the semantics described in 3.

### 4.5.1 `ldc.i4.0`

The `VisitLoadConstI40Instruction` method shown in listing 4.3 corresponds to the `ldc.i4.0` instruction described in 2.5.1 with its semantics presented in formula 3.11. The meaning of the method can be explained as follows:

- Line 3 creates a new stack value of type `int32` with value equal to 0.
- Line 4 pushes the newly created value to the top of the evaluation stack. `ControlState.EvaluationStack` is just a shorthand for accessing the evaluation stack of the top method state.
- Line 6 moves the instruction pointer of the top method state to the next instruction of the same method. The `MoveToNextInstruction` method is just a helper as such an operation is used very often.

---

```
1 protected override void VisitLoadConstI40Instruction(  
    LoadConstI40Instruction instruction)  
2 {  
3     var stackVal = new CilStackValueInt32(0);  
4     ControlState.EvaluationStack.Push(stackVal);  
5  
6     ControlState.MoveToNextInstruction();  
7 }
```

---

Code listing 4.3: The `VisitLoadConstI40Instruction` method.

### 4.5.2 `stloc.0`

The `VisitStoreLocal0Instruction` method is shown in listing 4.4. It is related to the `stloc.0` instruction described in 2.5.2 and its semantics presented in formula 3.22. The implementation can be understood as follows:

---

```
1 protected override void VisitStoreLocal0Instruction(  
    StoreLocal0Instruction instruction)  
2 {  
3     var localType = ControlState.Locals.GetLocalType(null, 0);  
4     ControlState.EvaluationStack.PopValue(_program, localType,  
        out var value);  
5     ControlState.Locals.Store(null, 0, value);  
6  
7     ControlState.MoveToNextInstruction();  
8 }
```

---

Code listing 4.4: The VisitSetLocal0Instruction method.



Figure 2: The process of implementing the interpreter.

## 5 Testing

Writing an interpreter of such a complex programming language as the CIL may be a little complicated. Implementing all the visit instructions at the same time seems to be impossible so a specific process needed to be introduced in order to achieve the desired effect. This particular process consists of 4 steps:

1. Write a test.
2. Cover the implementation of the interpreter so that the test passes.
3. Run all the existing tests.
4. Correct the implementation so that all the tests pass.
5. Repeat steps 1-4.

The process is illustrated in figure 2.

### 5.1 The tests

Potentially, a set of unit tests could be written based on the documentation and the semantics of each instruction. Nevertheless, such a task might be very difficult so another type of tests was used instead. Since there are functioning



implementations of the CLI, working of the interpreter can be compared to working of one of such implementations. To be exact, the **.NET Framework 4.7.2** was selected to write the tests.

Taking into account the abilities of the interpreter, only console programs writing to the standard input, reading from the standard output and using the standard error stream are considered proper tests. The vast majority of the tests are written in **C#**. Using the **ildasm** tool provided together with the **.NET Framework**, the tests can be decompiled to their CIL source code which can be then used as the input file for the interpreter.

Once a test is prepared, it can be executed using the interpreter and the result of the execution can be compared to the result of the original test program. The result of such a comparison is assumed to be correct when the output produced by the interpreter is the same as the output produced by the original program for the same input. Even though a single test can be potentially executed using an infinite set of different inputs, only a small part is used so that the process is not too time-consuming. In order to make the process as reliable as possible, a set of random inputs is prepared every time the test is executed.

## 5.2 The environment

Since repeating the process described above manually might be arduous for bigger numbers of tests, a dedicated system was written separately to automate the procedure. The system is hereinafter called *the tools* or *the environment*. It comprises 2 projects:

- an **ASP.NET Core WebAPI** application (the back-end),
- a **React.js** application (the front-end).

The tools provide several functionalities described below.

### 5.2.1 Finding tests

The environment watches the folder provided in the configuration and displays a list of the new tests that have been found. Each of the found tests can be then saved in the database and used to perform various operations. An example of the functionality is shown in figure 3.

### 5.2.2 Managing categories

The tools allow the user to define categories and subcategories that can be listed, ordered and filtered. The categories and the subcategories can be then assigned to the tests. The *Categories* page is shown in figure 4.

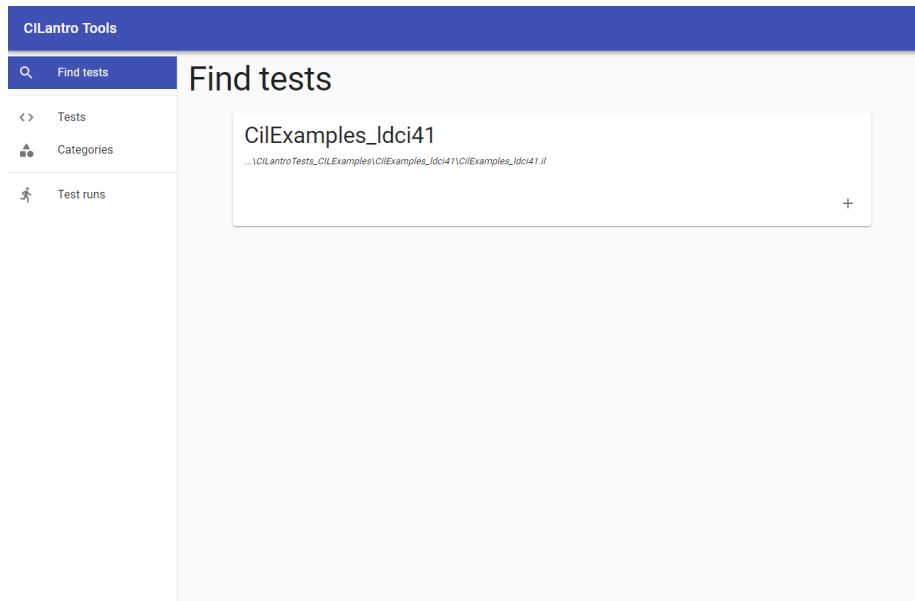


Figure 3: The environment - the *Find tests* functionality.

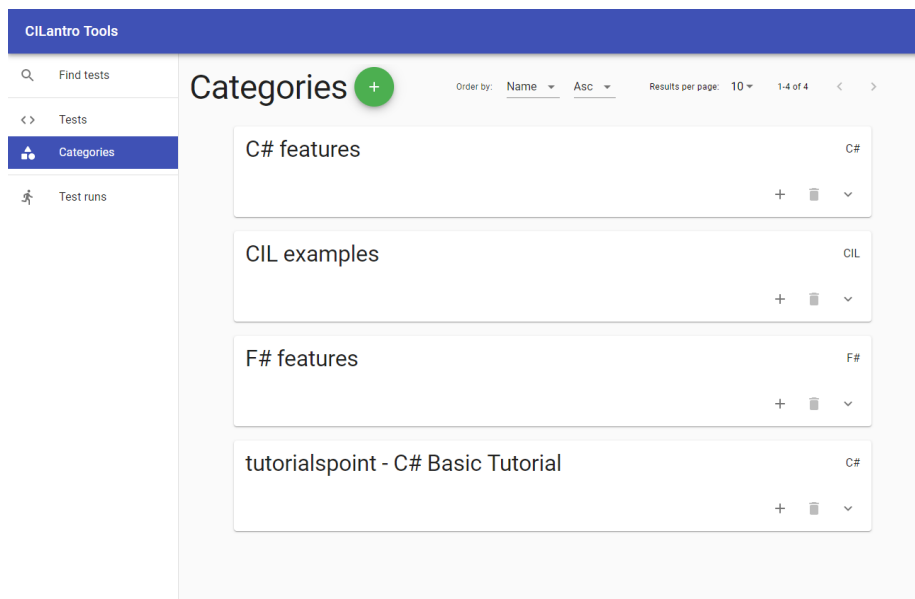


Figure 4: The environment - the *Categories* page.

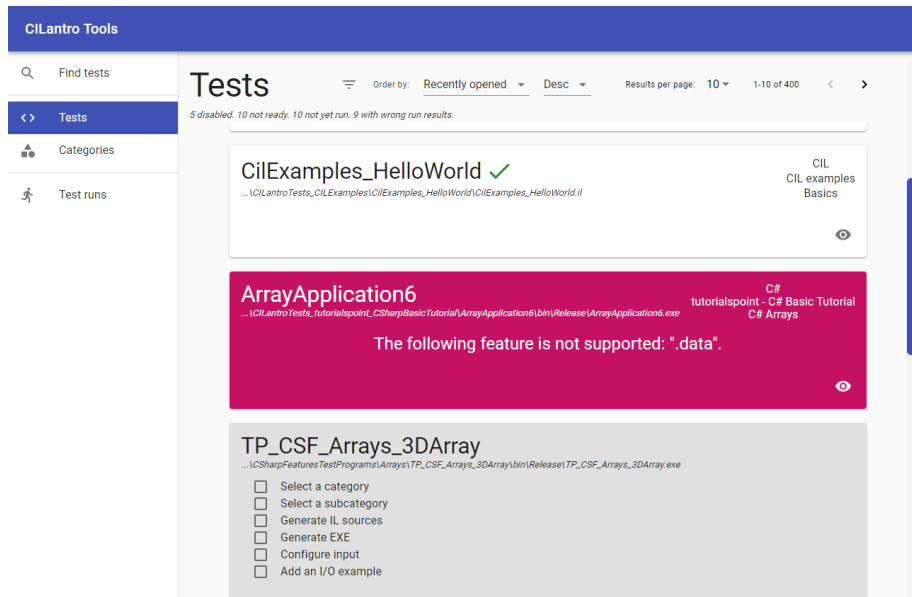


Figure 5: The environment - the *Tests* page.

### 5.2.3 Managing tests

The tests can be also listed, ordered and filtered. Each test is presented as a single card of a specific colour which determines the state of the test. If a card is grey, the related test is not configured and needs some additional attention - the card contains a list of tasks to be done in order to configure the test properly. If a card is purple, the corresponding test has been marked as unsupported. If a card is white, the test is ready to be used and run. Additionally, if a test has been already run, the card shows an icon that represents the result of the last run of the test. The *Tests* page is shown in figure 5.

### 5.2.4 Managing a single test

### TO DO: OPIS I SCREENSHOT ###

### 5.2.5 Managing test runs

The test runs can be listed, ordered and filter. A single run is represented by a card of a specific colour. If a card is orange, the corresponding run is being executed at the moment. If a card is red, the run has already ended but its result is wrong. If a card is green, the related run has ended and the result is correct. Each run can be also restarted and the user can display its details.

The page contains two buttons that start a single test run including all the well-configured tests. There are two types of runs: quick and full. A quick run

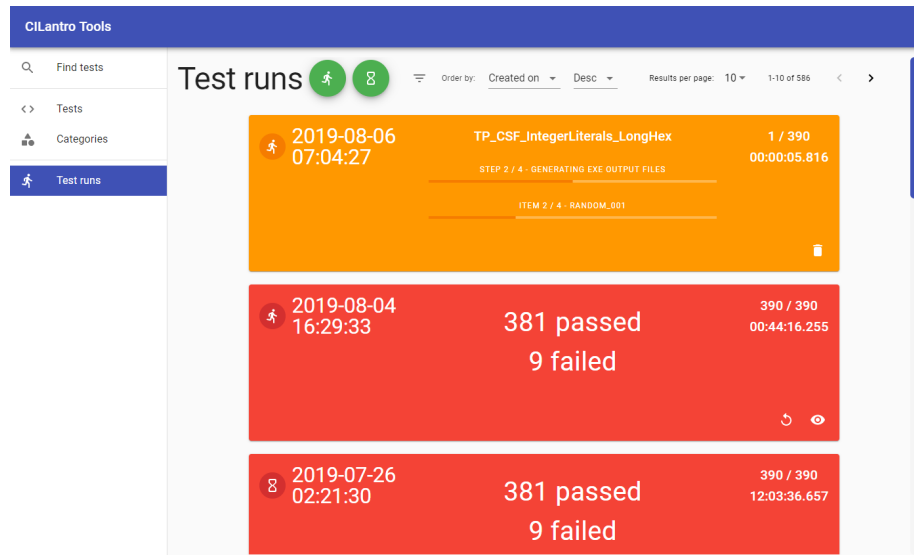


Figure 6: The environment - the *Test runs* page.

executes each test using only 3 random inputs while a full run uses 100 random inputs for each test. Regardless of the type, each run also executes the tests using the inputs defined in the tools.

The *Test runs* page is shown in figure 6.

### 5.2.6 Managing a single test run

### TO DO: OPIS I SCREENSHOT ###

## 6 Limitations

### TO DO: OPIS OGRANICZEN, KTÓRE TYCZĄ SIĘ SAMEGO INTER-  
PRETERA JAK I ŚRODOWISKA ###

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