

# DS7837/DS8837 Hex Unified Bus Receiver

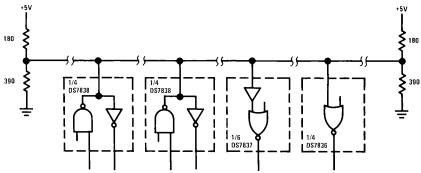
## **General Description**

The DS7837/DS8837 are high speed receivers designed for use in bus organized data transmission systems interconnected by terminated  $120\Omega$  impedance lines. The external termination is intended to be  $180\Omega$  resistor from the bus to ground. The receiver design employs a built-in input hysteresis providing substantial noise immunity. Low input current allows up to 27 driver/receiver pairs to utilize a common bus. Disable inputs provide time discrimination. Disable inputs and receiver outputs are TTL compatible. Performance is optimized for systems with bus rise and fall times  $\leq 1.0~\mu \text{s/V}.$ 

#### **Features**

- Low receiver input current for normal V<sub>CC</sub> or V<sub>CC</sub> = 0V (15 µA typ)
- Six separate receivers per package
- Built-in receiver input hysteresis (1V typ)
- High receiver noise immunity (2V typ)
- Temperature insensitive receiver input thresholds track bus logic levels
- TTL compatible disable and output
- Molded or cavity dual-in-line or flat package
- High speed

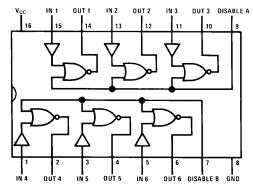
# **Typical Application**



TL/F/5811-1

## **Connection Diagram**

#### Dual-In-Line Package



TL/F/5811-2

Top View
Order Number DS7837J, DS8837M or DS8837N
See NS Package Number J16A, M16A or N16A

# **Absolute Maximum Ratings** (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V
Input Voltage 5.5V
Operating Temperature Range

Storage Temperature Range  $$-65^{\circ}\textrm{C}$ to $+150^{\circ}\textrm{C}$$ 

Maximum Power Dissipation\* at 25°C

Cavity Package 1433 mW
Molded DIP Package 1362 mW
SO Package 1002 mW
Lead Temperature (Soldering, 4 seconds) 260°C
\*Decate cavity package 9.6 mW/°C above 25°C decate molded DIP package

\*Derate cavity package 9.6 mW/°C above 25°C; derate molded DIP package 10.9 mW/°C above 25°C; derate SO package 8.01 mW/°C above 25°C.

Operating Conditions						
	Min	Max	Units			
Supply Voltage, (V <sub>CC</sub> )						
DS7837	4.5	5.5	V			
DS8837	4.75	5.25	V			
Temperature (T <sub>A</sub> )						
DS7837	-55	+ 125	°C			
DS8837	0	+70	°C			

## **Electrical Characteristics**

The following apply for  $V_{MIN} \leq V_{CC} \leq V_{MAX}$ ,  $T_{MIN} \leq T_A \leq T_{MAX}$ , unless otherwise specified (Notes 2 and 3)

Symbol	Parameter	Conditions		Min	Тур	Max	Units
V <sub>TH</sub>	High Level Receiver Threshold	V <sub>CC</sub> = Max	DS7837	1.65	2.25	2.65	٧
			DS8837	1.80	2.25	2.50	٧
V <sub>TL</sub>	Low Level Receiver Threshold	V <sub>CC</sub> = Min	DS7837	0.97	1.30	1.63	٧
			DS8837	1.05	1.30	1.55	V
I <sub>IH</sub>	Maximum Receiver Input Current	$V_{IN} = 4V$	$V_{CC} = V_{MAX}$		15.0	50.0	μΑ
			$V_{CC} = 0V$		1.0	50.0	μΑ
I <sub>IL</sub>	Logical "0" Receiver Input Current	$V_{IN} = 0.4V$ , $V_{CC} = V_{MAX}$			1.0	50.0	μΑ
$V_{IH}$	Logical "1" Input Voltage		Disable	2.0			V
V <sub>IL</sub>	Logical "0" Input Voltage		Disable			0.8	V
I <sub>IH</sub>	Logical "1" Input Current	Disable Input	$V_{\text{IND}} = 2.4V$			80.0	μΑ
			$V_{IND} = 5.5V$			2.0	mA
I <sub>IL</sub>	Logical "0" Input Current	$V_{\mbox{\footnotesize{IN}}} = 4V, V_{\mbox{\footnotesize{IND}}} = 0.4V,$ Disable Input				-3.2	mA
V <sub>OH</sub>	Logical "1" Output Voltage	$\begin{split} V_{IN} &= 0.5 \text{V, } V_{IND} = 0.8 \text{V,} \\ I_{OH} &= -400 \; \mu\text{A} \end{split}$		2.4			٧
V <sub>OL</sub>	Logical "0" Output Voltage	$V_{\text{IN}}=4V$ , $V_{\text{IND}}=0.8V$ , $I_{\text{OH}}=16$ mA			0.25	0.4	٧
los	Output Short Circuit Current	$V_{\text{IN}} = 0.5 \text{V}, V_{\text{IND}} = 0 \text{V}, V_{\text{OS}} = 0 \text{V},$ $V_{\text{CC}} = V_{\text{MAX}}, \text{(Note 4)}$		-18.0		-55.0	mA
Icc	Power Supply Current	V <sub>IN</sub> = 4V, V <sub>IND</sub> = 0V, (Per Package)			45.0	60.0	mA
V <sub>CL</sub>	Input Clamp Diode	$V_{IN} = -12 \text{ mA}, V_{IND} = T_A = 25^{\circ}\text{C}$	= −12 mA,		-1.0	-1.5	V

# **Switching Characteristics** $T_A = 25^{\circ}C$ , nominal power supplies unless otherwise noted

Symbol	Parameter	Conditions			Тур	Max	Units
t <sub>pd</sub>	Propagation Delays	$V_{IND} = 0V$ ,	Input to Logical "1" Output, (Note 5)		20	30	ns
		Receiver	Input to Logical "0" Output, (Note 6)		18	30	ns
		Input = 0V,	Input to Logical "1" Output		9	15	ns
	Disable, (Note 7	Disable, (Note 7)	Input to Logical "0" Output		4	10	ns

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. Except for "Operating Temperature Range" they are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

Note 2: Unless otherwise specified min/max limits apply across the  $-55^{\circ}$ C to  $+125^{\circ}$ C temperature range for the DS7837 and across the  $0^{\circ}$ C to  $+70^{\circ}$ C range for the DS8837. All typical values are for  $T_A = 25^{\circ}$ C and  $V_{CC} = 5V$ .

Note 3: All currents into device pins shown as positive, out of device pins as negative, all voltages referenced to ground unless otherwise noted. All values shown as max or min on absolute value basis.

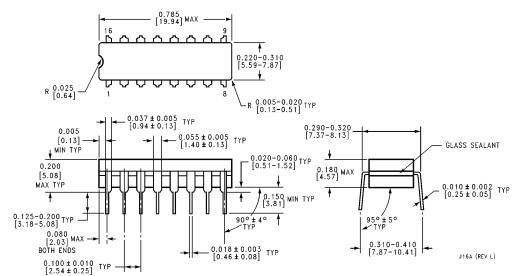
Note 4: Only one output at a time should be shorted.

Note 5: Fan-out of 10 load,  $C_{LOAD} = 15$  pF total. Measured from  $V_{IN} = 1.3V$  to  $V_{OUT} = 1.5V$ ,  $V_{IN} = 0V$  to 3V pulse.

Note 6: Fan-out of 10 load,  $C_{LOAD}=15$  pF total. Measured from  $V_{IN}=2.3V$  to  $V_{OUT}=1.5V$ ,  $V_{IN}=0V$  to 3V pulse.

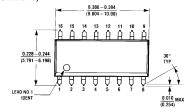
Note 7: Fan-out of 10 load,  $C_{LOAD} = 15 \ pF$  total. Measured from  $V_{IN} = 1.5 V$  to  $V_{OUT} = 1.5 V$ ,  $V_{IN} = 0 V$  to 3V pulse.

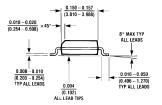
## Physical Dimensions inches (millimeters)

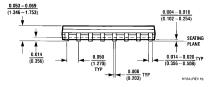


Ceramic Dual-In-Line Package (J) Order Number DS7837J NS Package Number J16A

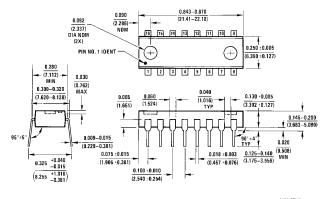
## Physical Dimensions inches (millimeters) (Continued)







#### SO Package (M) Order Number DS8837M NS Package Number M16A



Molded Dual-In-Line Package (N) **Order Number DS8837N** NS Package Number N16A

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