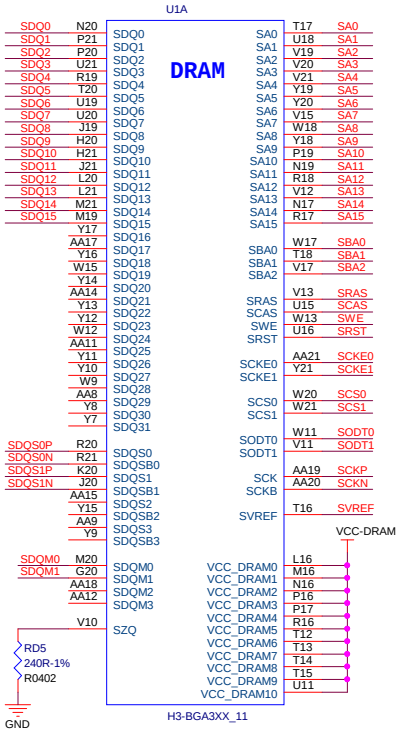
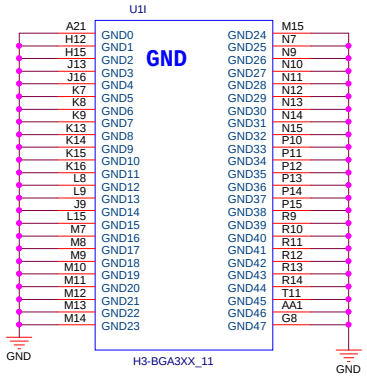
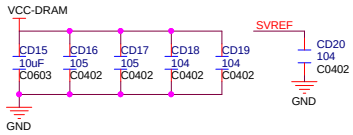
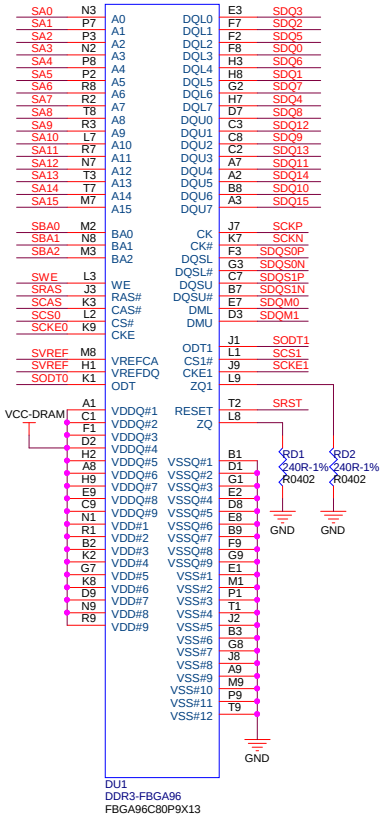
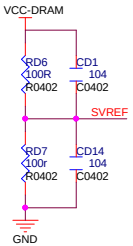
DDR3 16x2

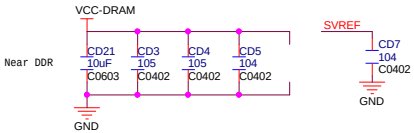


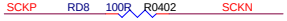


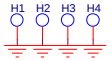












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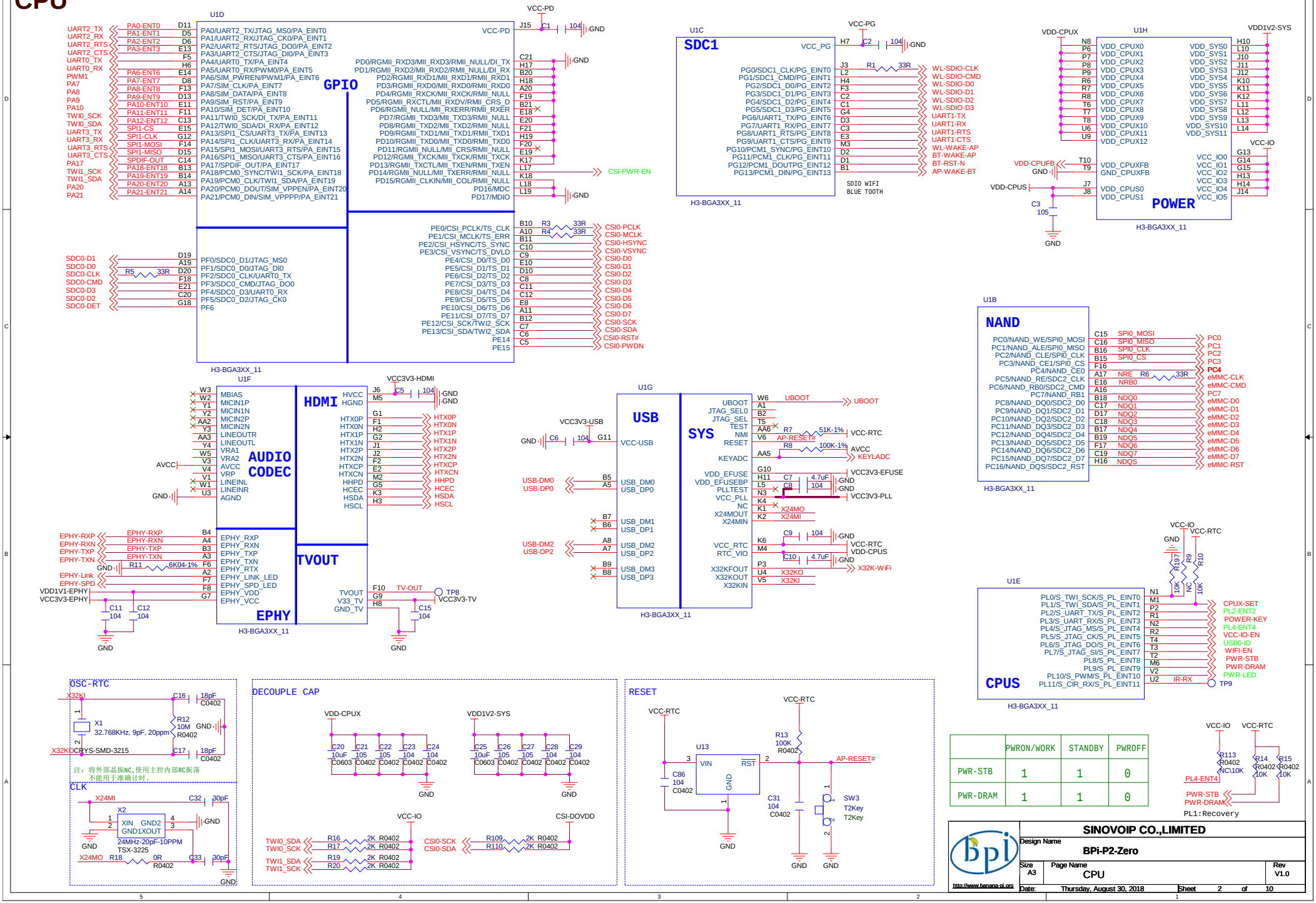
CPU



	PWRON/WORK	STANDBY	PWROFF
PWR-STB	1	1	0
PWR-DRAM	1	1	0

PL1:Recovery

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POWER

PWR-STB
PWR-DRAM

VCC-IO
VCC-PD
VCC-PG
VCC3V3-EPHY
VCC3V3-HDMI
VCC3V3-EFUSE
VCC3V3-SDC
VCC3V3-NAND
VCC3V3-USB
VCC3V3-AUDIO
VCC3V3-UART
VCC3V3-JTAGX
VCC3V3-FELED
VCC3V3-KEY
VCC3V3-LED

VCC-5V
VCC5V-USB
VCC5V-HDMI
VCC5V-SPDIF

VCC-1V2
VDD1V2-SYS
VDD1V1-EPHY

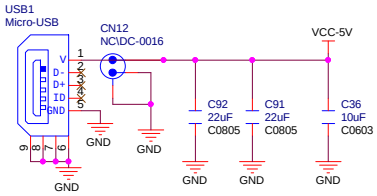
VDD-CPUX

VCC-DRAM

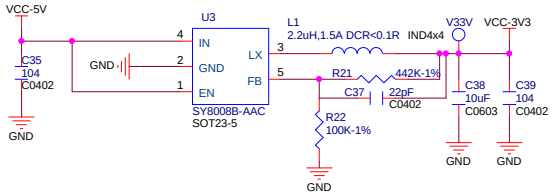
VCC-RTC
VCC3V3-IR
VCC3V3-KEYPWR

AVCC
VCC3V3-PLL
VCC3V3-TV

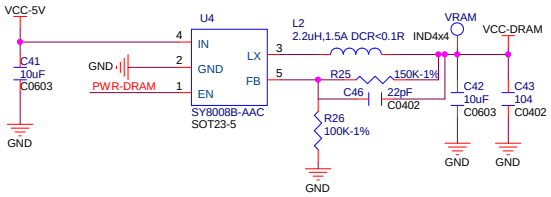
VCC-5V



VCC3V3

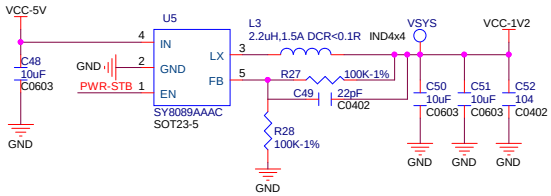


DRAM

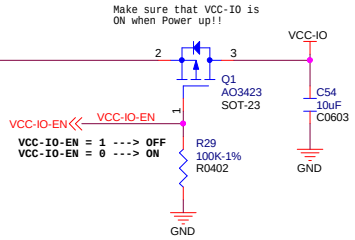


RTC

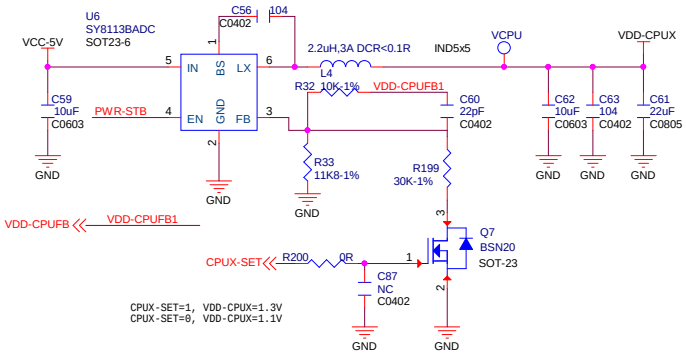
SYSTEM



VCC-IO



CPUX



AVCC

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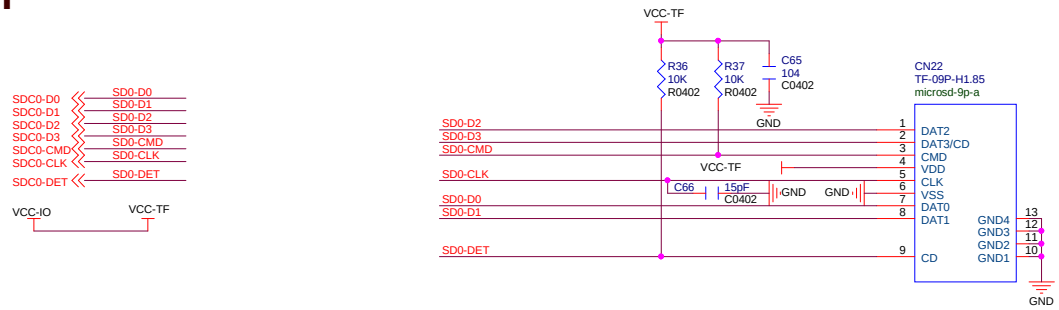
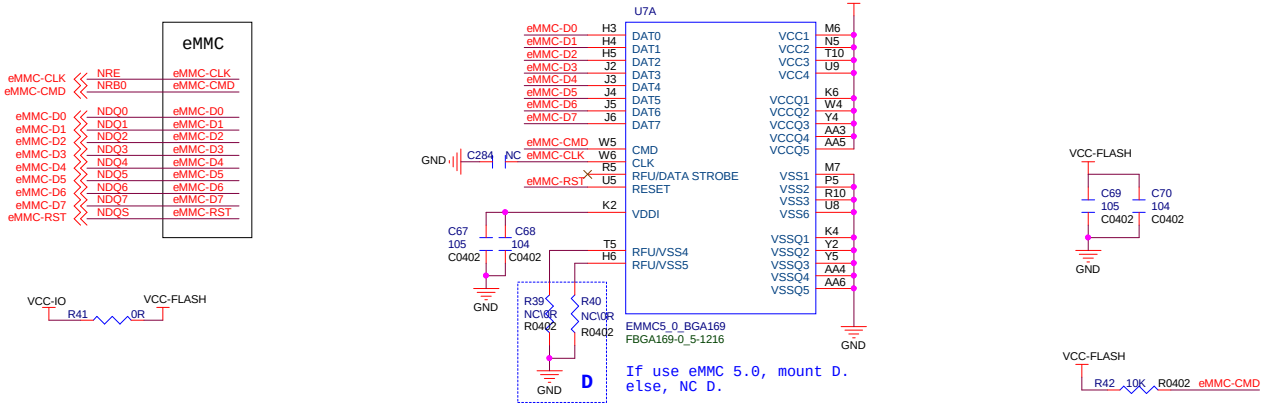
TF



eMMC



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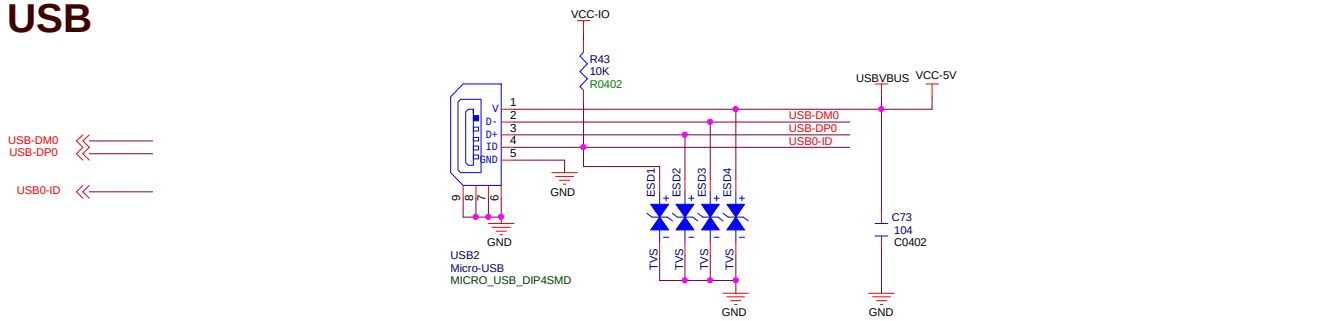

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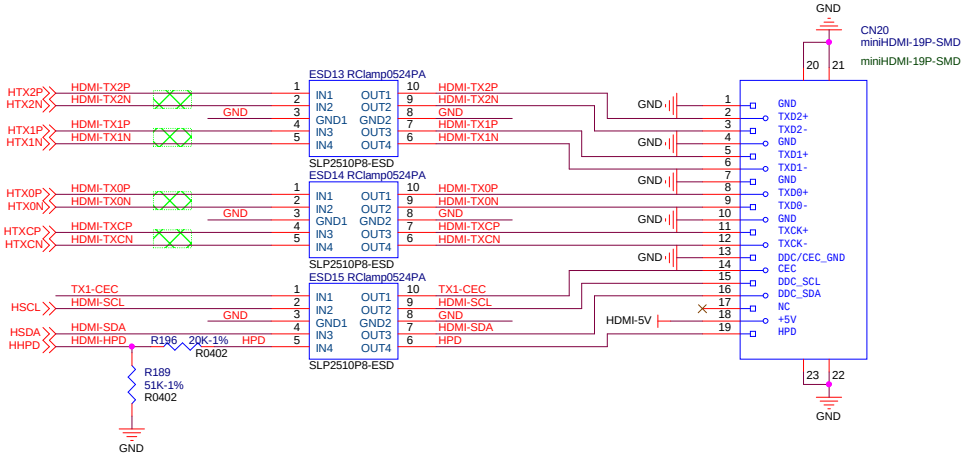
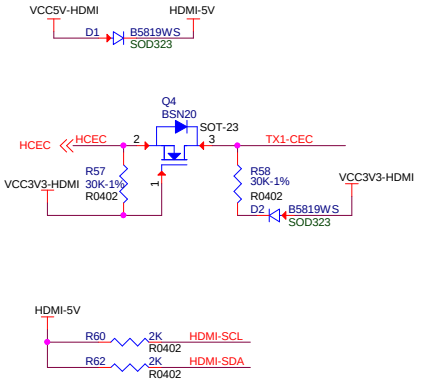
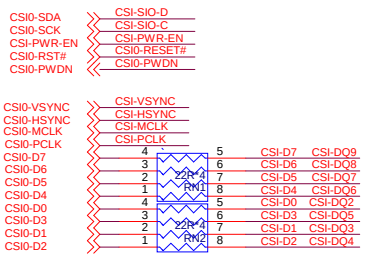
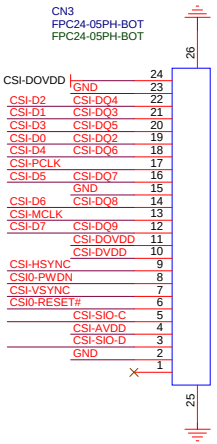
HDMI-CSI

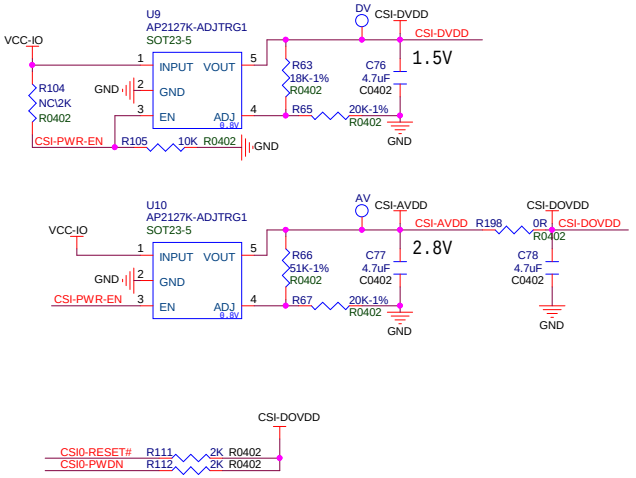




CSI







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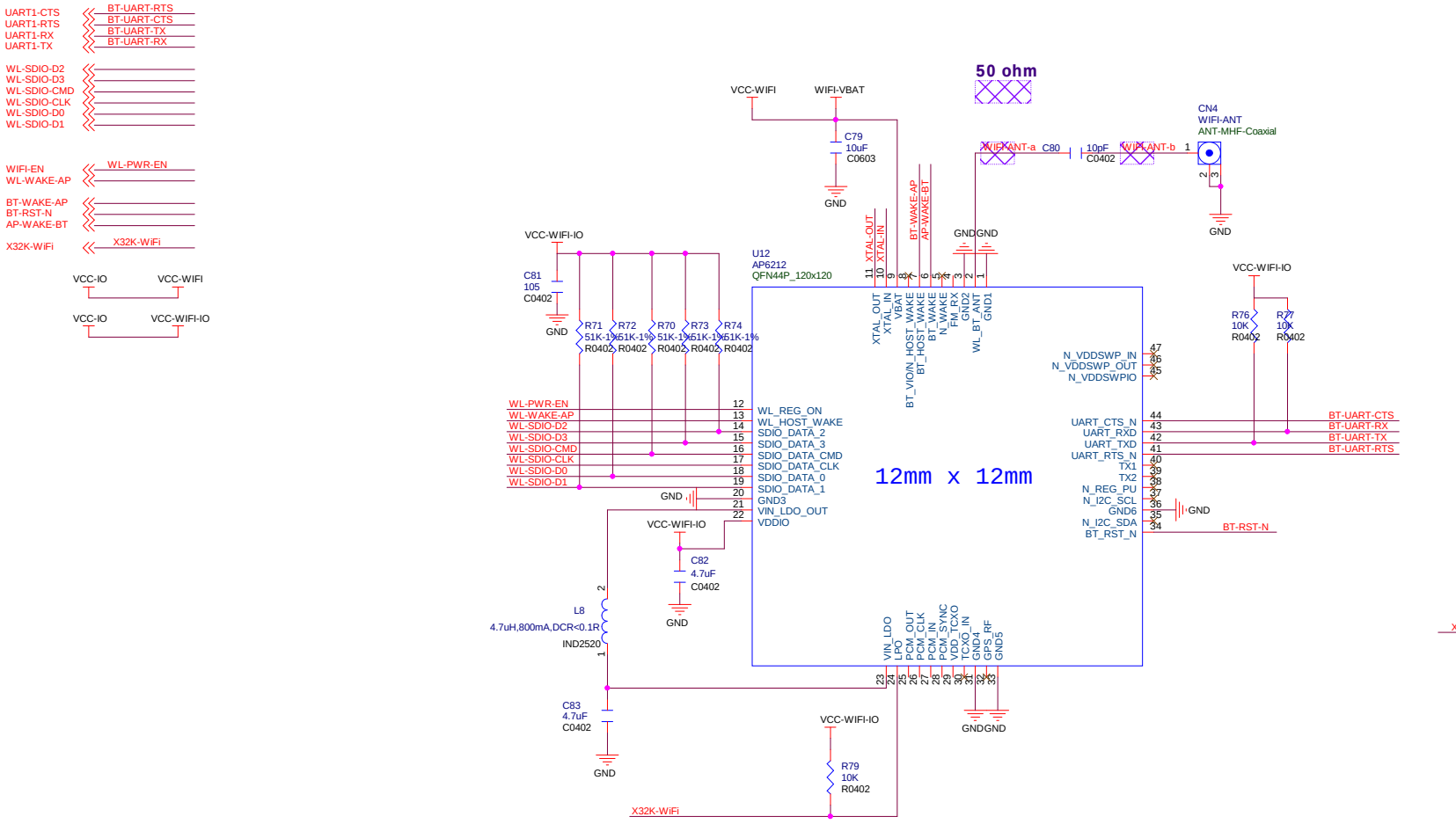
SDIO WIFI+BT



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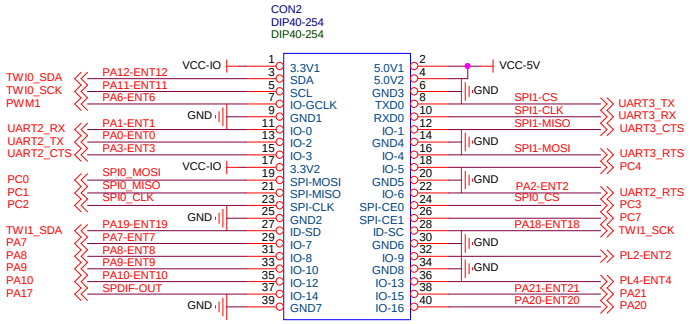
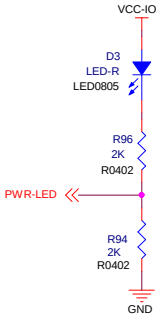
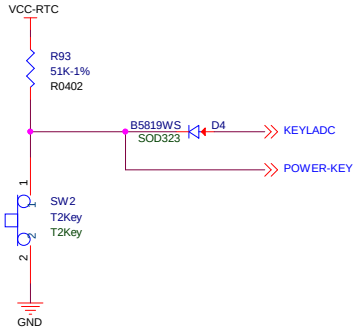
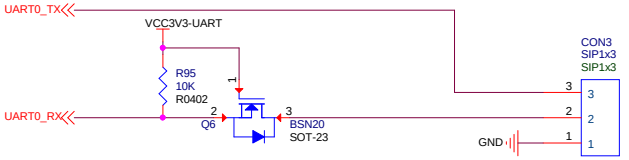
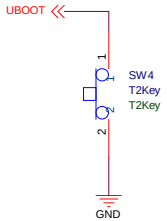
EXT GPIO-IR-KEY-LED











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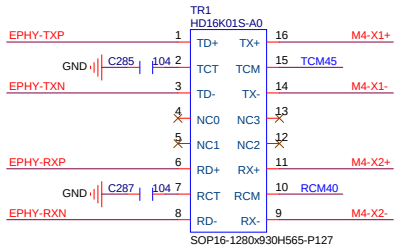
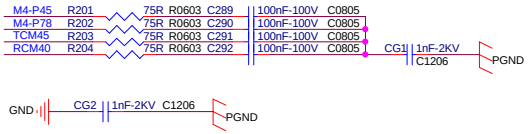
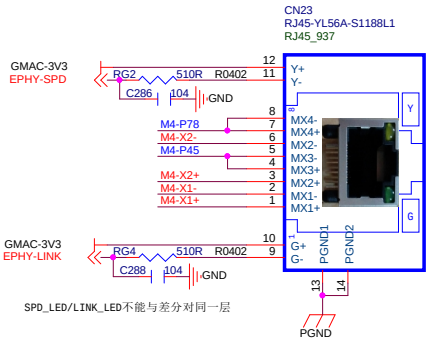
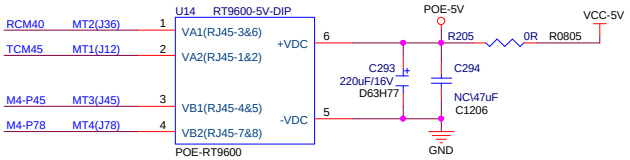
FE POE

EPHY-RXN
EPHY-RXP
EPHY-TXN
EPHY-TXP
EPHY-SPD
EPHY-Link









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REVISION HISTORY

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Revision	Description	Date	Drawn	Checked
Ver 1.0	Release	2018-07-11		

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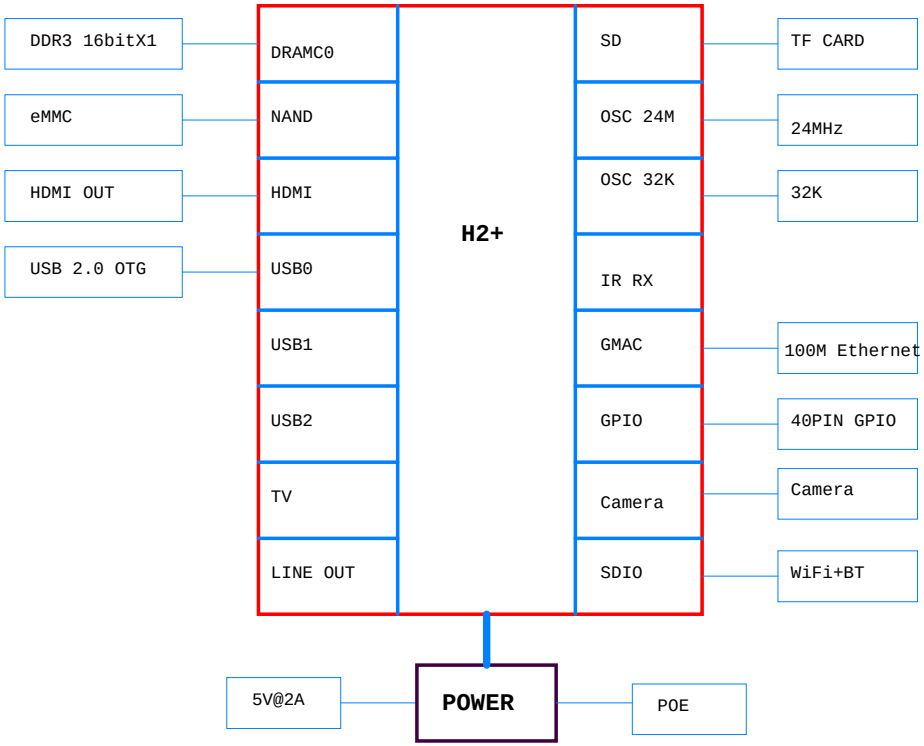
BLOCK



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