## Leandro Lisboa Penz

#### Contact

About me

#### Experience

Software engineer

architect

Leader of test automation tea

architect

Embedded softwa

#### Education

MRA

Organizational Leadership and

Master of Science

Computer Science

Electrical Engineering

Other course

Languages

Misc

# Leandro Lisboa Penz

lpenz@lpenz.org

## Leandro Lisboa Penz

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2 About me

3 Experience

Software engineer

Embedded software architect

Leader of test automation team; embedded software architect

Leader of embedded software team

Embedded software developer

4 Education

MBA - Organizational Leadership and Management Master of Science, Computer Science Graduation, Electrical Engineering

- **5** Other courses
- 6 Languages
- Misc

Contact

Experience

Software engineer Embedded softwar architect Leader of test automation team;

Leader of embedded software team Embedded software developer

Education

MBA -

Leadership and Management Master of Science, Computer Science Graduation, Electrical Engineeri

Languages

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lpenz@lpenz.org

# Contact

About me

Experienc

Embedded software architect Leader of test automation team; embedded software architect Leader of embedder software team

Fmail.

developer

Education

Leadership and Management Master of Science, Computer Science Graduation.

Other course

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Homepage: http://www.lpenz.org
LinkedIn: https://linkedin.com/in/lpenz

Lattes CV: http://lattes.cnpq.br/0824061646899870
GitHub: https://github.com/lpenz

Twitter: Olpenz

Stack Overflow: https://stackoverflow.com/cv/lpenz

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About me

Software engine

automation team; embedded software architect Leader of embedder software team

Education

MBA -Organizational Leadership and Management Master of Science, Computer Science Graduation,

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I'm currently working as an embedded software architect of DmOS — DATACOM's universal embedded OS. I focus on modularity, continuous integration, incremental qualification and DevOps. I work very closely with the DevOps team, bringing solutions and new technologies, and also helping in general.

I was one of the architects that worked on the initial definitions of the architecture, processes and tools used nowadays in the company. The architecture group itself was something new back then.

Before the creation of the architecture group, I led small (8) development teams. At first, I led teams of embedded software development for about 5 years, as I started as a developer myself. I then led the test infrastructure team for about a year

About me

Software engineer
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architect
Leader of test
automation team;
embedded software
architect
Leader of embeddet
software team

# Education

MBA -Leadership and Management Master of Science, Computer Science Graduation, Electrical Engineer

Languages

while also acting as a software architect for the Ethernet switch product line.

I hold a master degree in computer science from UFRGS. My research line was on computer networks, distributed systems, fault tolerance and security. My dissertation was about distributed firewall rule coherence. I implemented the prototype checker in Haskell.

I hold an MBA on management and leadership from UNISINOS. I graduated in electrical engineering from UFRGS.

I value technical excellence, out-of-the-box thinking, generalism, strategic planning and long-term maintainability. I'm always looking for vulnerabilities and enhancement opportunities in my knowledge and tool set.

## About me

Experienc

Embedded softwar architect Leader of test automation team; embedded softward architect

software team Embedded softwar developer

### Education

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Langua

Out of the professional context, I have studied and obtained a license to fly sailplanes. I can definitely say that I learned a lot from doing that, from the differences with my area (extreme risk aversion) as well as from the similarities (briefing/debriefing  $\sim$  sprint planning/review).

# Software engineer I

Contact

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Software engineer

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automation to embedded sof

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Educatio

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Arista Networks
Software engineer

2016 - present

About i

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# Education

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Languages

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# DATACOM

# **Embedded software architect**

2014 - 2016

Software architect of DATACOM's embedded universal operating system: DmOS.

- Research and high-level definition the system architecture.
- Design of the overall development process.
- A lot of code review.
- · Coding of some core modules.

Software architect assigned to DevOps.

- Initial tooling and structuring of the DevOps infrastructure.
- Definition of the DevOps process: homologation vs test vs production environments.
- Alignment of the infrastructure with DmOS's requirements.

# Embedded software architect II

Comtact

About me

Experience

Embedded software

architect Leader of test

automation team; embedded softwar architect

Leader of embeddo software team

Embedded softwa developer

Education

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Organizationa Leadership an

Master of Science

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Languages

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• Coding of some of the solutions.

# Leader of test automation team; embedded software architect I

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About r

Experience
Software engineer

Leader of test automation team; embedded software

Leader of embedder software team Embedded software developer

# MRA -

Leadership and Management Master of Science, Computer Science Graduation,

Other course

Language

Misc

# **DATACOM**

Leader of test automation team; embedded software architect

2012 - 2014

Software Team Leader of Ethernet switch test automation team.

- Coordination of the team that was responsible for:
  - test development: in ruby;
  - test excution: 24/7, in sync with product integration;
  - $\bullet$  test infrastructure maintenance:  $\sim$  5 testbeds, most with Ethernet ring topologies.
- Backlog and defect priorization.
- Agile methodologies.
- Performance evaluation, feedback.

# Leader of test automation team; embedded software architect II

#### Contact

About r

#### Experienc

architect

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automation team;
embedded software

Leader of embedded software team Embedded software

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# Software architect of DATACOM's Ethernet switch product line:

- Evaluation and implementation of static analysis tools.
- Development of debugging tools.
- Overall development process enhancements.
- I was also heavily involed with the design and implementation of tools that gathered data and generated development and defect metrics using python and django, full stack.

# Leader of embedded software team I

#### Contac

About m

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Leader of embedded software team Embedded software developer

MBA -

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# **DATACOM**

# Leader of embedded software team

2007 - 2011

Software team leader of DATACOM's NG-SDH Multiplexer product line.

- Coordination of distributed teams ( $\sim$  8, about half local, half remote) of embedded software developers.
- Guidance and reference in the development of solutions.
- Backlog and defect priorization.
- Agile methodologies.
- Performance evaluation, feedback.
- A lot of code review.

About r

Experience
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Leader of test
automation team,
embedded software
architect
Leader of embeddec
software team
Embedded software

# developer Education

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Langua

Misc

# **DATACOM**

# **Embedded software developer**

2002 - 2007

Team member of the first NG-SDH Multiplexer developed in Brazil.

- Software development using the C linguage for embedded Linux systems: network servers, debug tools and drivers (kernel space).
- Unix server administration and maintenance.
- Script development (test automation) and scripting language extension: Lua, Python and TCL (Expect).

# MBA - Organizational Leadership and Management

### Conta

About m

#### Experience

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automation team; embedded software architect Leader of embedde

software team Embedded software developer

# Lucation

Organizational Leadership and Management

Master of Science, Computer Science Graduation, Electrical Engineering

Other courses

Langua

Vale do Rio dos Sinos University (UNISINOS)
MBA - Organizational Leadership and Management

2012 - 2013

# Subjects:

- Business management
- Project management
- Human resources
- Leadership
- Organizational identity and culture

# Master of Science, Computer Science

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# Federal University of Rio Grande do Sul (UFRGS) Master of Science, Computer Science

2006 - 2008

Orientation: Prof. Dr. Raul Fernando Weber

Research line: fault tolerance, network security and distributed systems.

**Dissertation**: Coherence in distributed packet filters

Formal definition of the concept of anomaly in isolated and distributed network packet filters through the use of graphs and set theory. Development of a prototype anomaly checker in Haskell.

Online: http://www.lume.ufrgs.br/handle/10183/22813

# Graduation, Electrical Engineering

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Education

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Graduation, Electrical Engineering

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# Federal University of Rio Grande do Sul (UFRGS) Graduation, Electrical Engineering

1998 - 2002

The program is ranked among the top in Brazil, according to the evaluation process carried out by the CAPES agency of the Brazilian Ministry of Education.

Other courses

# Pontifícia Universidade Católica - RS Agile methodologies: project management with SCRUM 2009

Vale do Rio dos Sinos University (UNISINOS) Software project management

2006

Study of PMI's PMBOK.

**UFRGS** - Data processing center TCP/IPv4 routing - Internet high availability

2003

Focused on network routing protocols: RIP, OSPF and BGP.

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Languages

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Portuguese: Native

English: Fluent

German: Basic

## 200

Embedded software architect

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- I installed my first Debian GNU/Linux at home in 2003, and never reinstalled it from scratch. I use the "testing" release, and update it constantly.
- I learned Haskell while developing the prototype of my MSc dissertation in 2006. Since then, I have used it on side projects and on programming contests when I have the chance.
- Even though I did not take computer science at graduation, I was 6th on the entry exam of the MSc at my University (UFRGS).
- I once got all questions right in a logical reasoning test.
   The psychologist that evaluated it said that she had never seen that before.

About m

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 This CV is maintained at http://github.com/lpenz/cv with configuration management, automated tests and continuous deployment to http://cv.lpenz.org in several output formats.