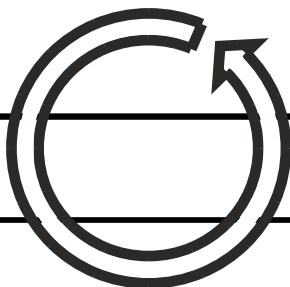
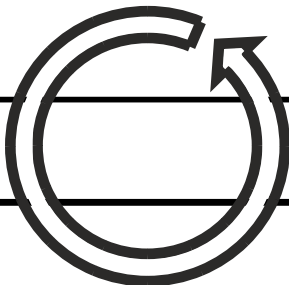


Electronic System Level



refine model

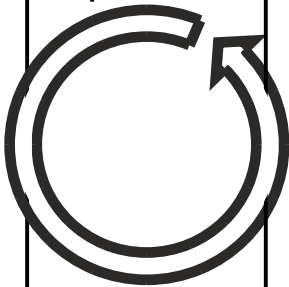
SystemC-PPA



generate properties

RTL

implement



refine

Properties