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Designator
ARXch_1_pg1.SchDoc

Designator
ARXch_1_pg2.SchDoc

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ARXch_1_pg3.SchDoc

Designator
ARXch_2_pg1.SchDoc

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ARXch_2_pg2.SchDoc

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ARXch_2_pg3.SchDoc

Designator
ARXch_3_pg1.SchDoc

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ARXch_3_pg2.SchDoc

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ARXch_3_pg3.SchDoc

Designator
ARXch_4_pg1.SchDoc

Designator
ARXch_4_pg2.SchDoc

Designator
ARXch_4_pg3.SchDoc

Designator
ARXch_5_pg1.SchDoc

Designator
ARXch_5_pg2.SchDoc

Designator
ARXch_5_pg3.SchDoc

Designator
ARXch_6_pg1.SchDoc

Designator
ARXch_6_pg2.SchDoc

Designator
ARXch_6_pg3.SchDoc

Designator
ARXch_7_pg1.SchDoc

Designator
ARXch_7_pg2.SchDoc

Designator
ARXch_7_pg3.SchDoc

Designator
ARXch_8_pg1.SchDoc

Designator
ARXch_8_pg2.SchDoc

Designator
ARXch_8_pg3.SchDoc

Designator
ARXch_9_pg1.SchDoc

Designator
ARXch_9_pg2.SchDoc

Designator
ARXch_9_pg3.SchDoc

Designator
ARXch_10_pg1.SchDoc

Designator
ARXch_10_pg2.SchDoc

Designator
ARXch_10_pg3.SchDoc

Designator
ARXch_11_pg1.SchDoc

Designator
ARXch_11_pg2.SchDoc

Designator
ARXch_11_pg3.SchDoc

Designator
ARXch_12_pg1.SchDoc

Designator
ARXch_12_pg2.SchDoc

Designator
ARXch_12_pg3.SchDoc

Designator
ARXch_13_pg1.SchDoc

Designator
ARXch_13_pg2.SchDoc

Designator
ARXch_13_pg3.SchDoc

Designator
ARXch_14_pg1.SchDoc

Designator
ARXch_14_pg2.SchDoc

Designator
ARXch_14_pg3.SchDoc

Designator
ARXch_15_pg1.SchDoc

Designator
ARXch_15_pg2.SchDoc

Designator
ARXch_15_pg3.SchDoc

Designator
ARXch_16_pg1.SchDoc

Designator
ARXch_16_pg2.SchDoc

Designator
ARXch_16_pg3.SchDoc

Designator
ARK_FCell_SPI.SchDoc

Designator
ARK_J0.SchDoc

Designator
ARK_PWR.SchDoc

Title

Size

B

Number

Revision

Date: 7/31/2015

Sheet of

File: E:\Analog Receiver Board Files\16chARK_Master.SchDoc

Drawn By:

1

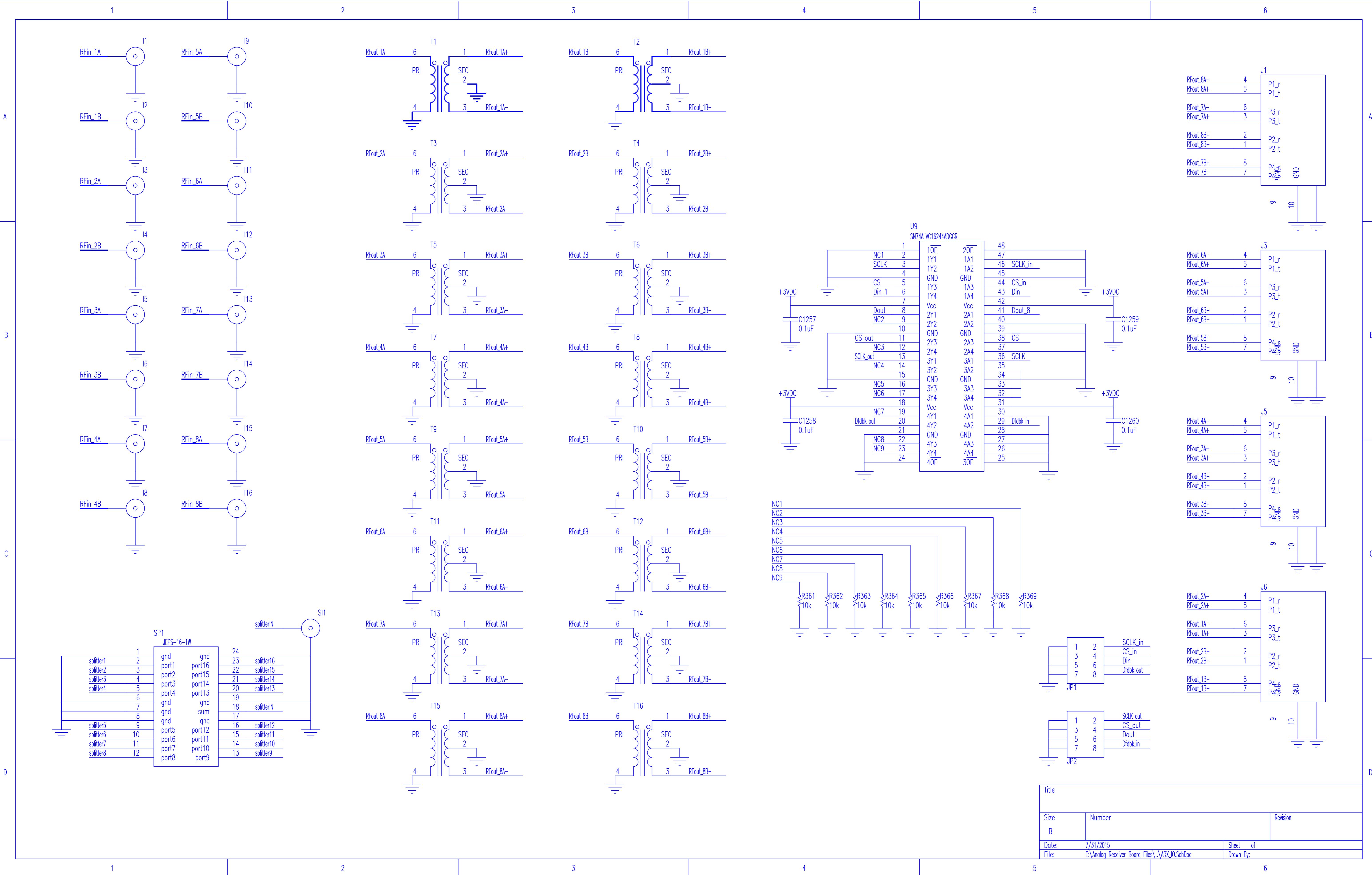
2

3

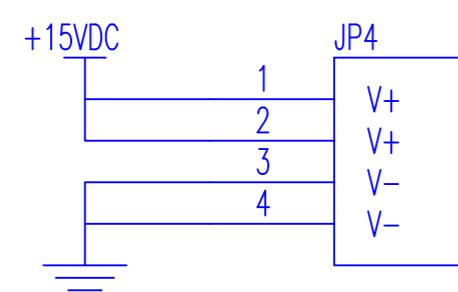
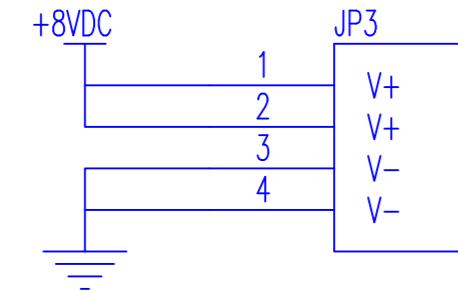
4

5

6



A



+8VDC
1
2
R378
DS1

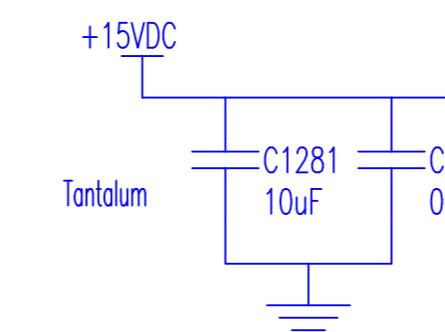
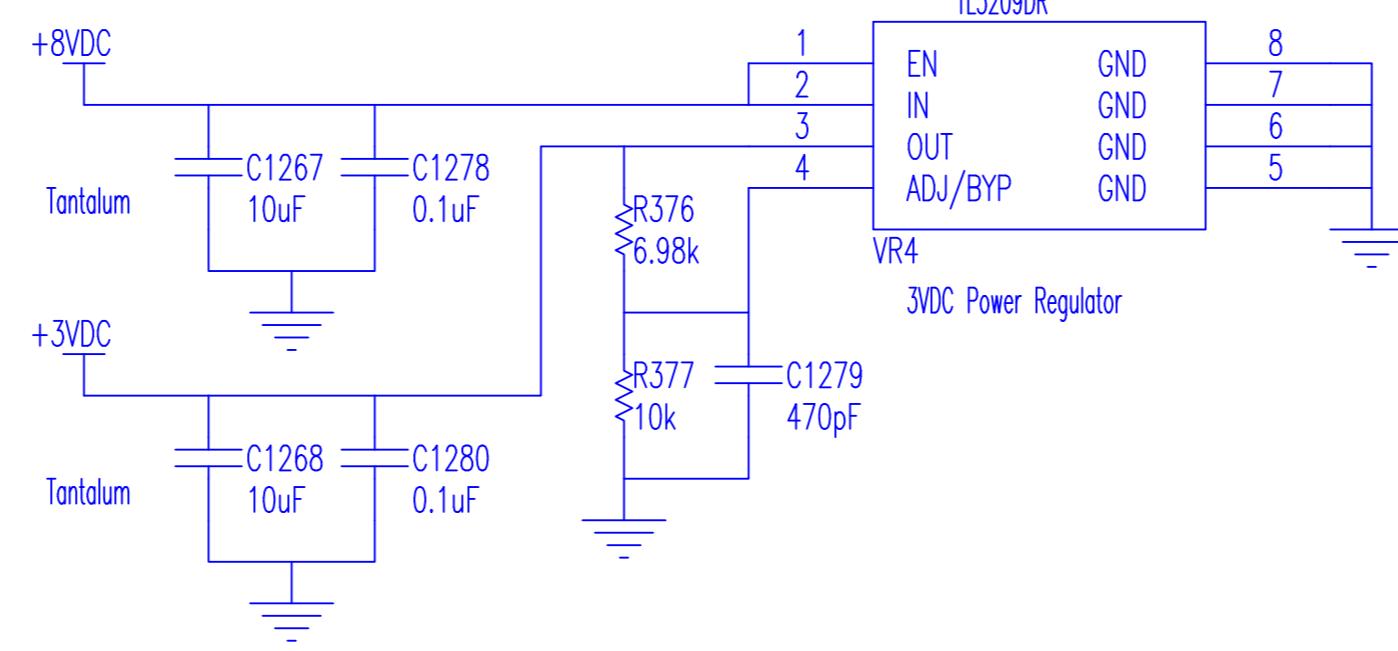
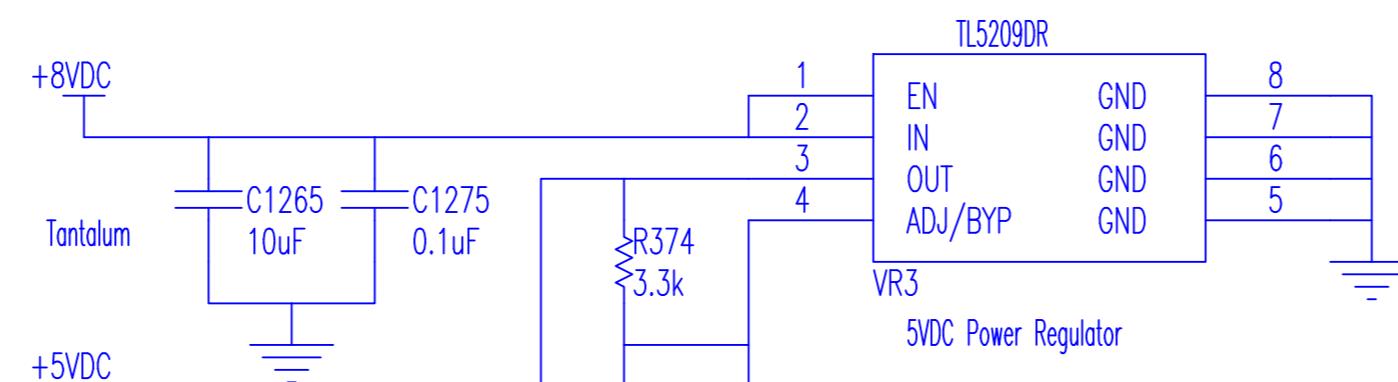
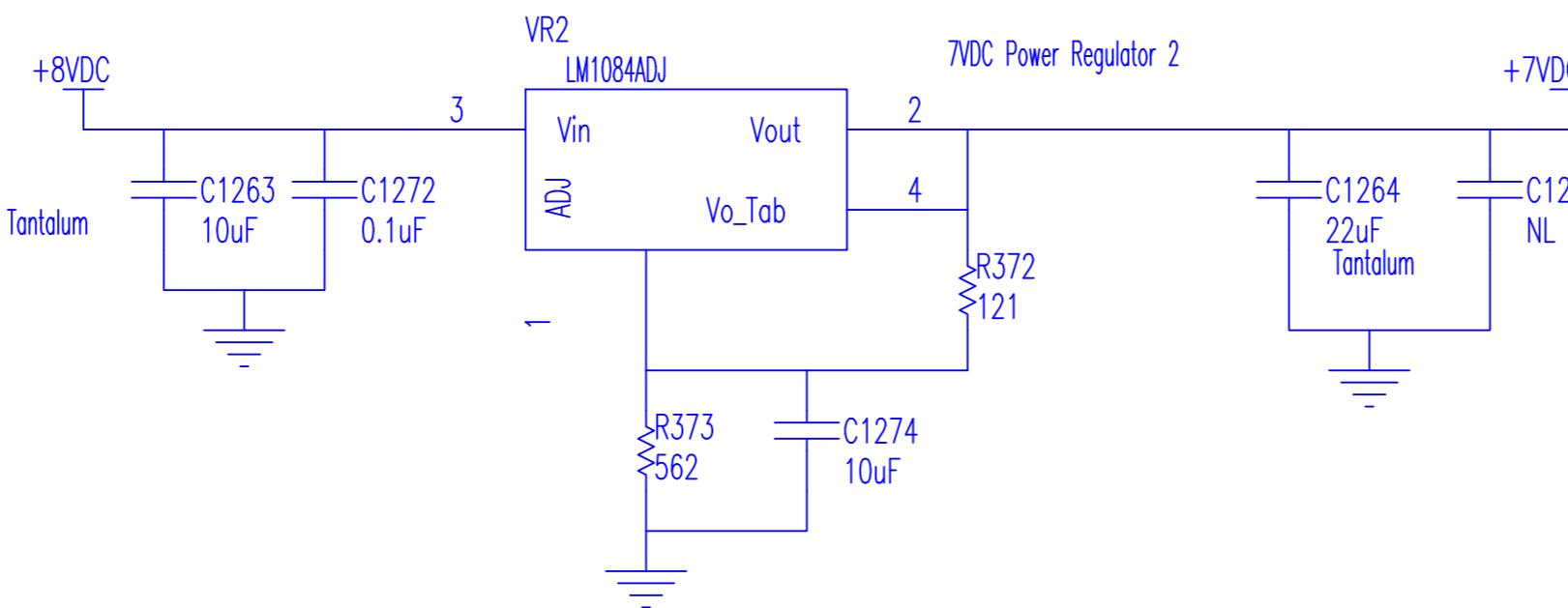
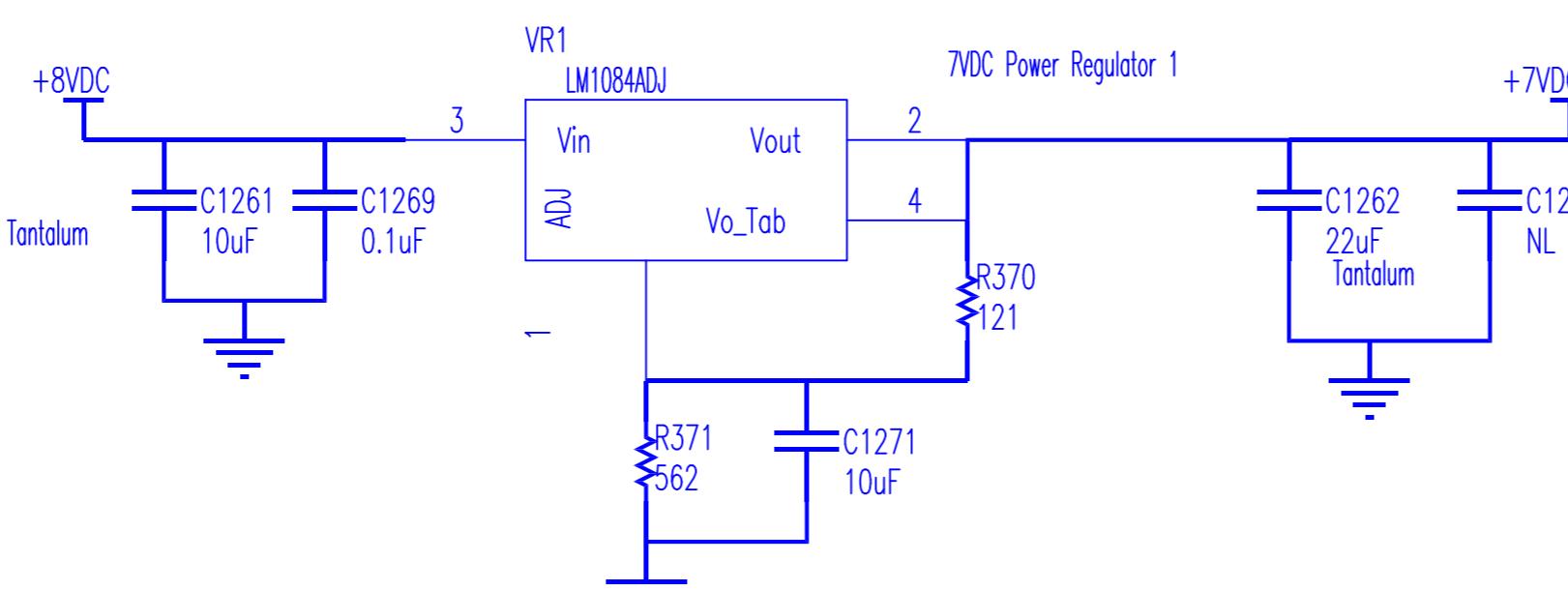
+15VDC
1
2
R379
DS2

+7VDC_1
1
2
R380
DS3

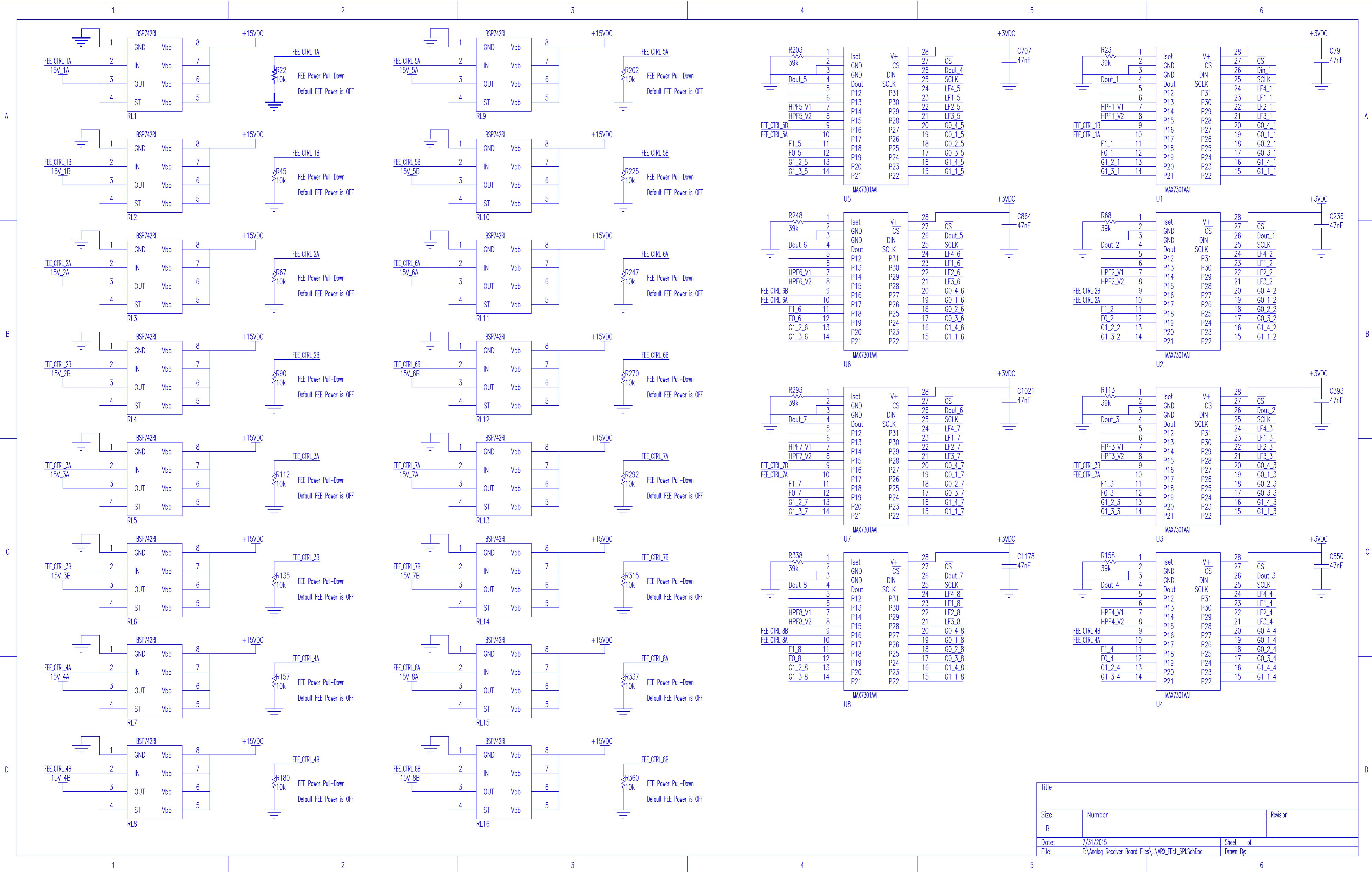
+7VDC_2
1
2
R381
DS4

+5VDC
1
2
R382
DS5

+3VDC
1
2
R383
DS6

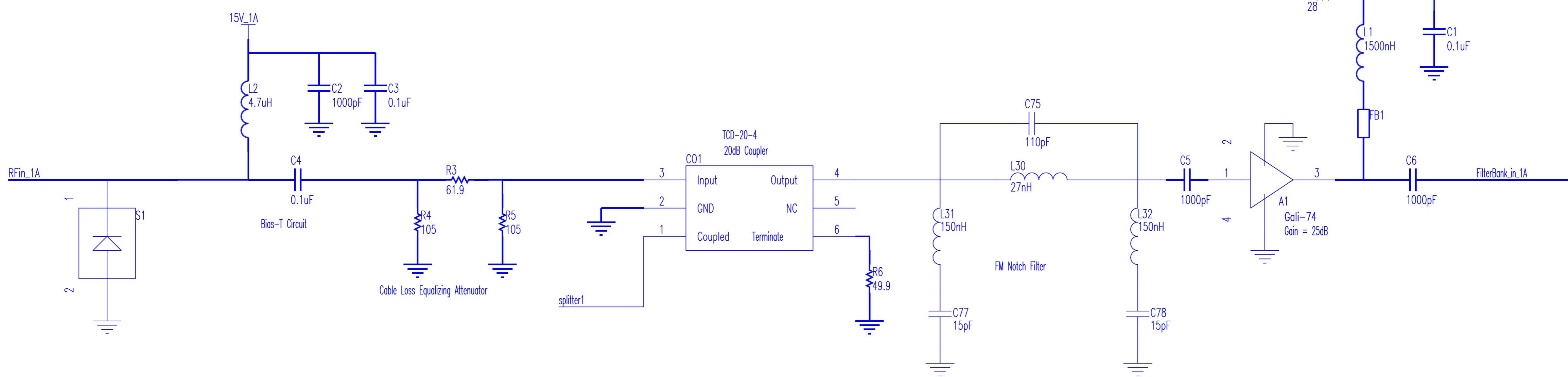


Title			
Size	Number	Revision	
B			
Date:	7/31/2015	Sheet	of
File:	E:\Analog Receiver Board Files\VARX_PWR.SchDoc	Drawn By:	

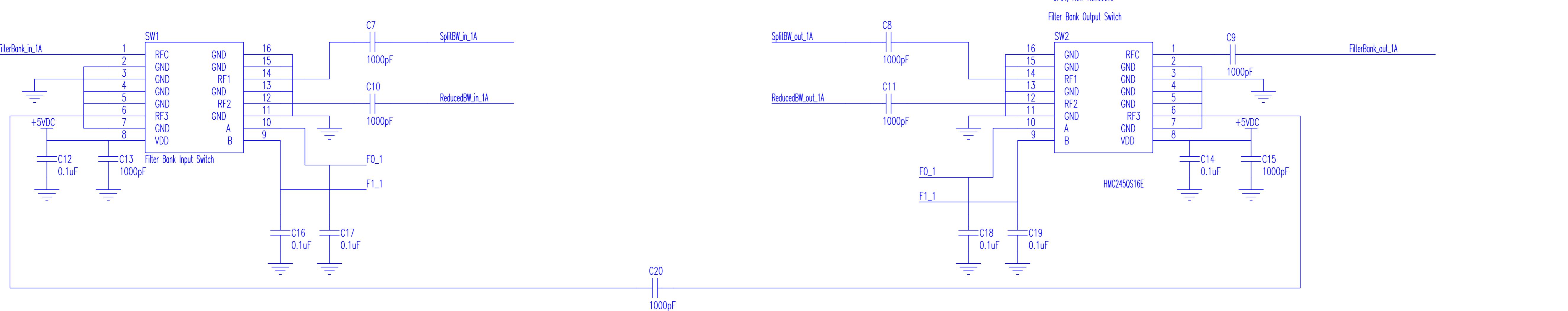


1 2 3 4 5 6

A A



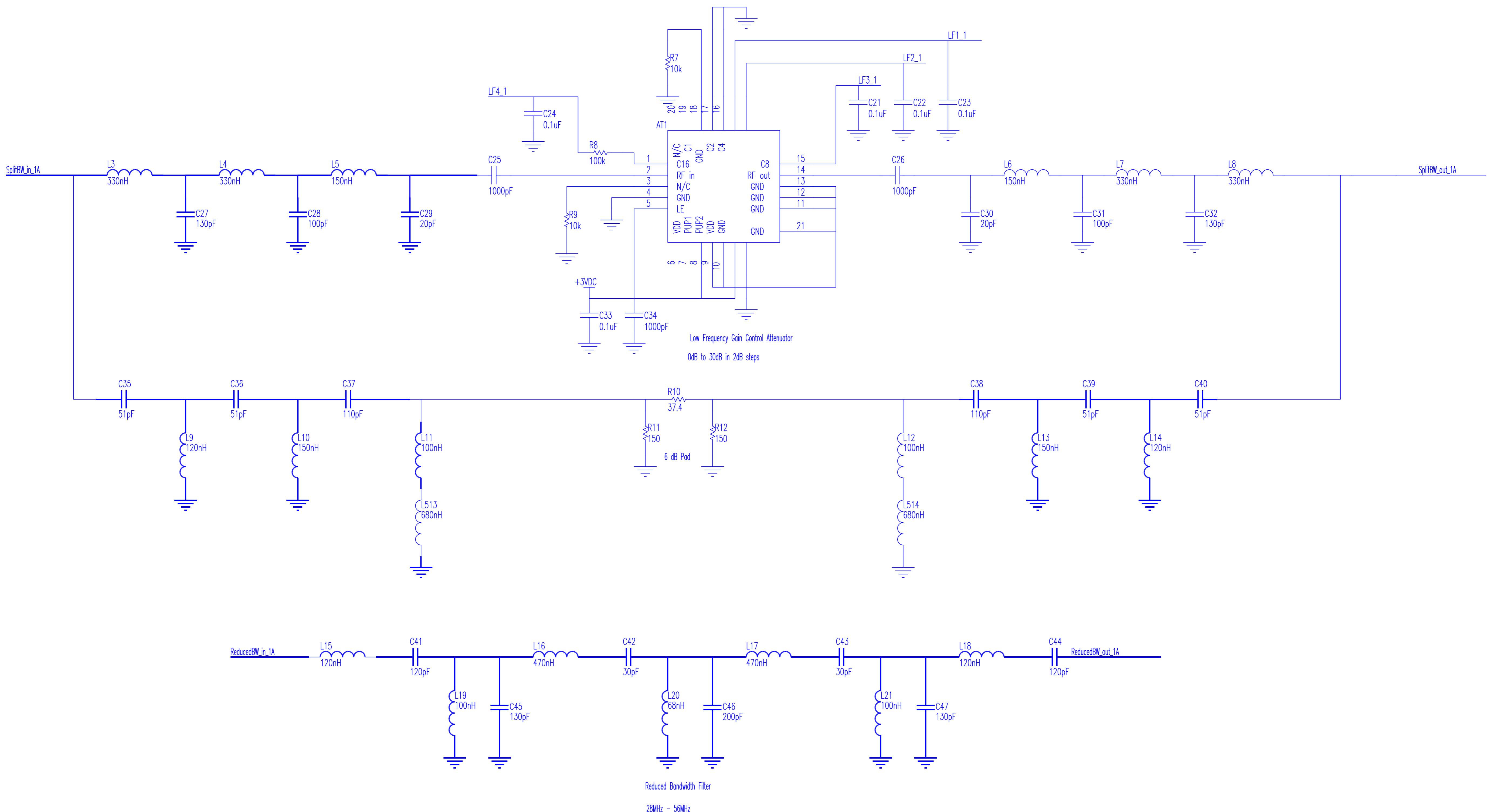
B B



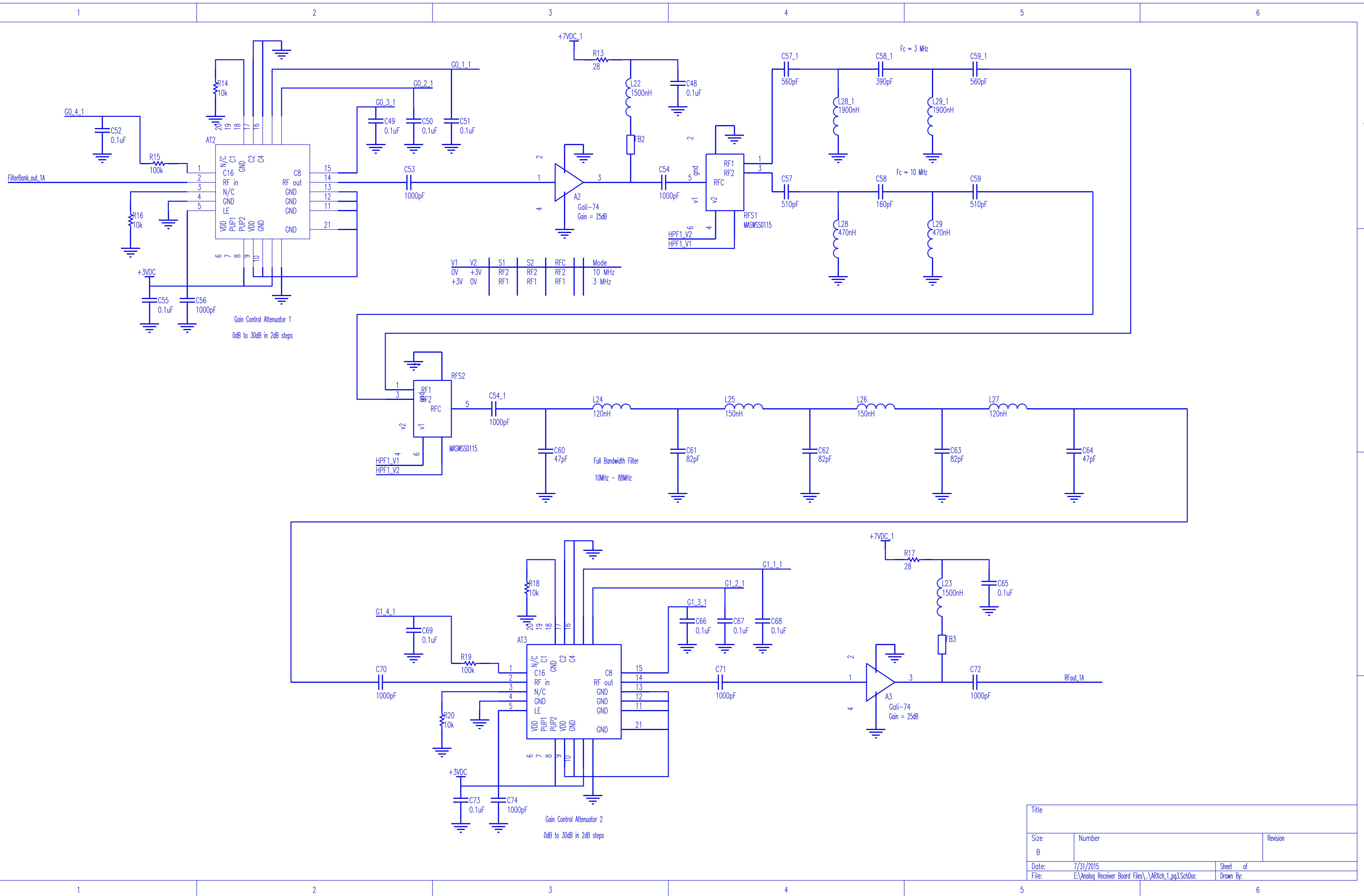
D D

Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\ARXch_1.pq1.SchDoc		Drawn By:

1 2 3 4 5 6

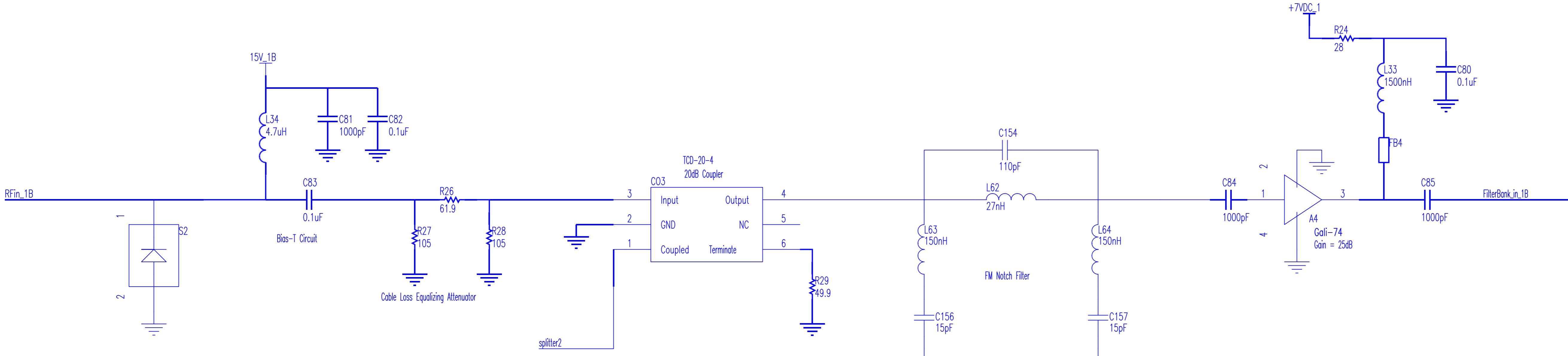


Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\ARXch_1.pq2.SchDoc		Drawn By:



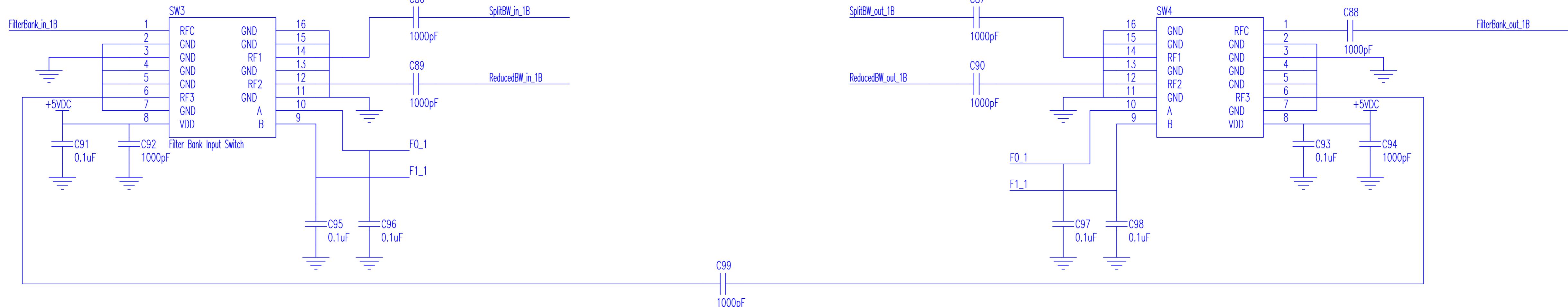
1 2 3 4 5 6

A



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C

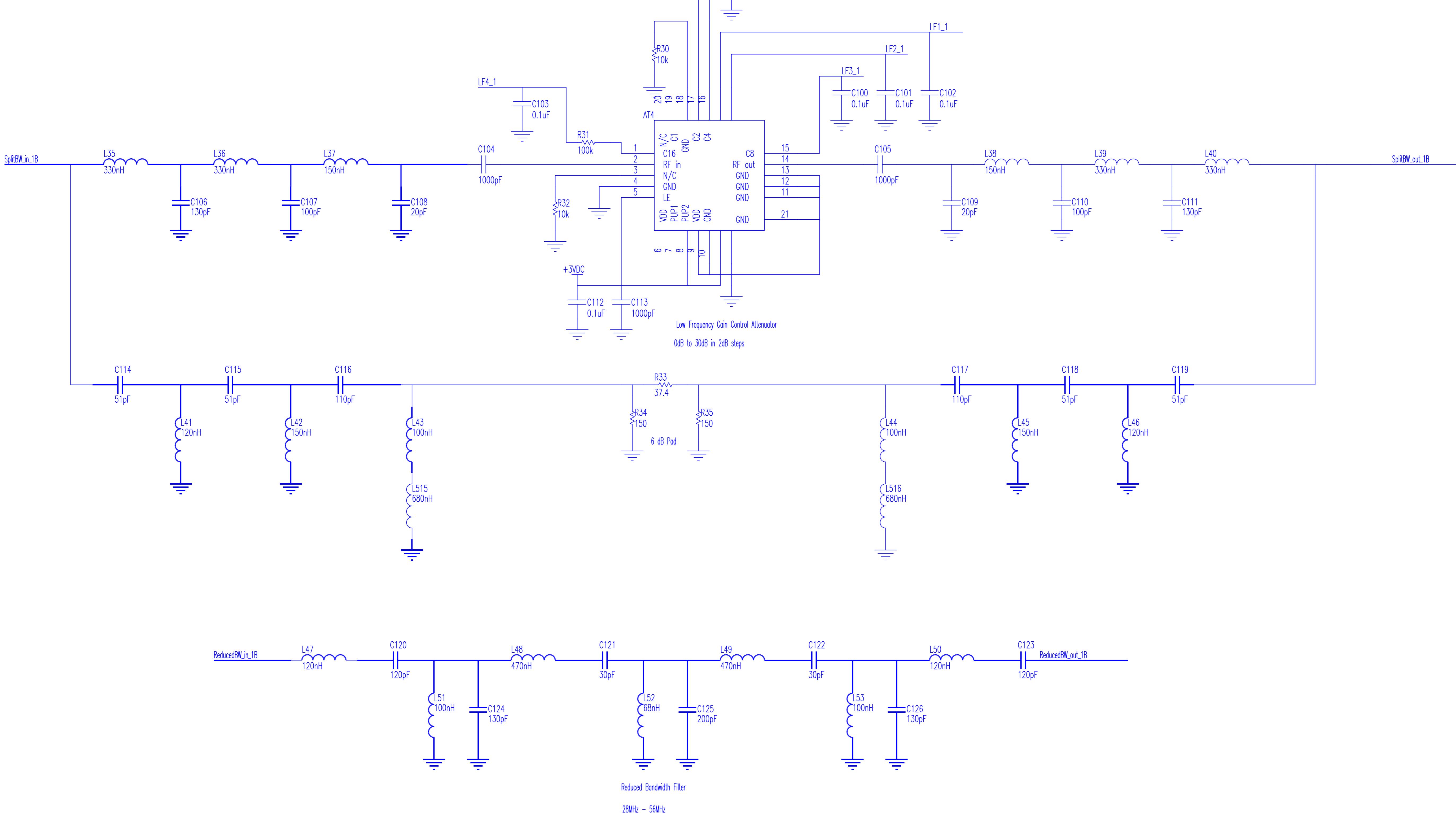
C

D

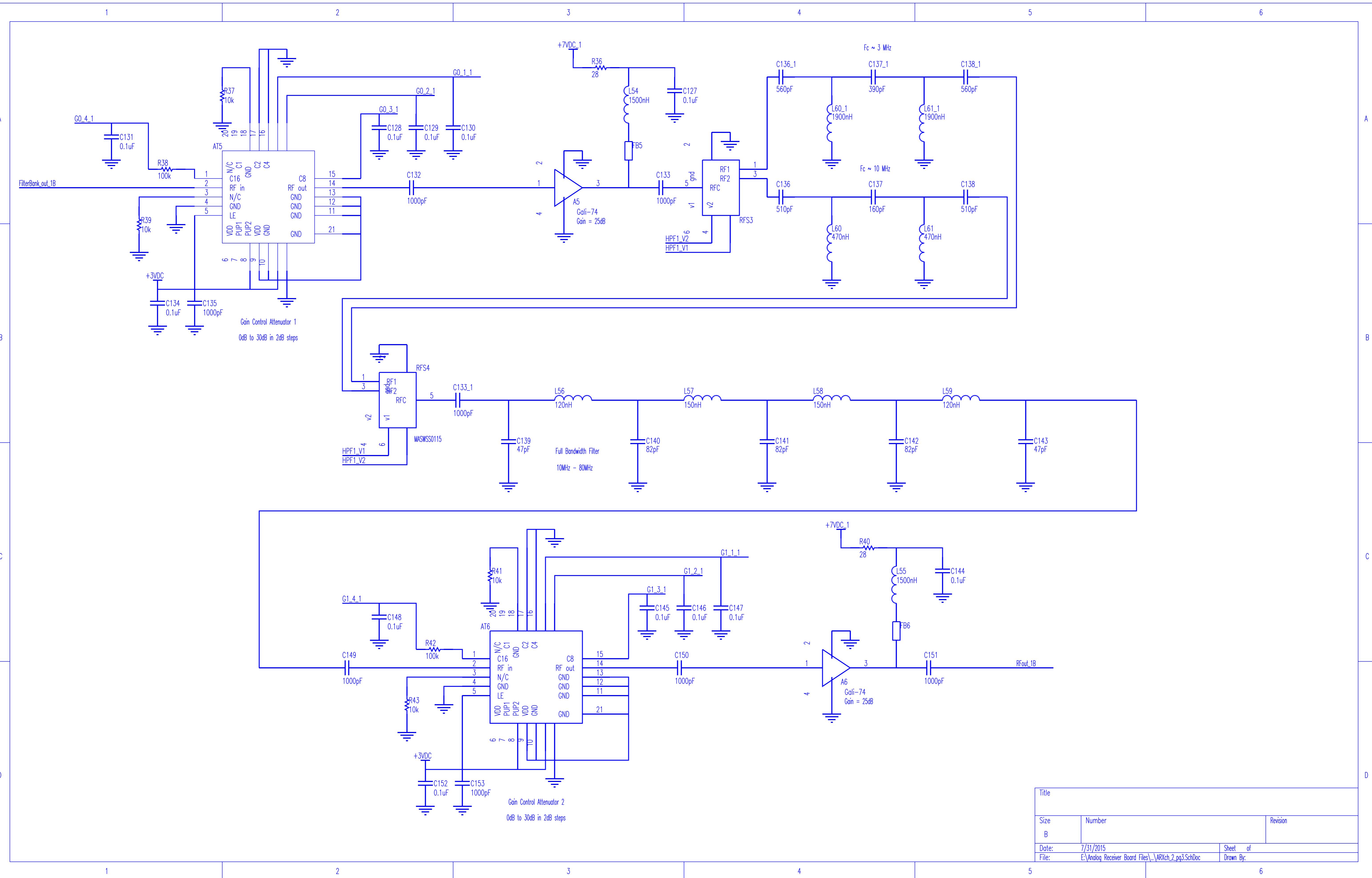
D

Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\VARXch_2.pq1.SchDoc		Drawn By:

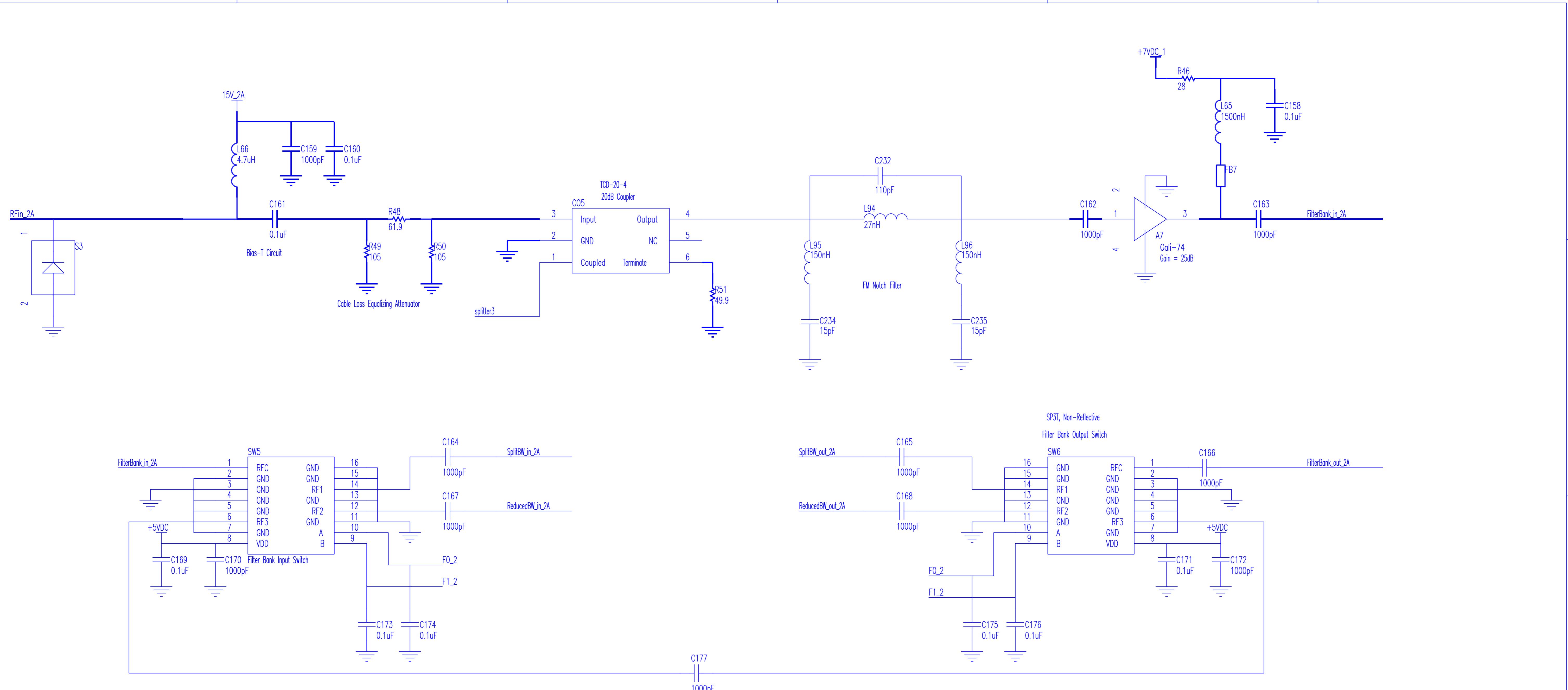
1 2 3 4 5 6



Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\VARXch_2.pq2.SchDoc		Drawn By:

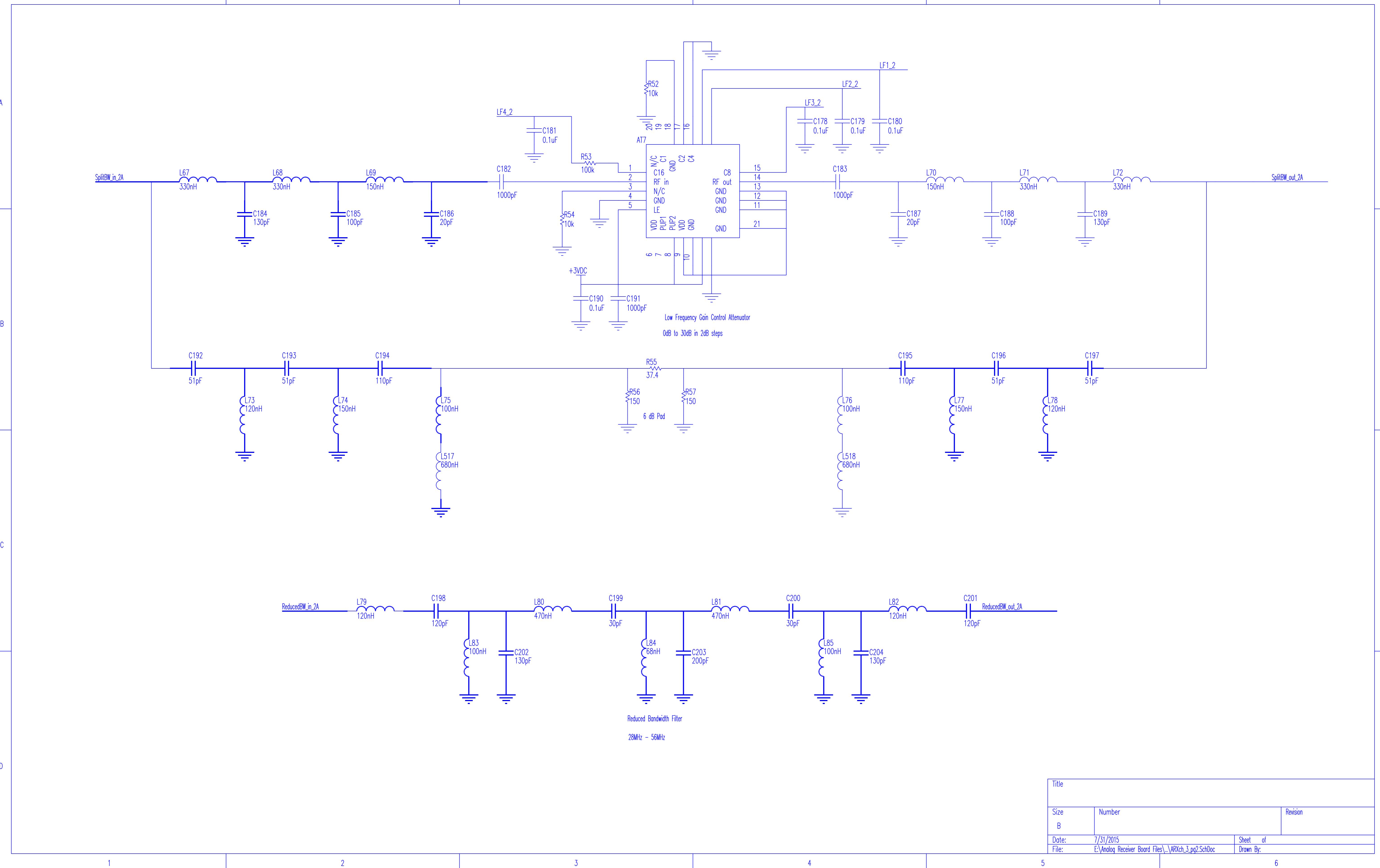


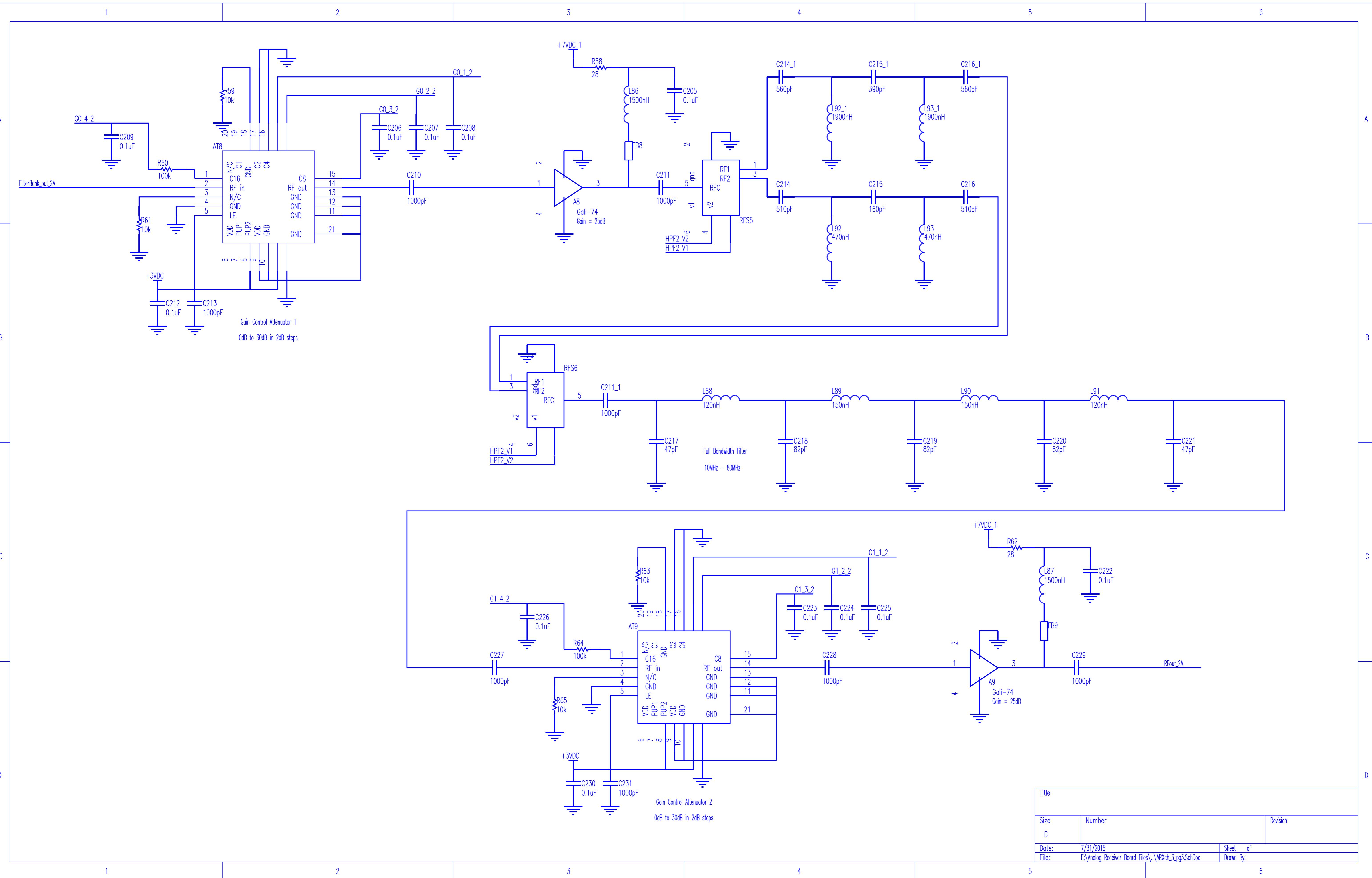
1 2 3 4 5 6



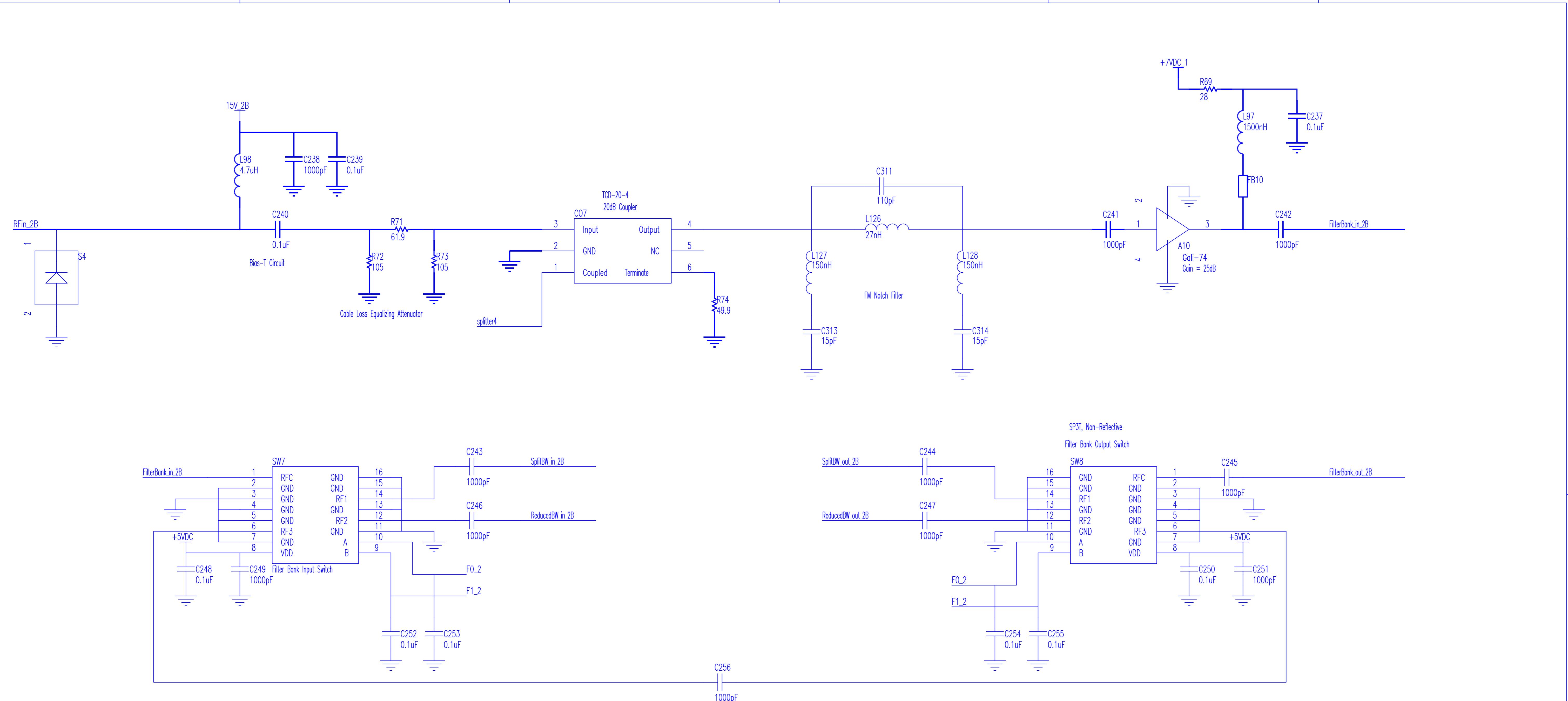
Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\ARXch_3.pq1.SchDoc		Drawn By:

1 2 3 4 5 6



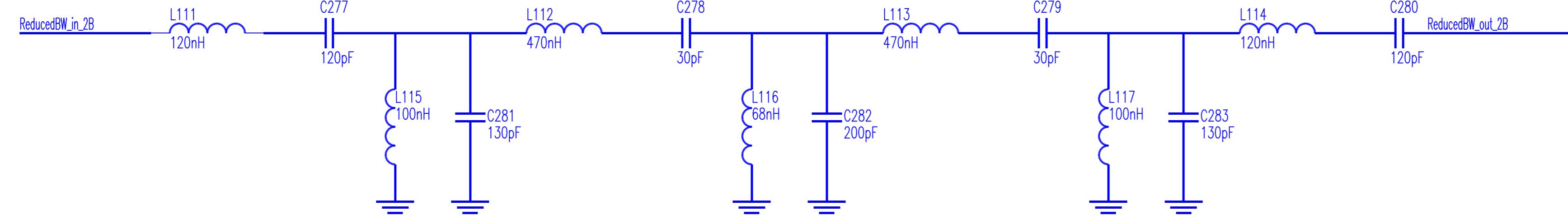
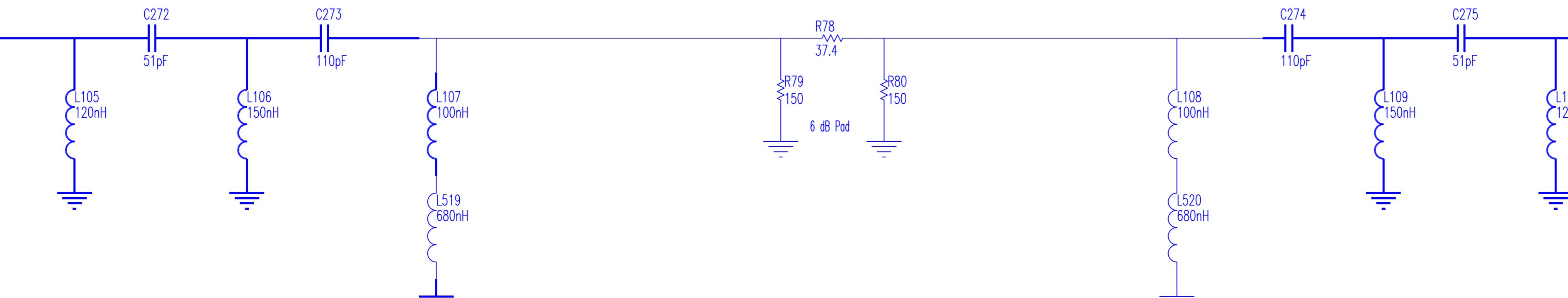
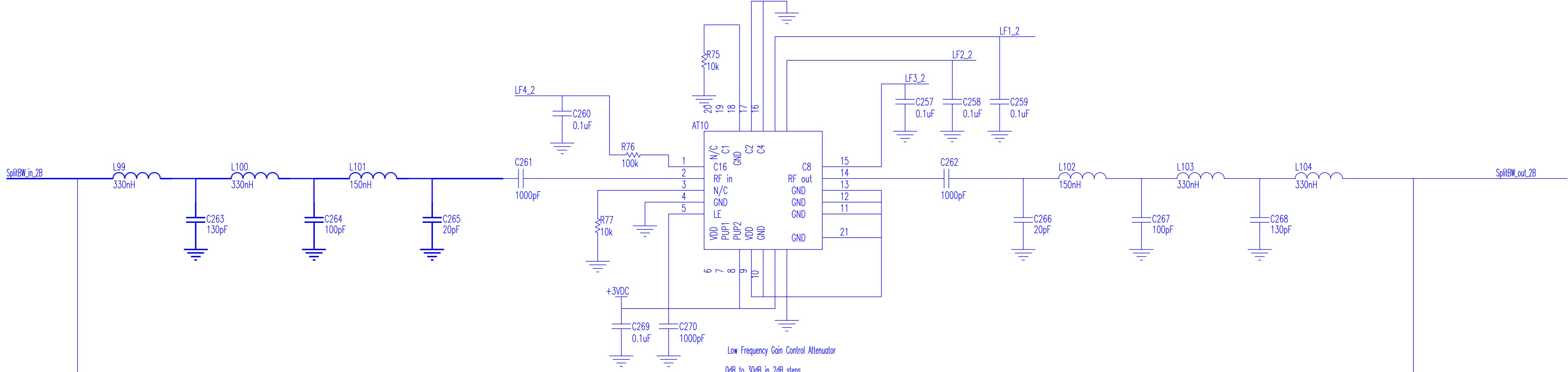


1 2 3 4 5 6



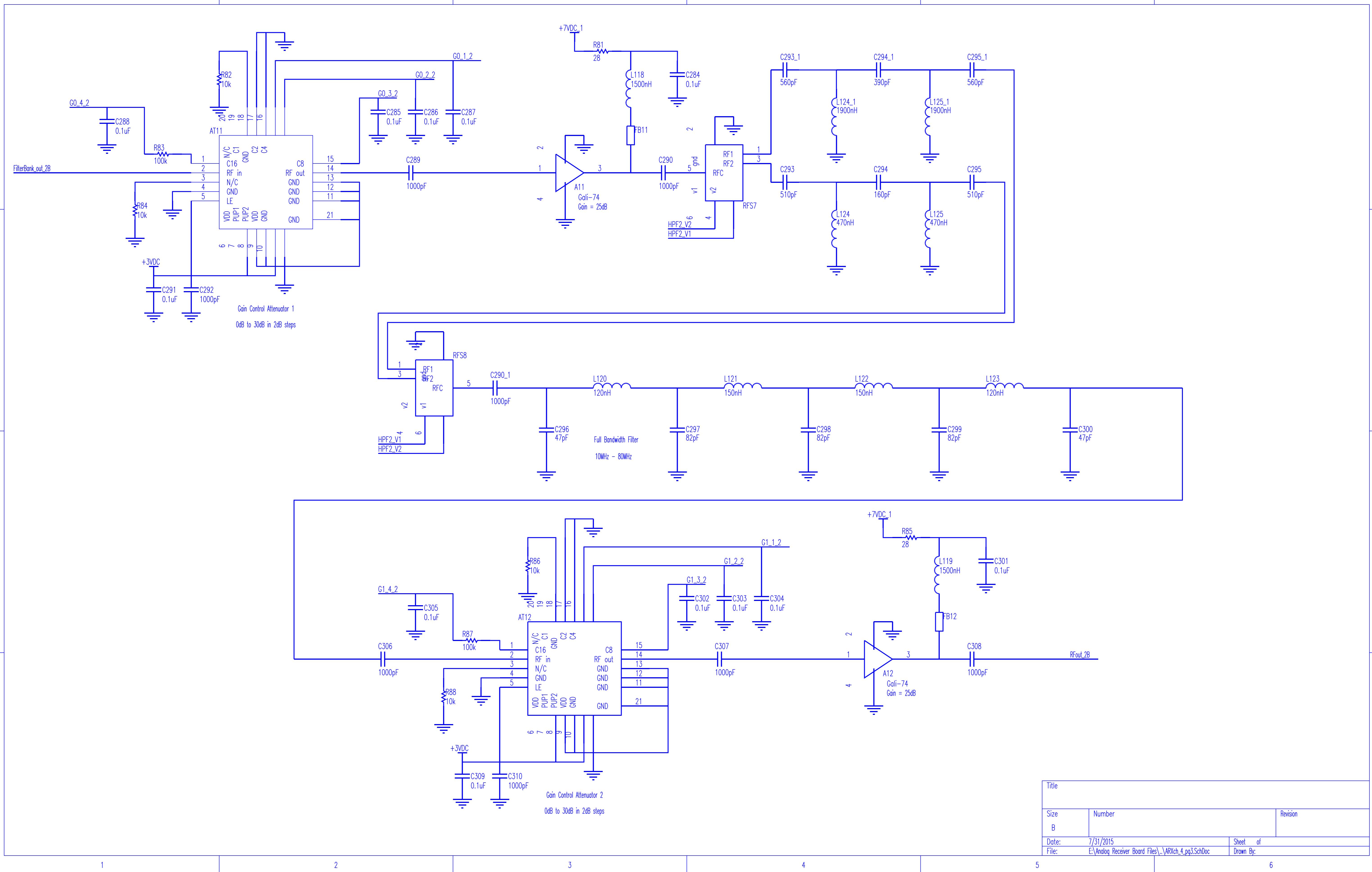
Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\ARXch_4_pg1.SchDoc		Drawn By:

1 2 3 4 5 6



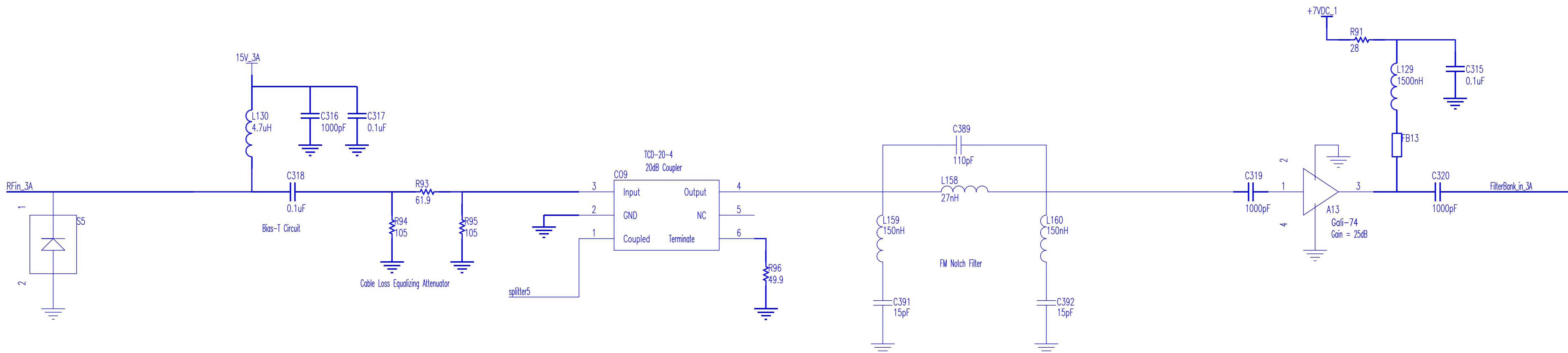
Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\ARXch.4.pqSchDoc		Drawn By:

1 2 3 4 5 6

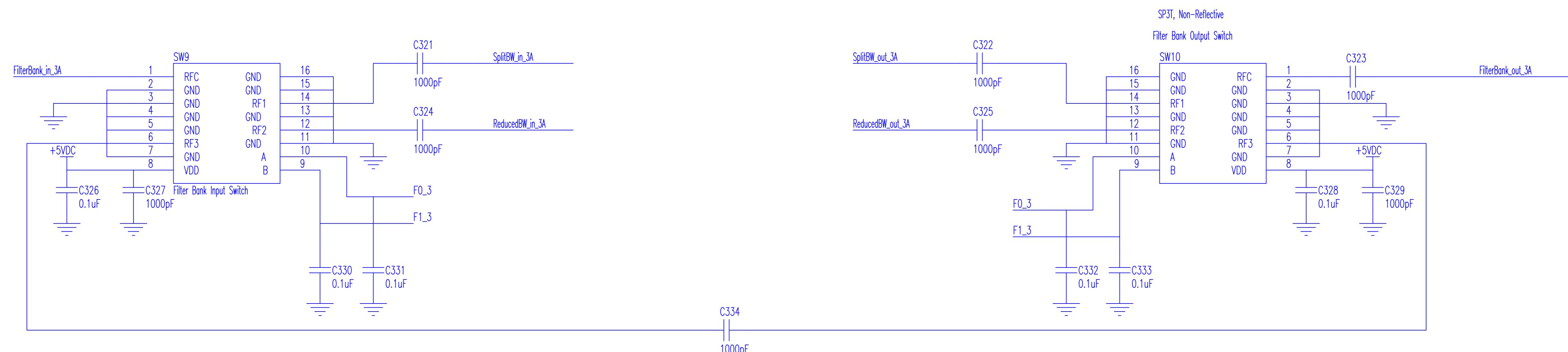


1 2 3 4 5 6

A



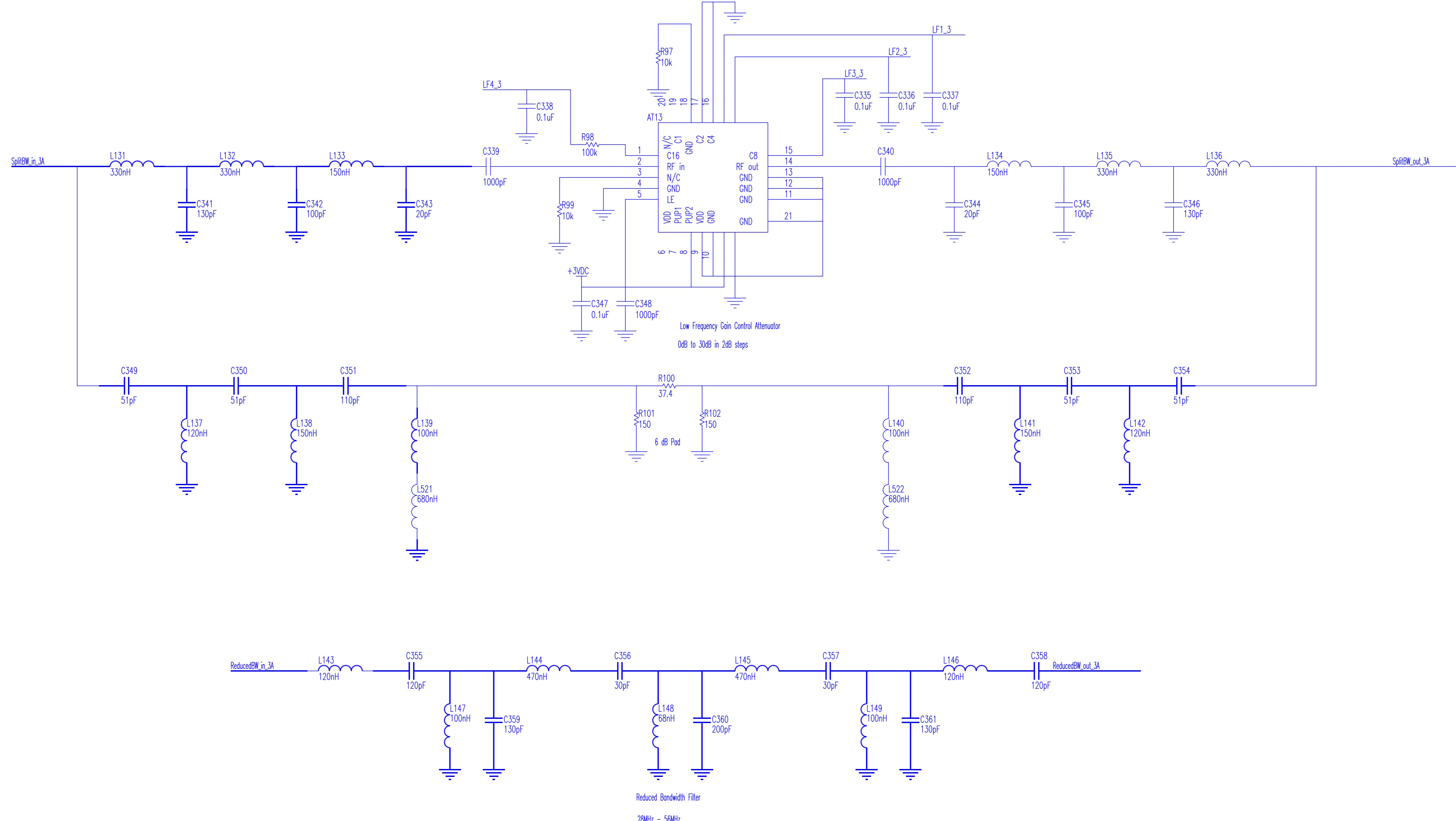
B



C

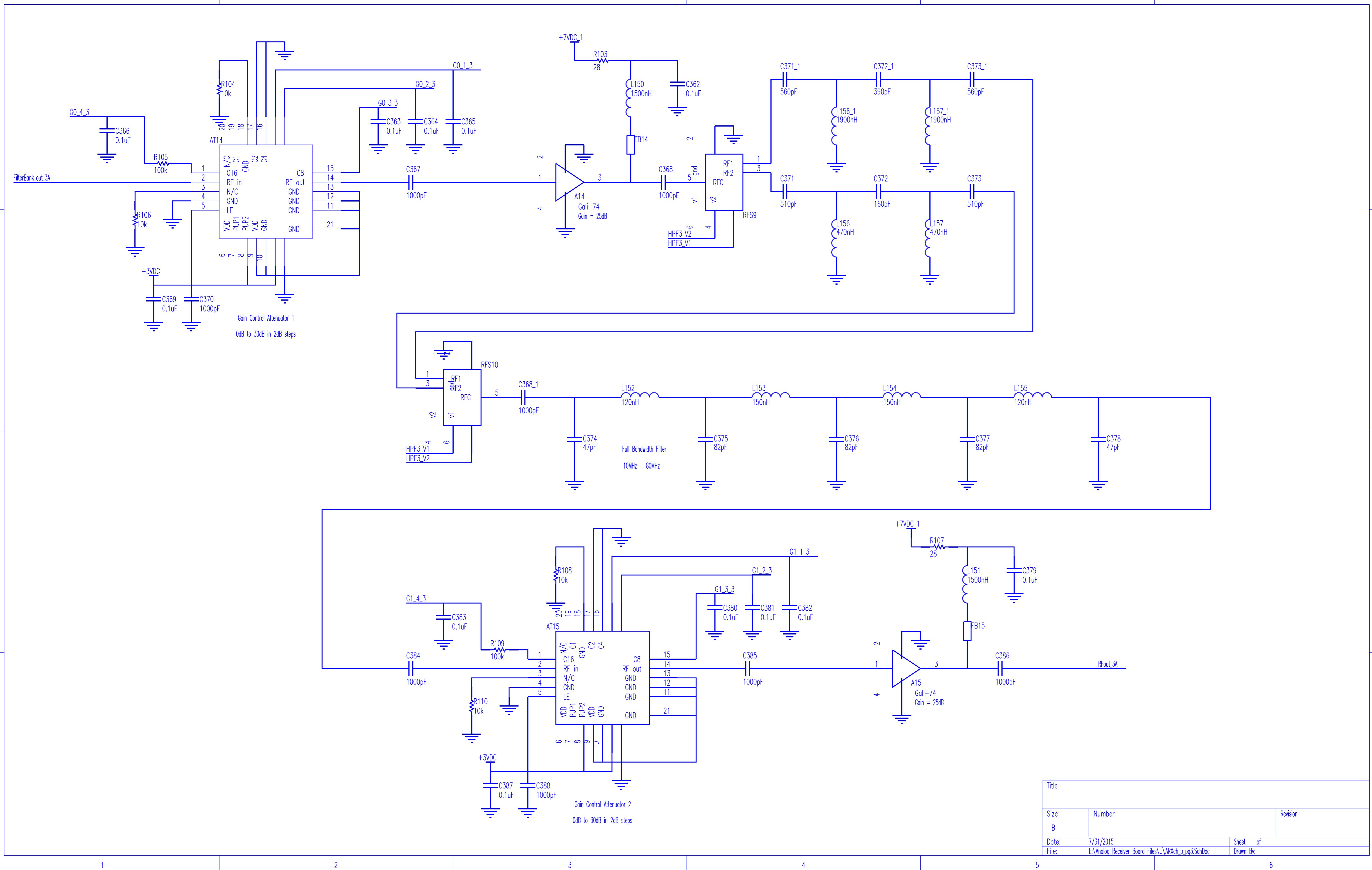
Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\ARXch_5.pq1.SchDoc		Drawn By:

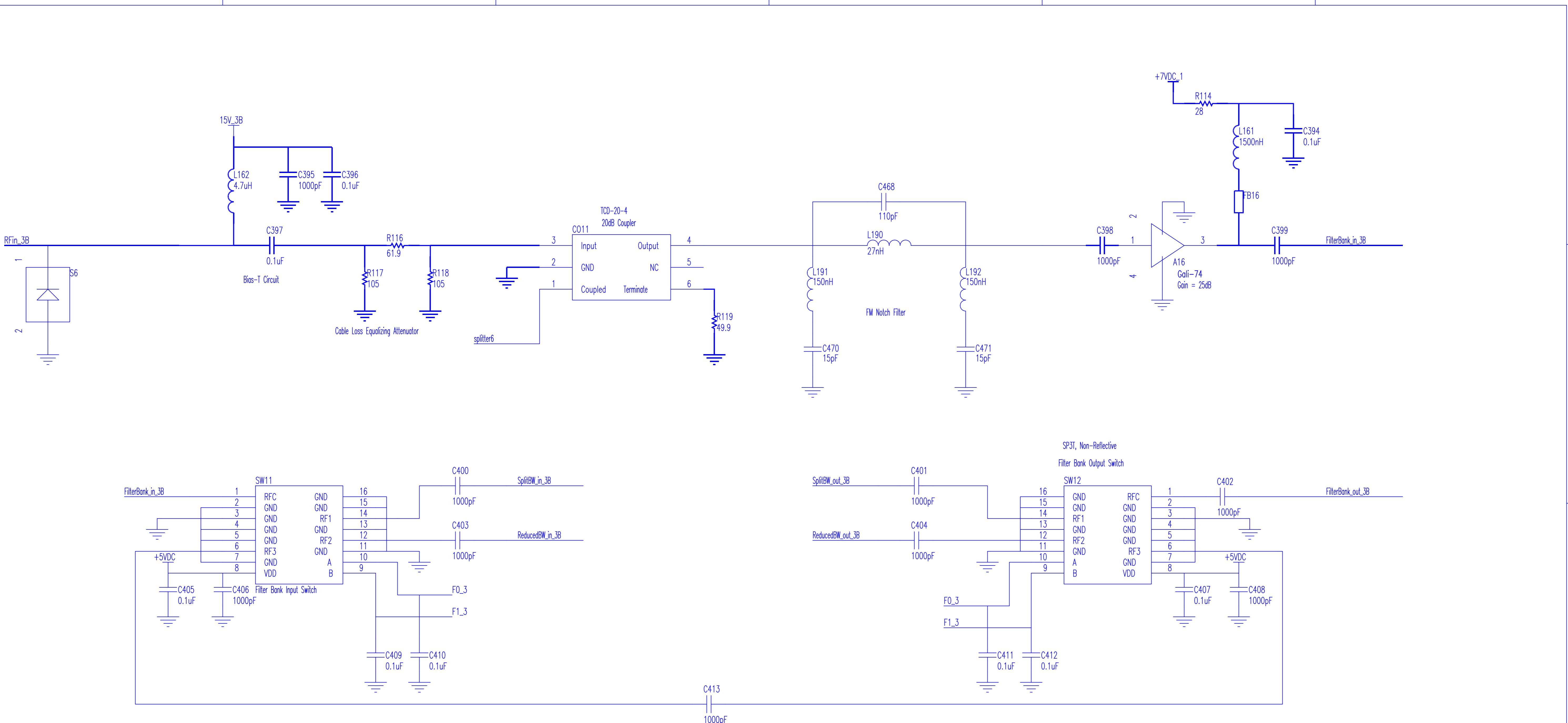
1 2 3 4 5 6



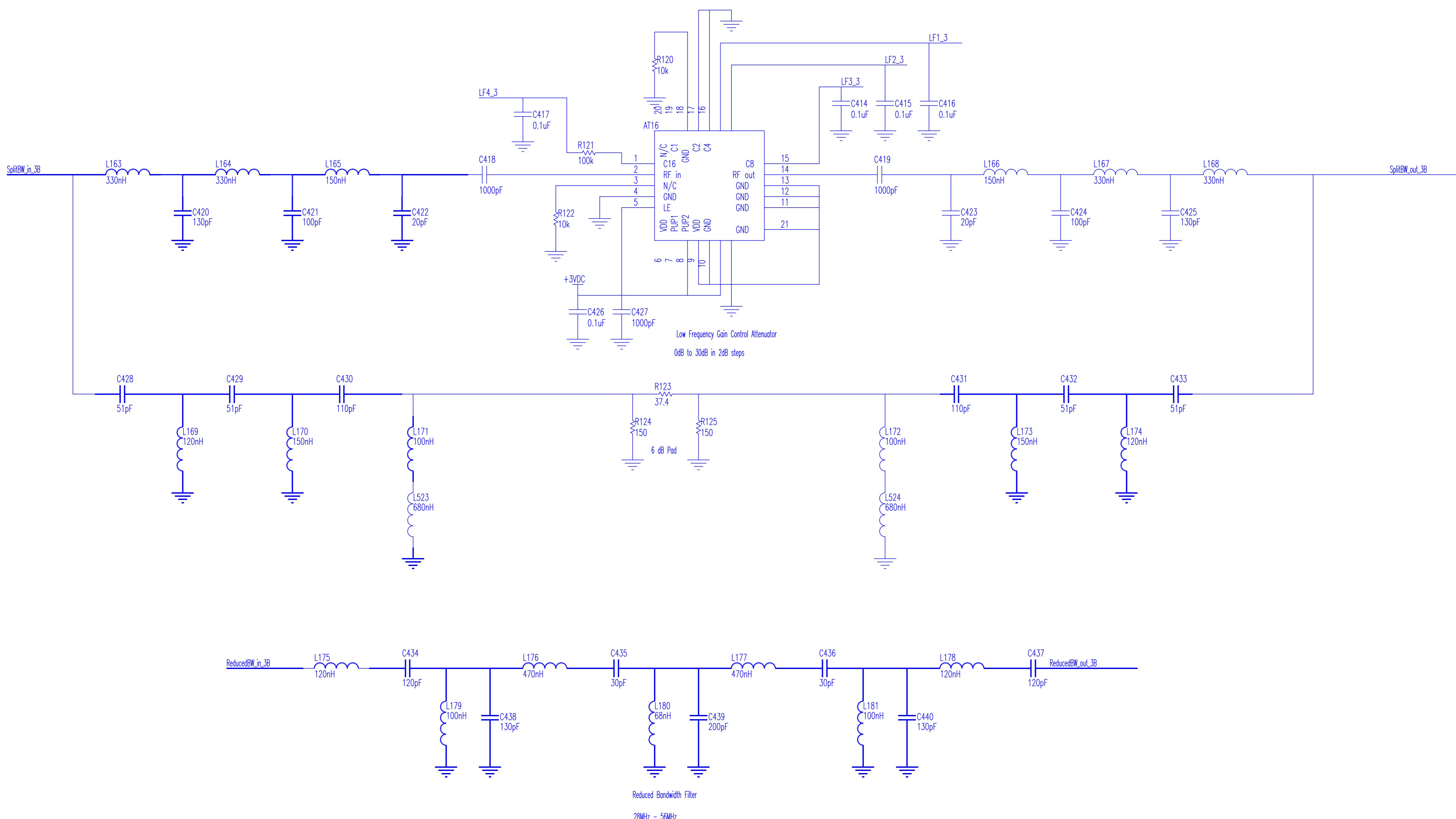
Title		
Size B	Number	Revision
Date:	7/31/2015	Sheet of
File:	E:\Analog Receiver Board Files\..\ARYch_5_pg2.SchDoc	Drawn By:

1 2 3 4 5 6



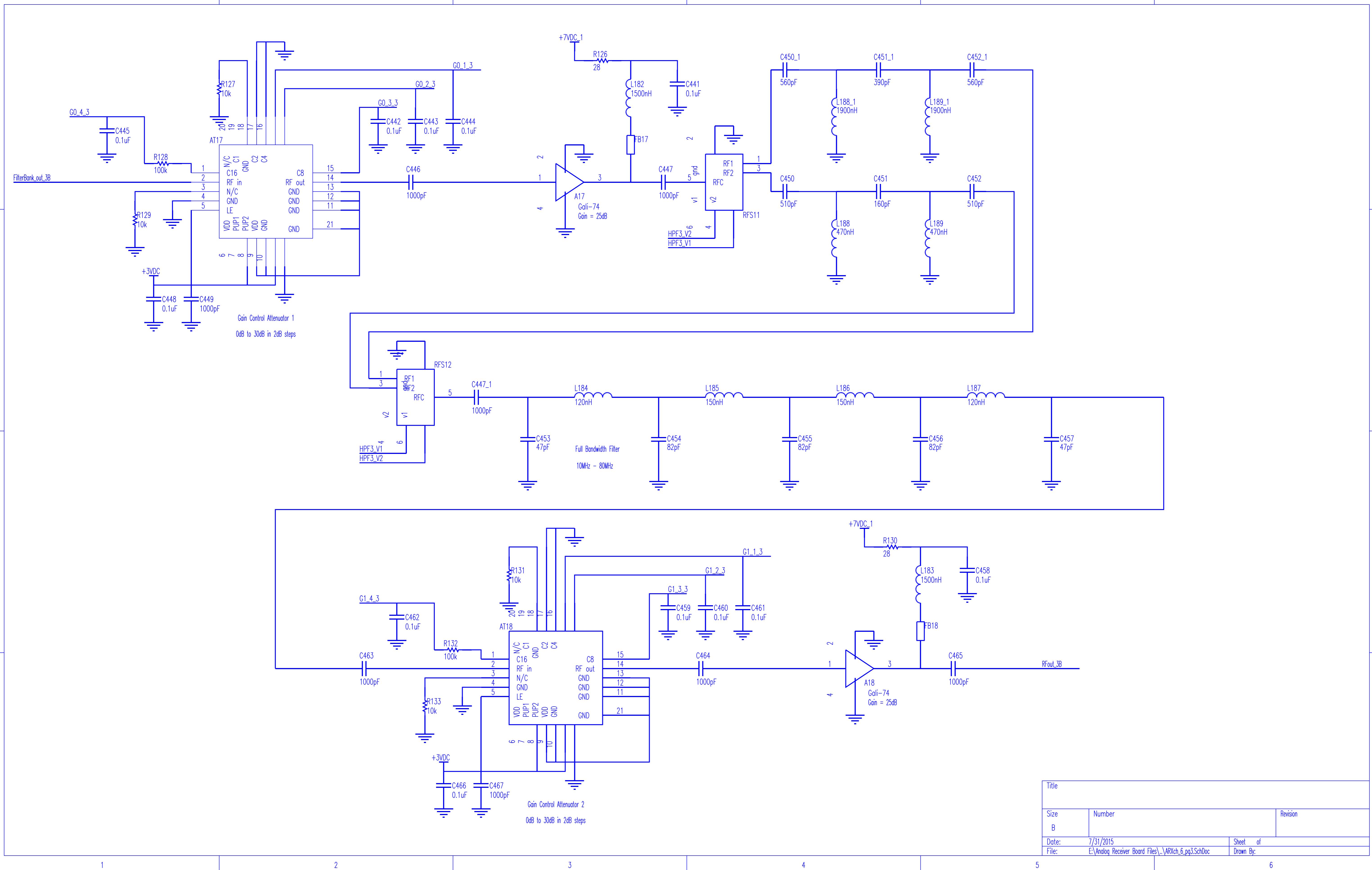


Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\ARXch.6.pq1.SchDoc		Drawn By:

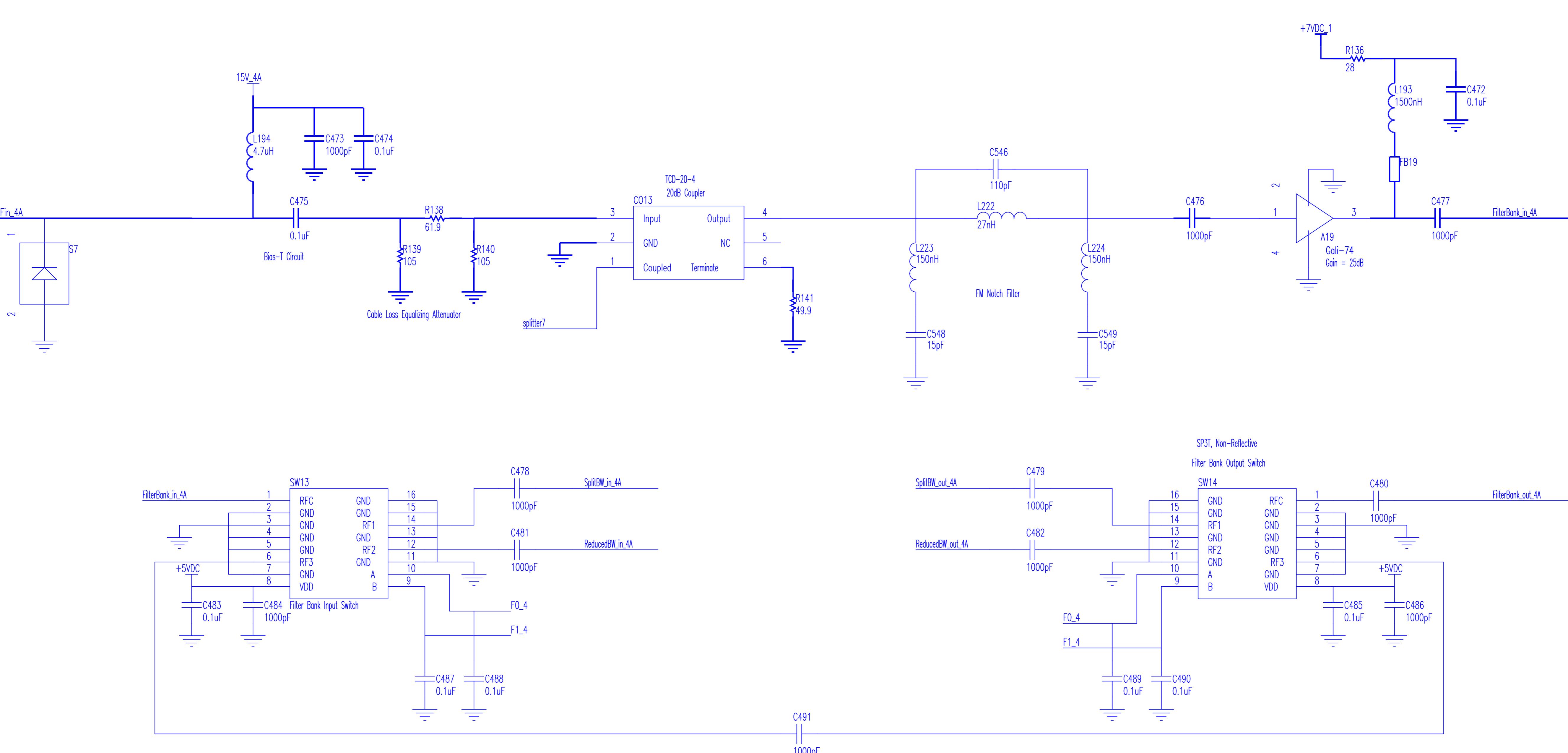


Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\ARXch.6.pq2.SchDoc		Drawn By:

1 2 3 4 5 6

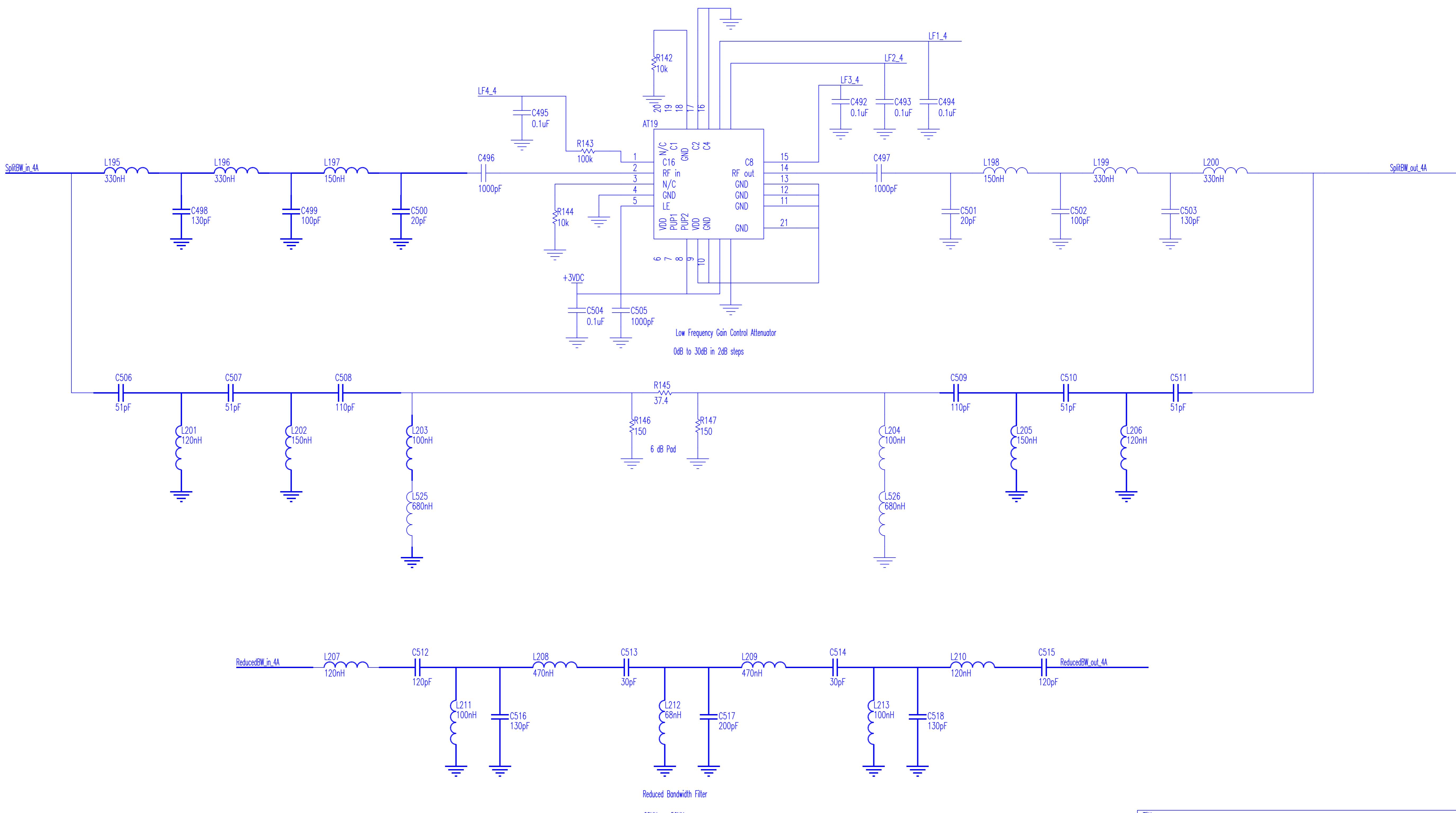


1 2 3 4 5 6

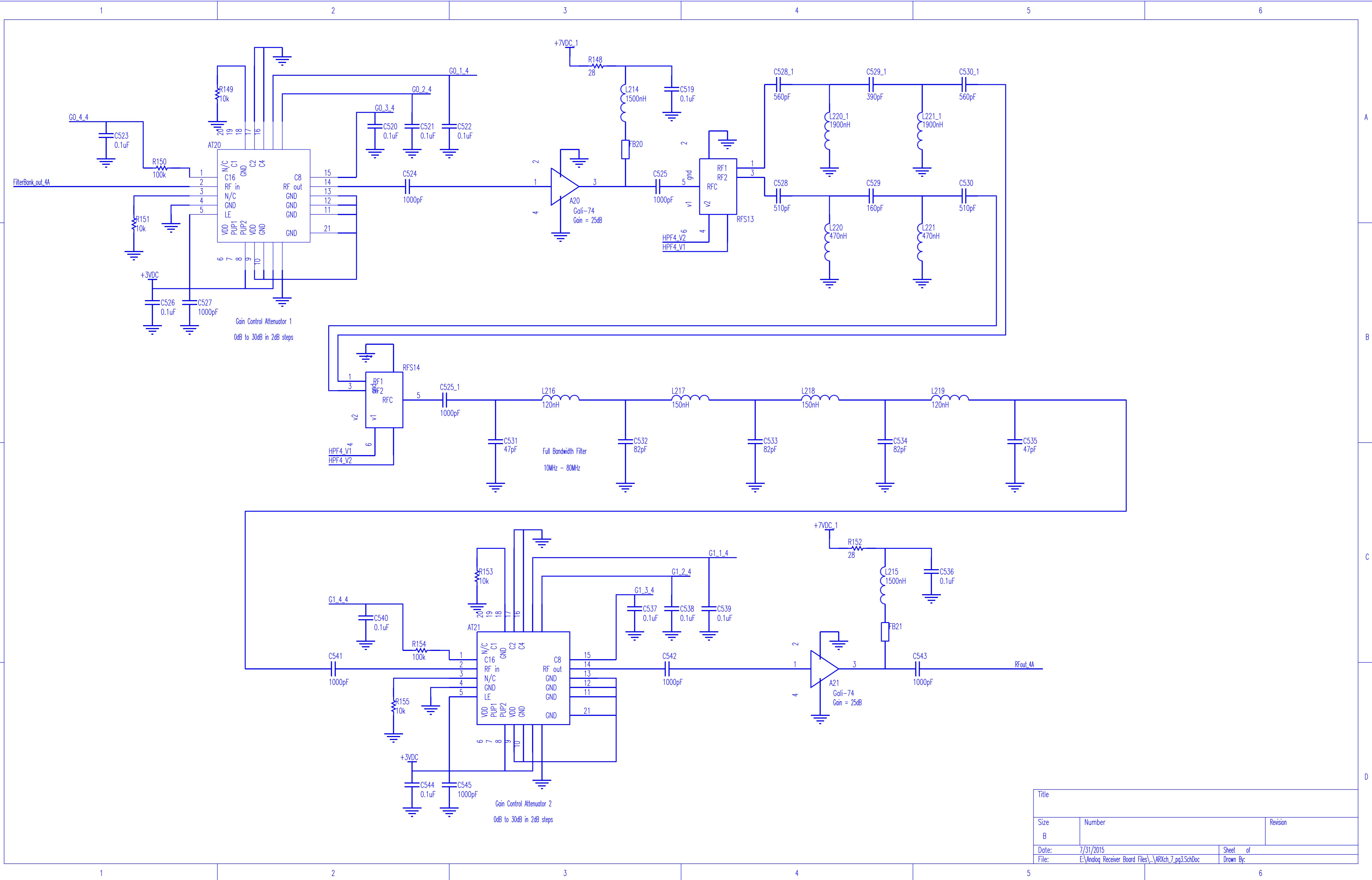


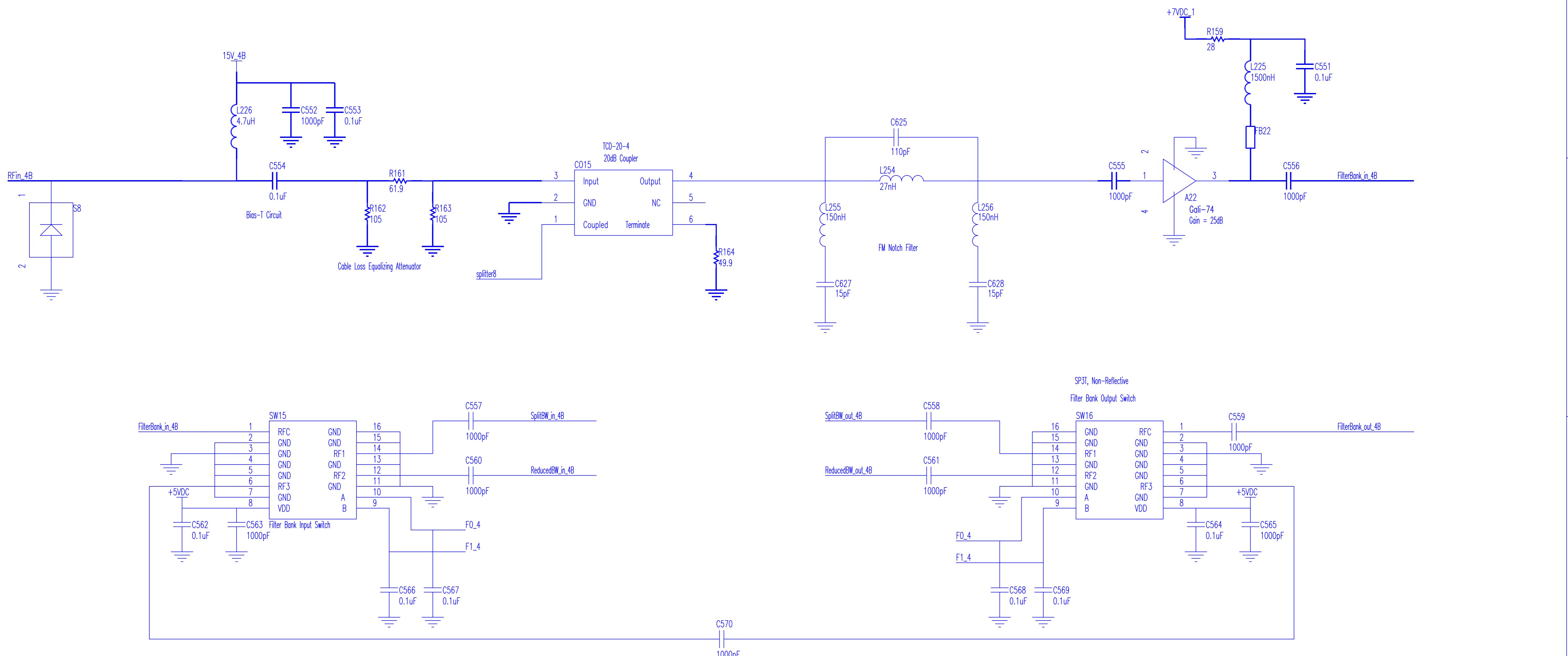
Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\ARXch_7.pq1.SchDoc		Drawn By:

1 2 3 4 5 6

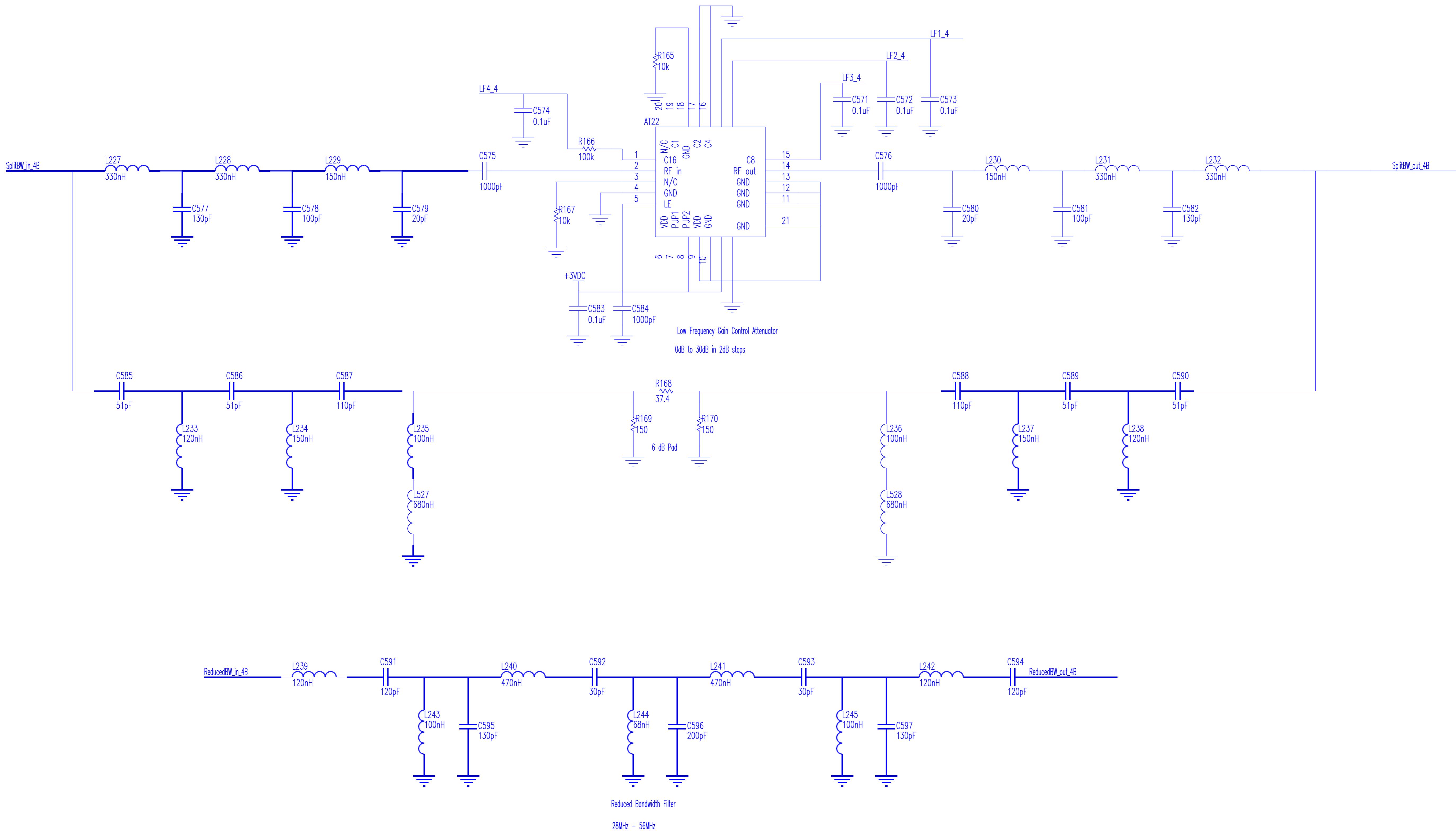


Title	
Size B	Number
Date: 7/31/2015	Sheet of
File: E:\Analog Receiver Board Files\..\ARXch_7_pg2.SchDoc	Drawn By:



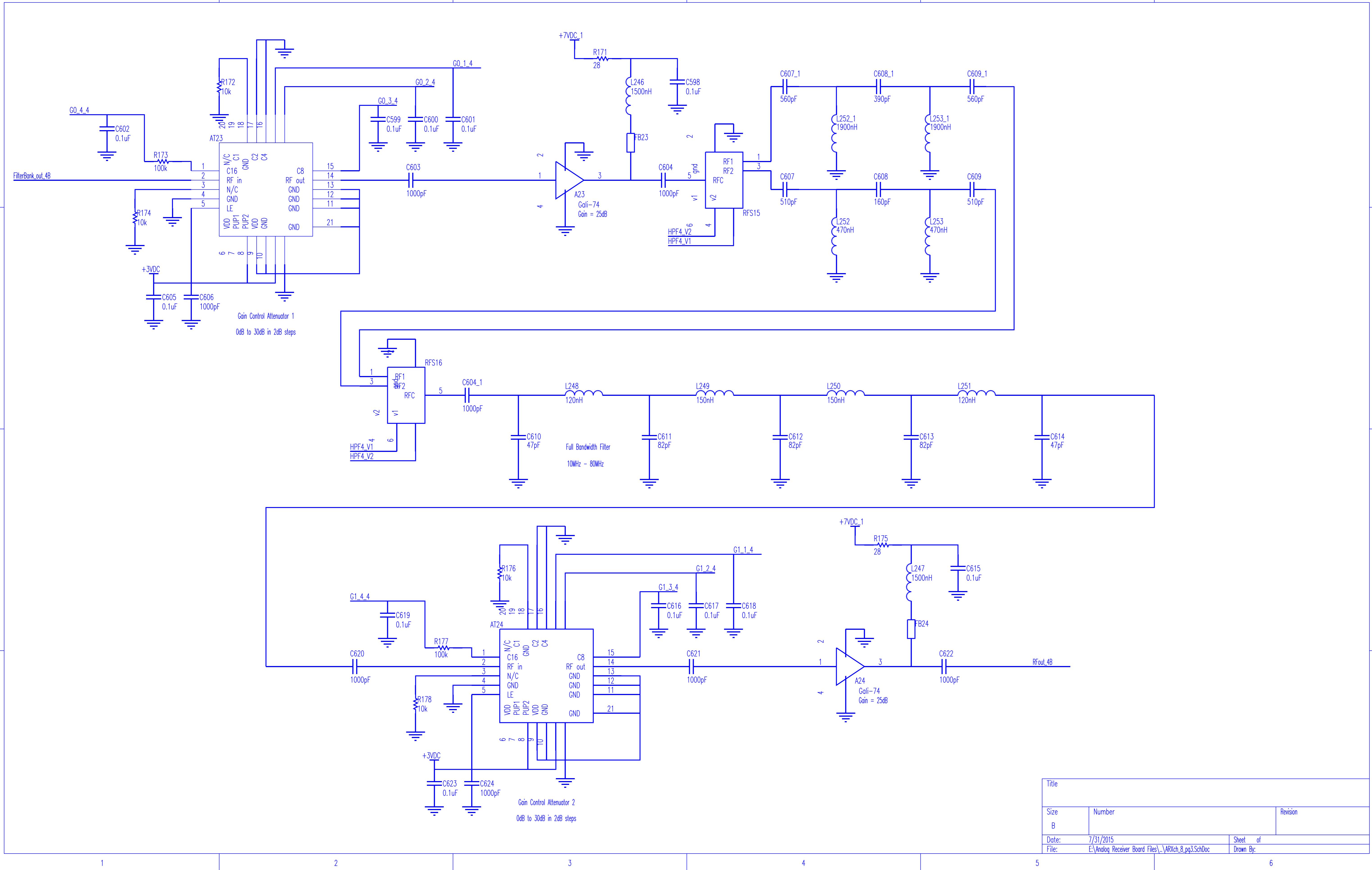


Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\ARXch.8.pq1.SchDoc		Drawn By:



Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\ARXch.8.pq2.SchDoc		Drawn By:

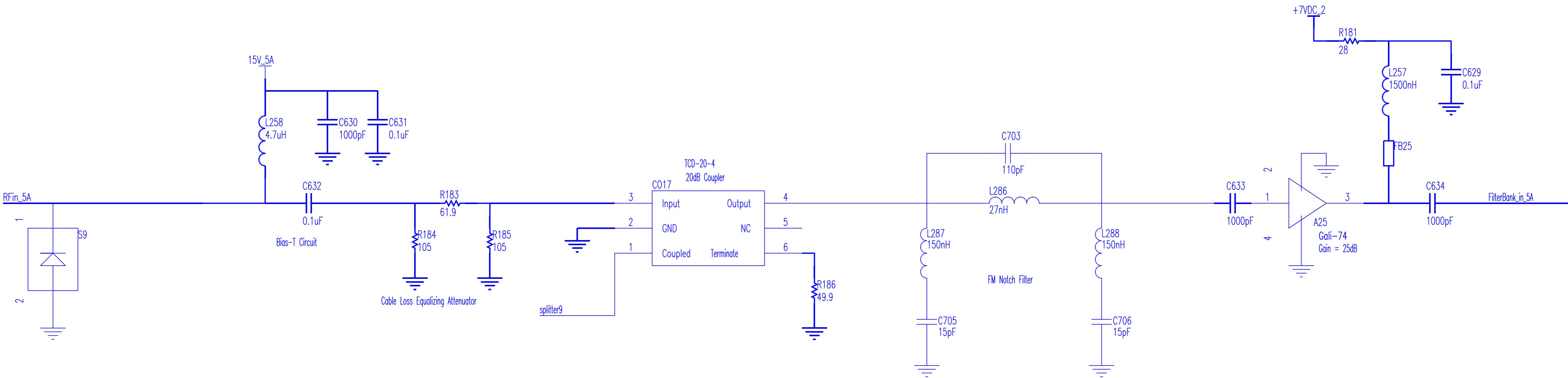
1 2 3 4 5 6



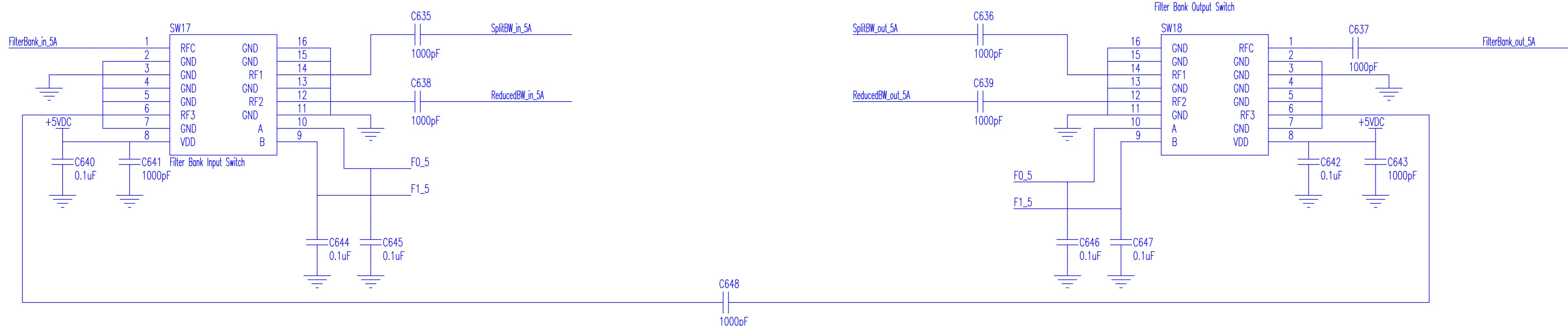
1 2 3 4 5 6

1 2 3 4 5 6

A



B

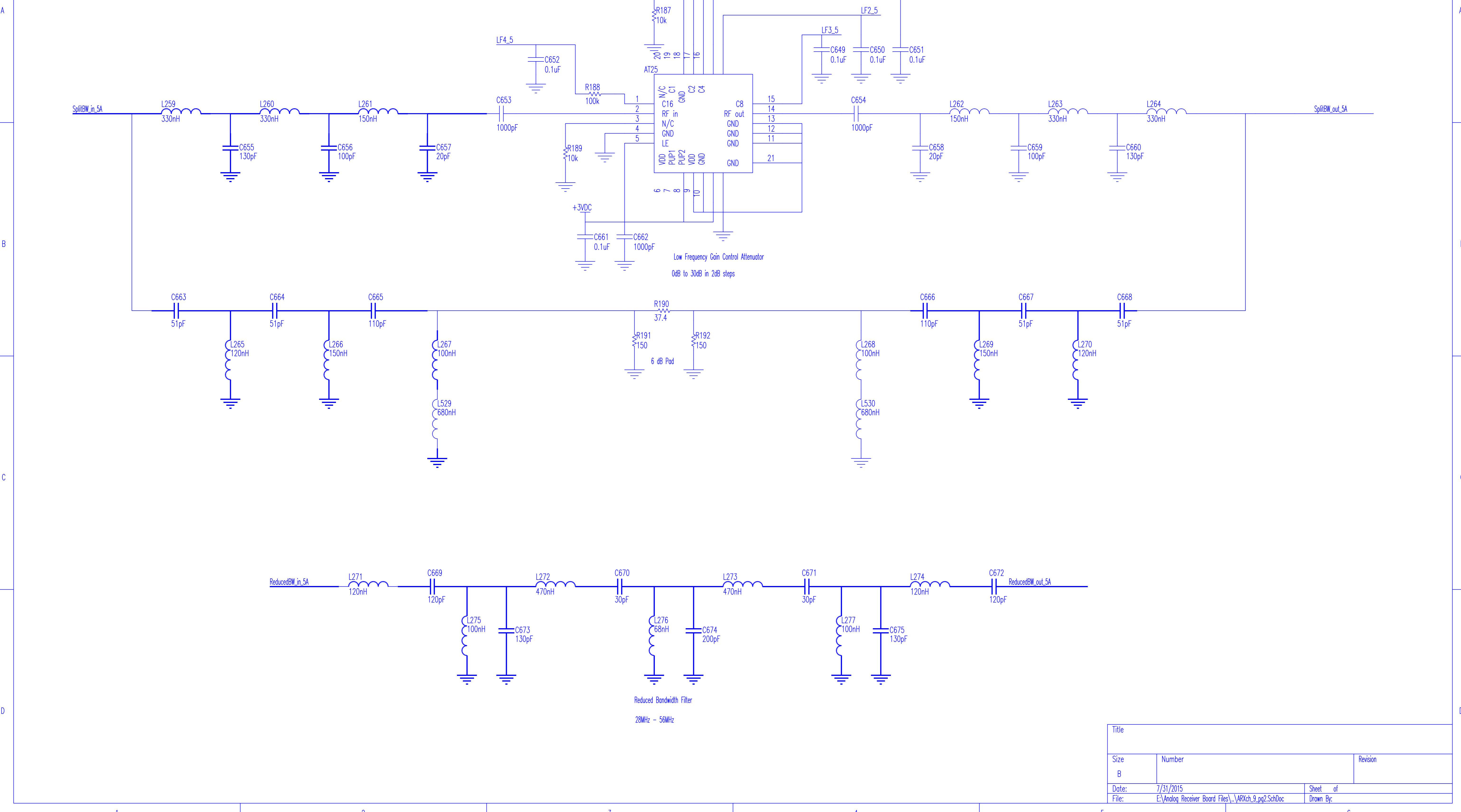


C

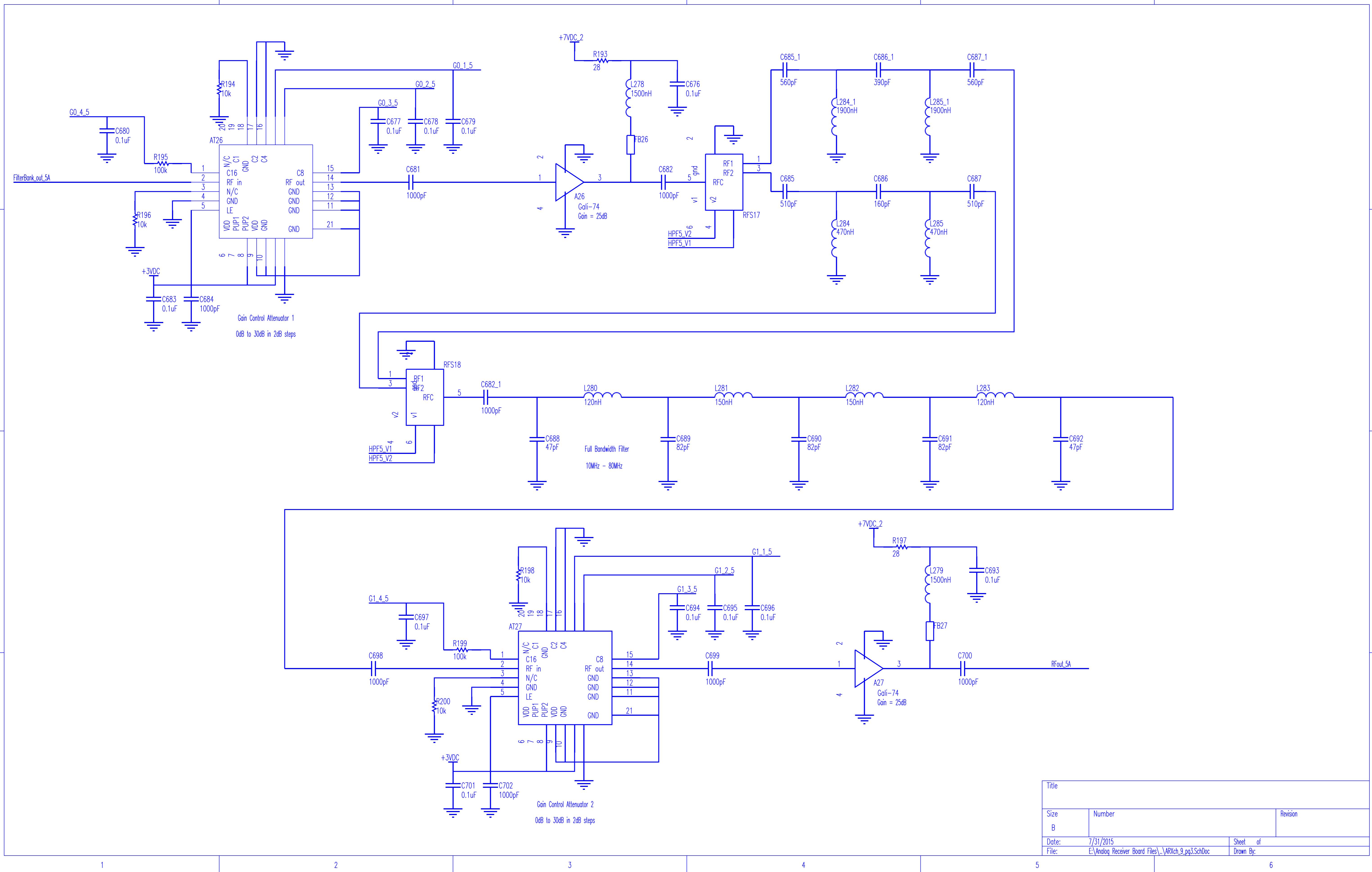
D

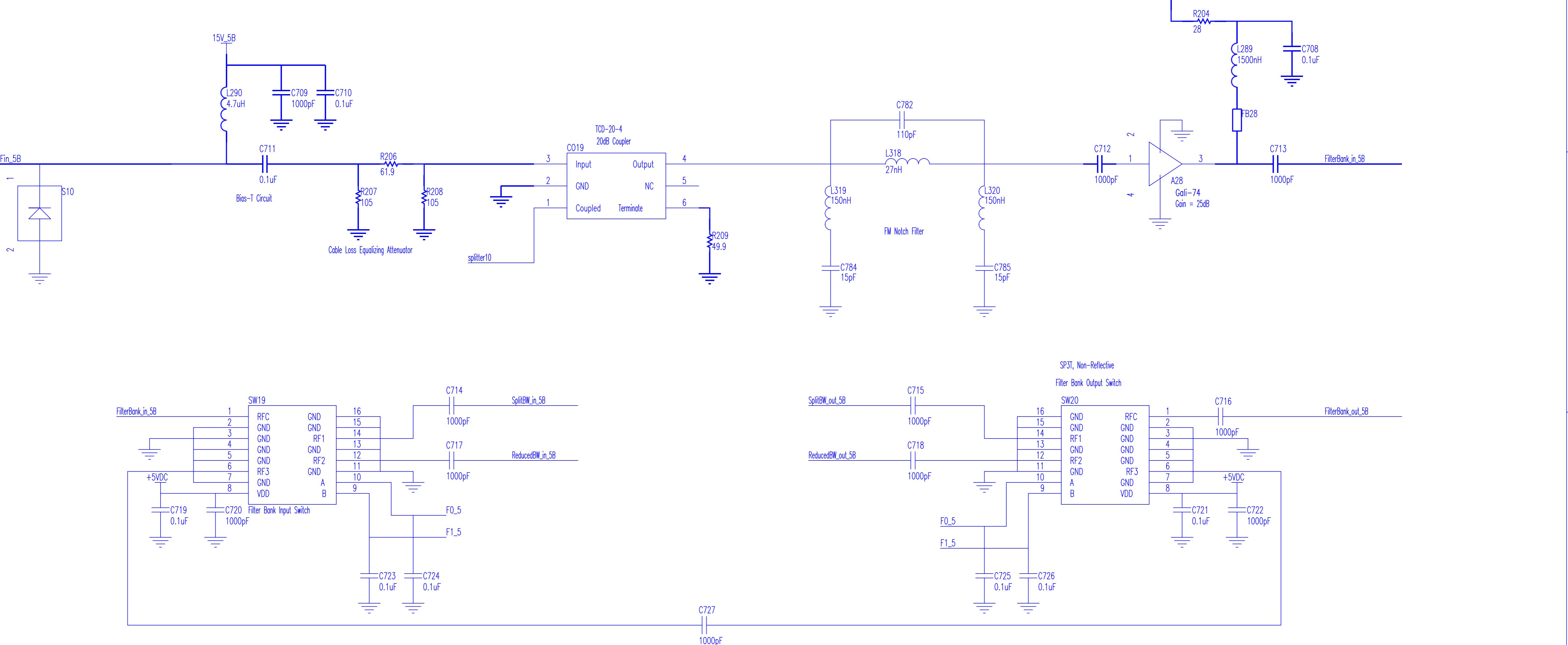
Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\ARXch.9_pg1.SchDoc		Drawn By:

1 2 3 4 5 6

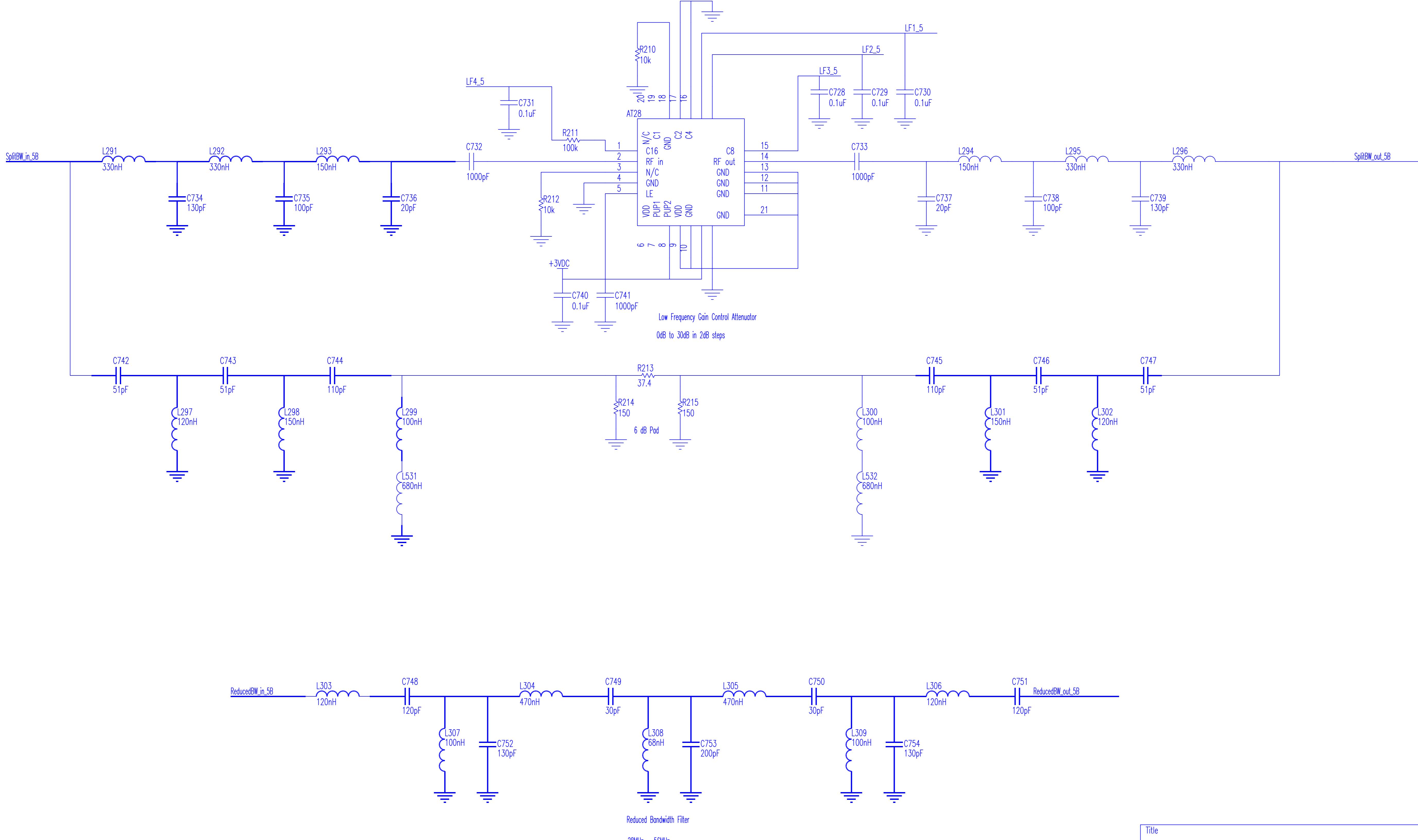


1 2 3 4 5 6



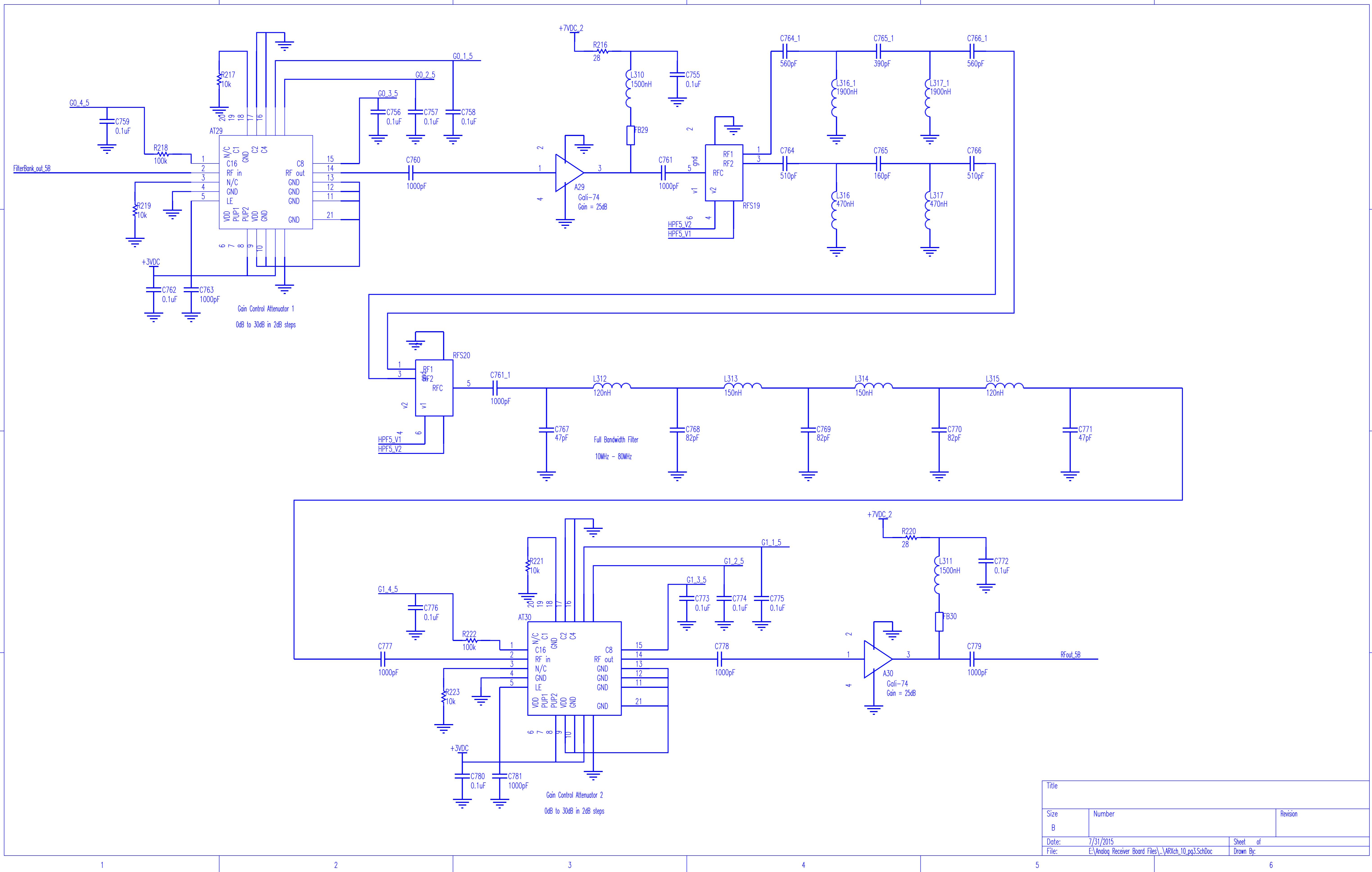


Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\ARXch_10_pg1.SchDoc		Drawn By:

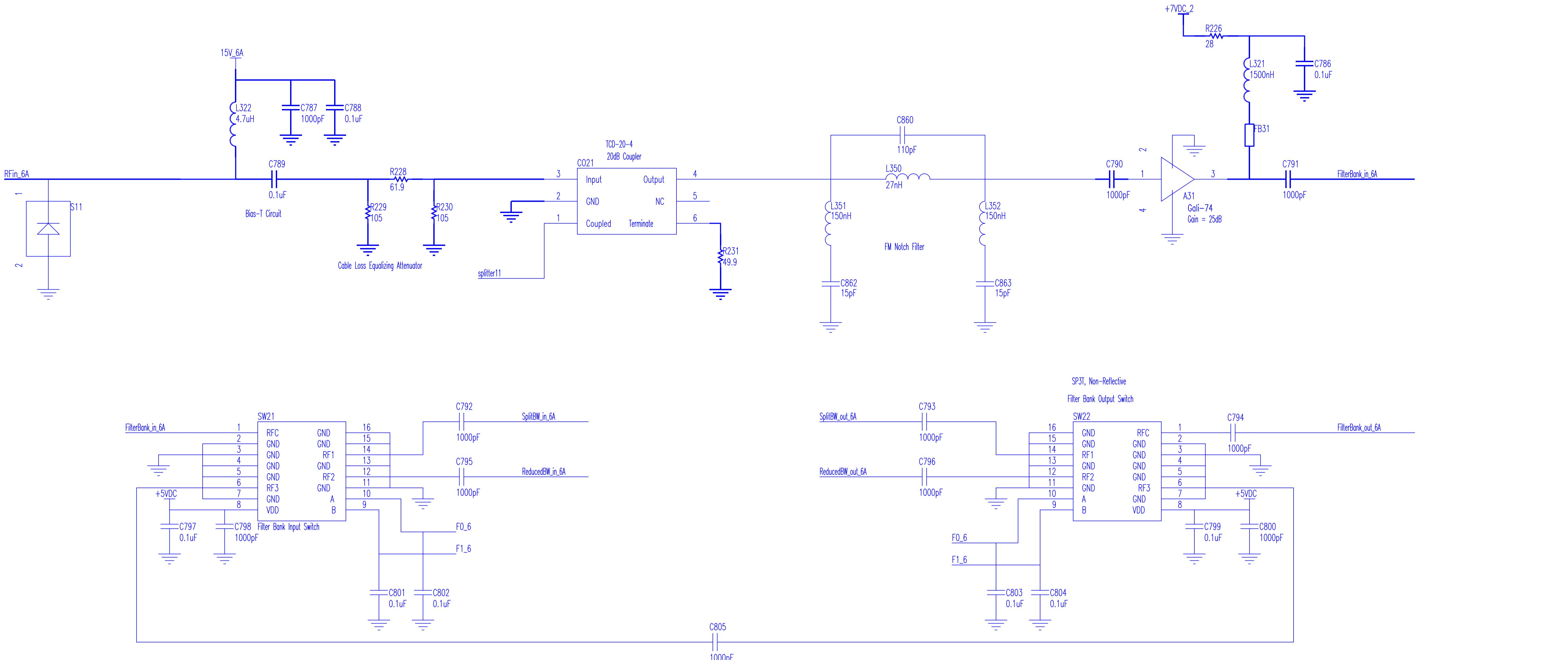


Title		
Size B	Number	Revision
Date:	7/31/2015	Sheet of
File:	E:\Analog Receiver Board Files\..\ARXch_10_pg2.SchDoc	Drawn By:

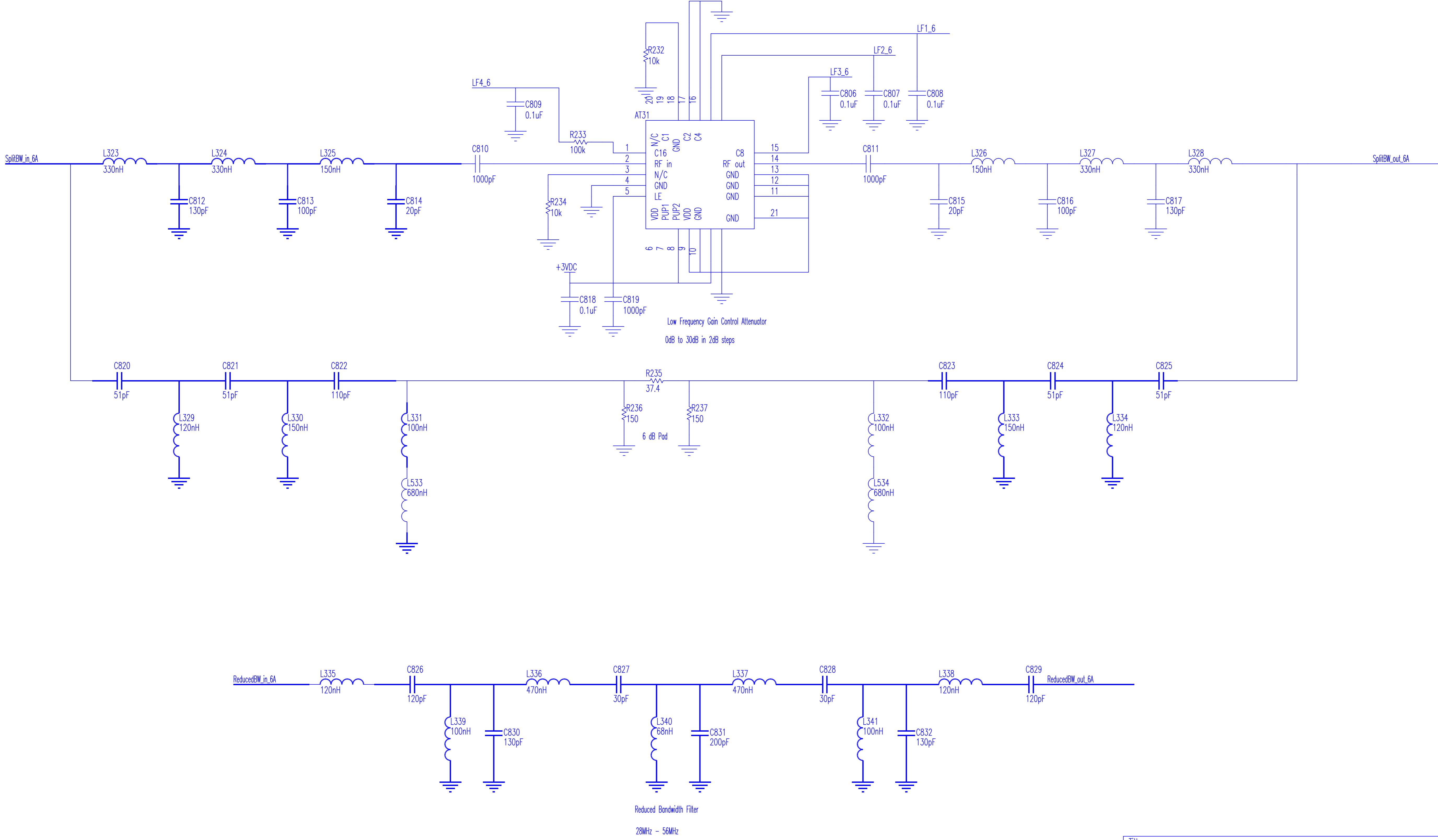
1 2 3 4 5 6



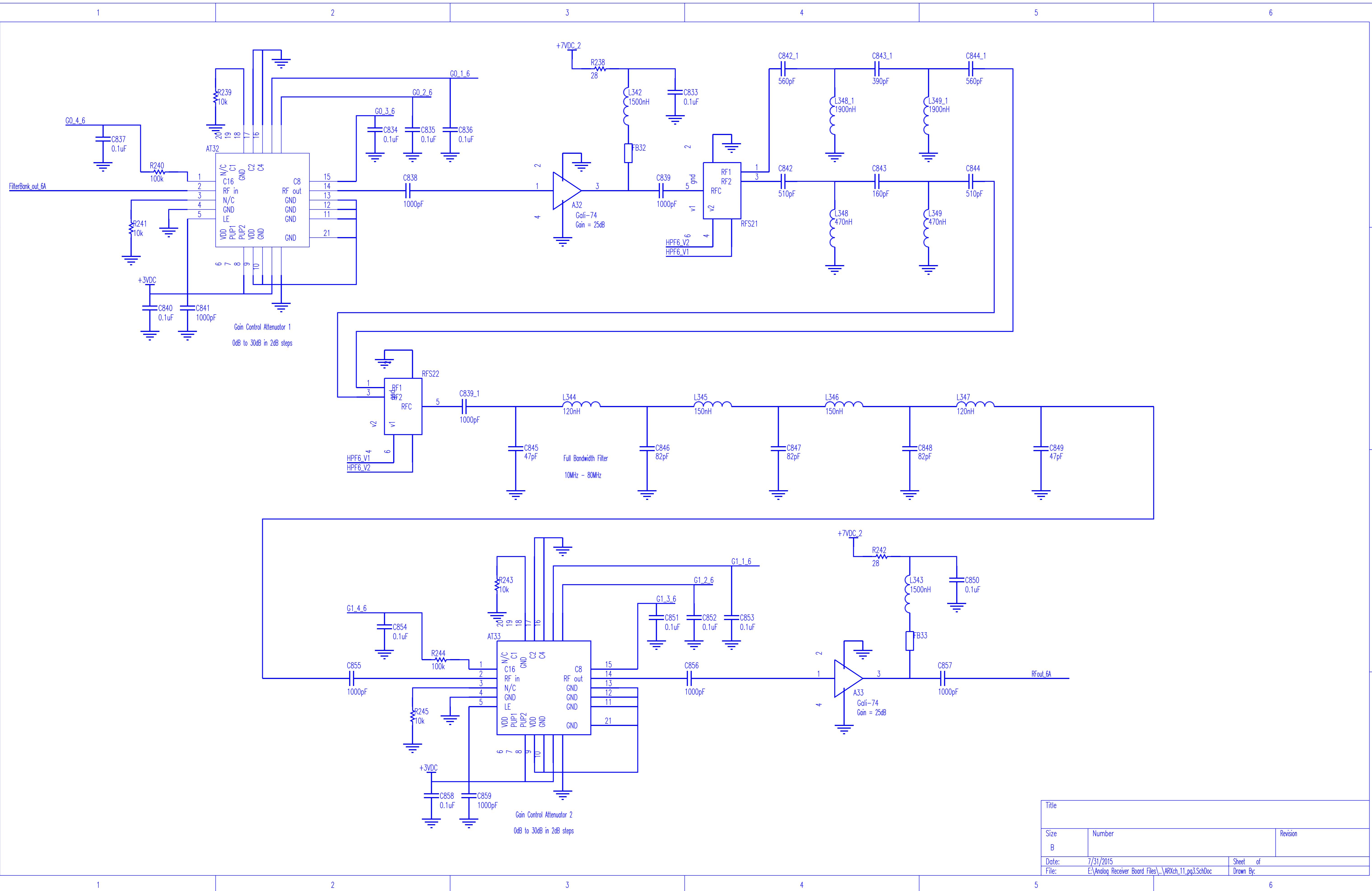
1 2 3 4 5 6



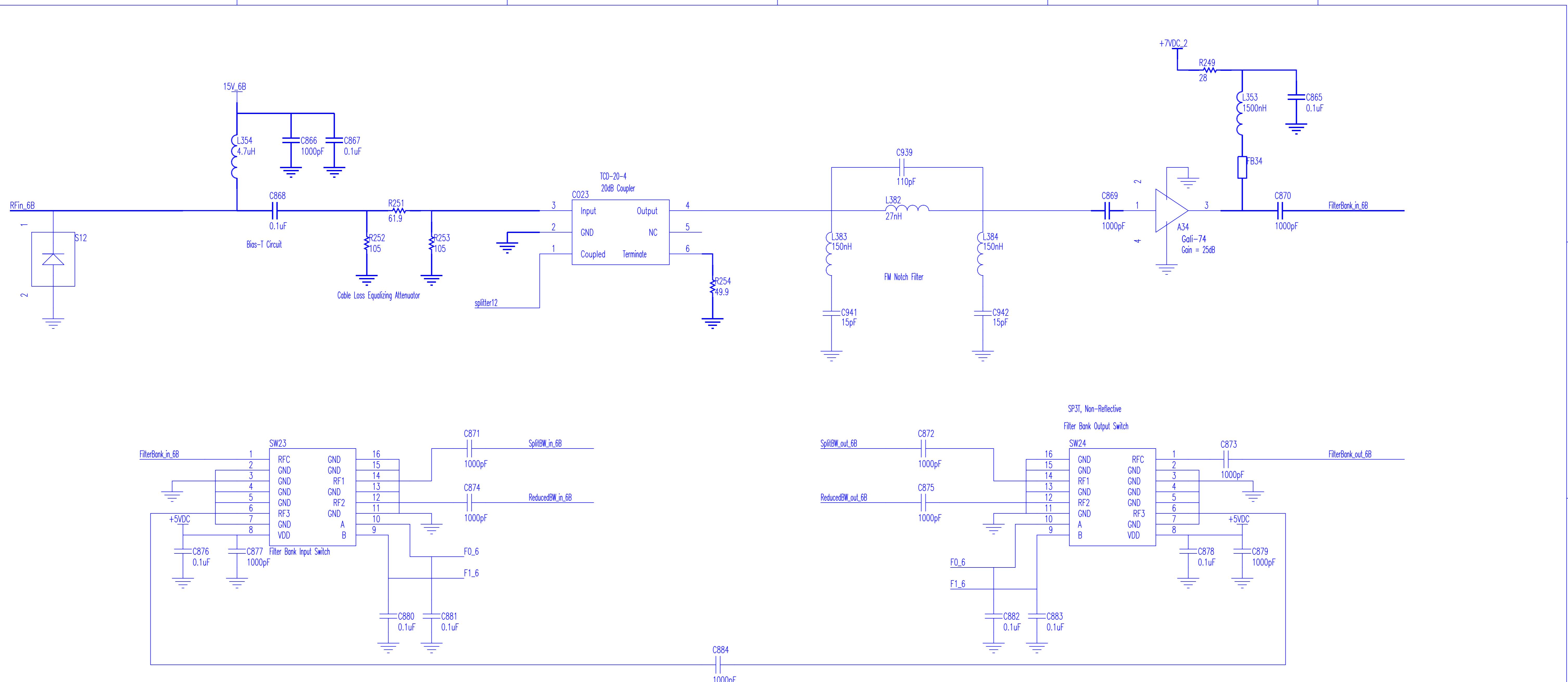
Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\VARXch.11.pg1.SchDoc		Drawn By:



Title		
Size B	Number	Revision
Date:	7/31/2015	Sheet of
File:	E:\Analog Receiver Board Files\..\ARXch_11_pg2.SchDoc	Drawn By:

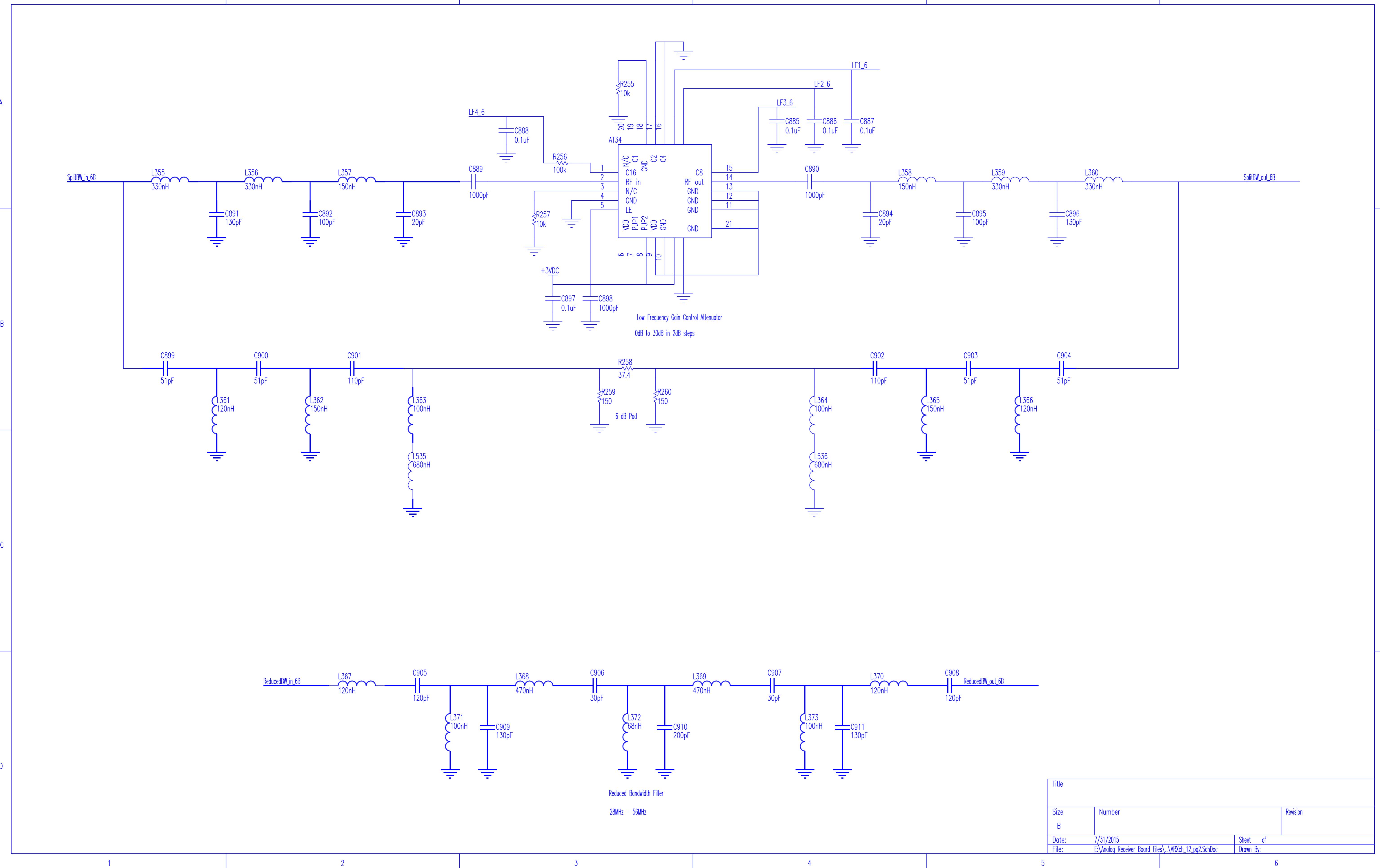


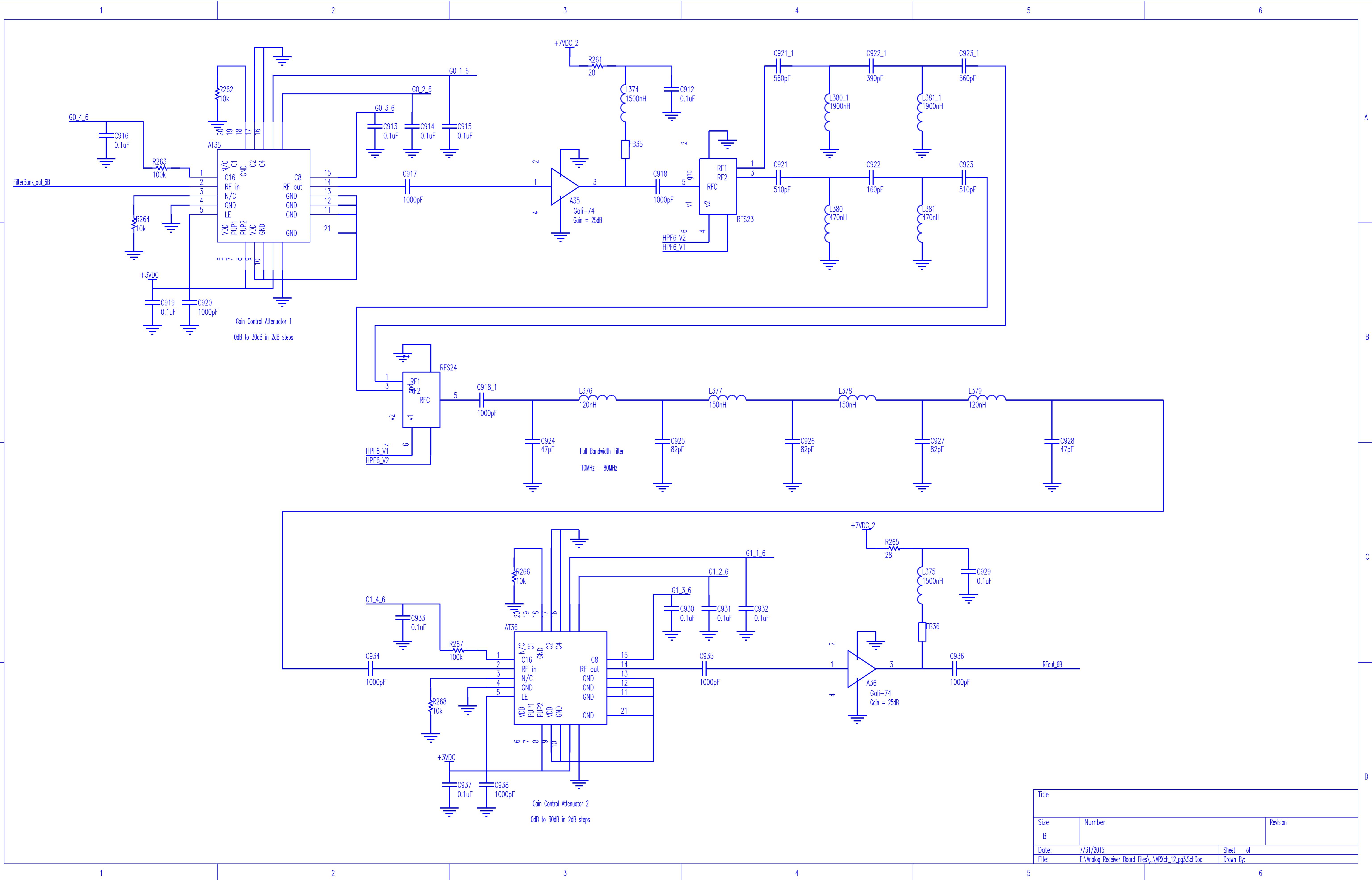
1 2 3 4 5 6



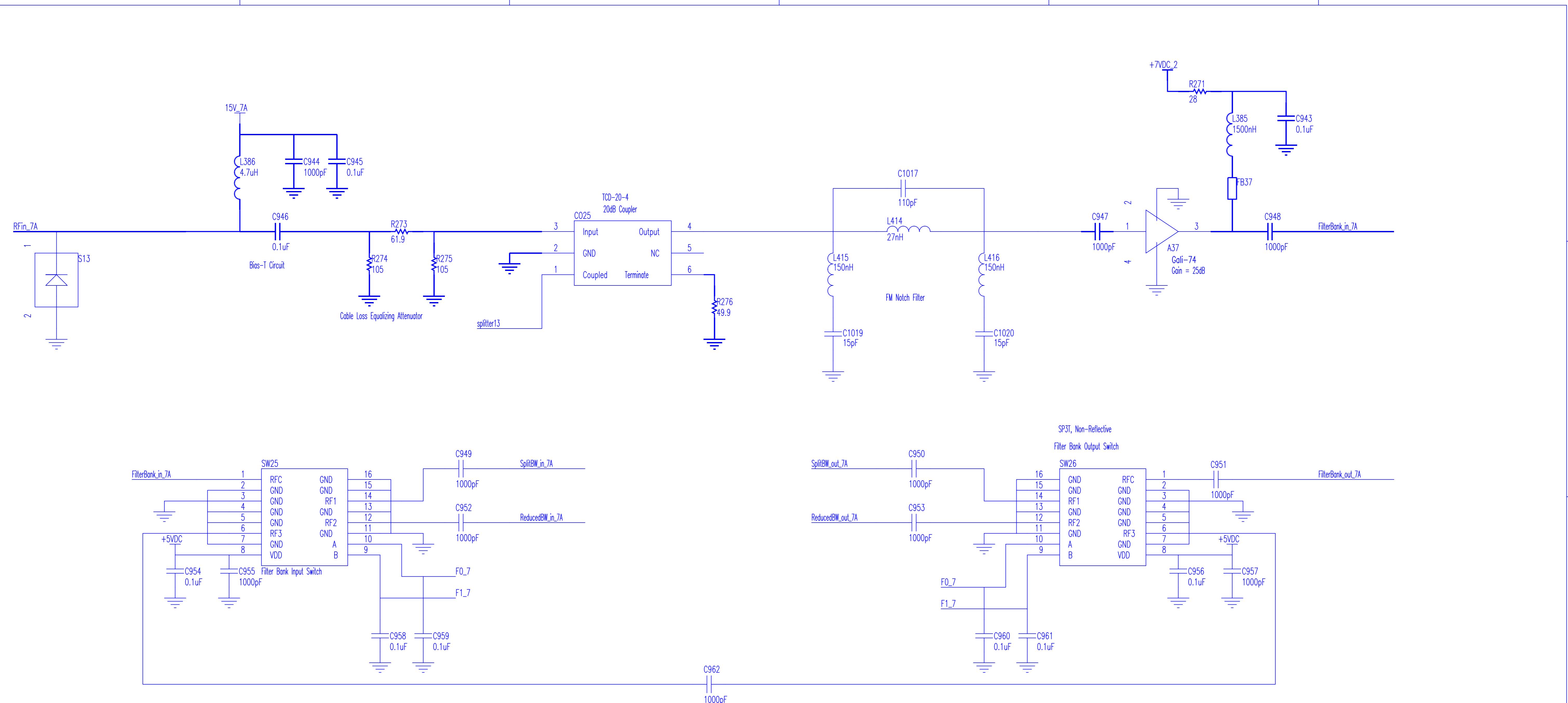
Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\VARXch_12_pg1.SchDoc		Drawn By:

1 2 3 4 5 6





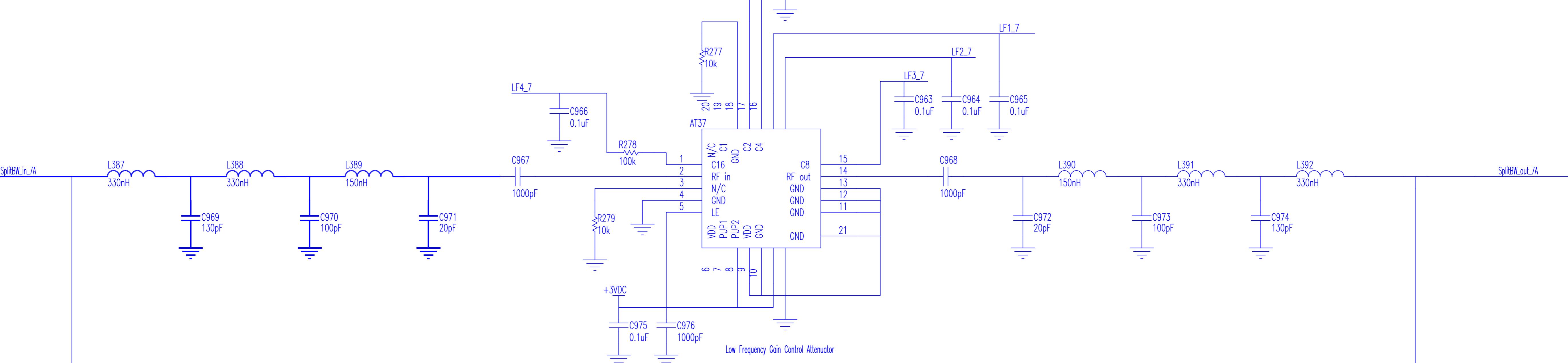
1 2 3 4 5 6



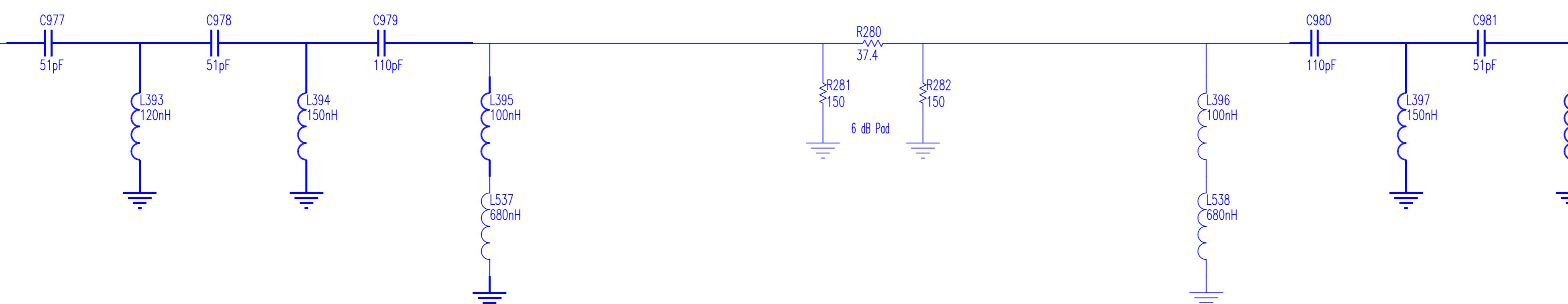
Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\VARXch_13.ppt.SchDoc		Drawn By:

1 2 3 4 5 6

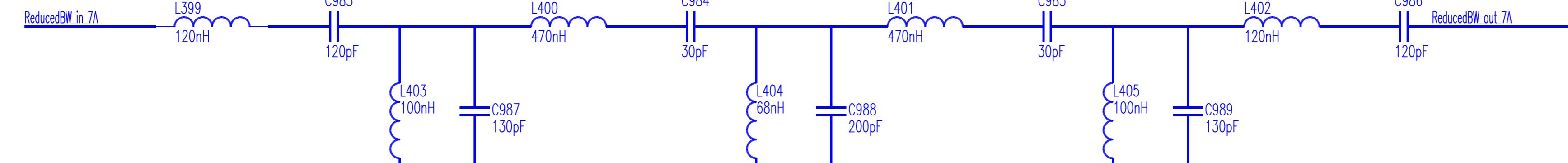
A



B



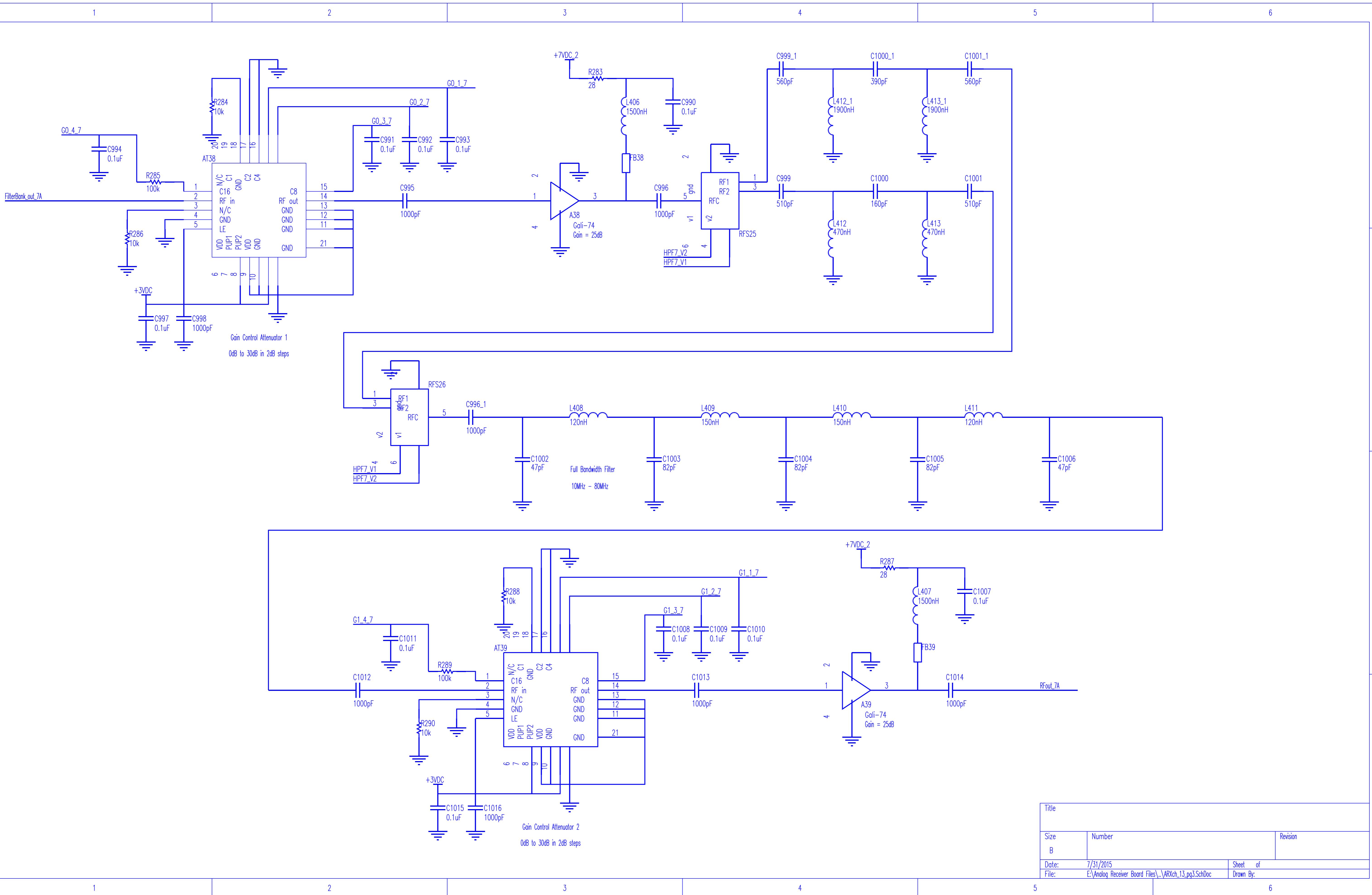
C



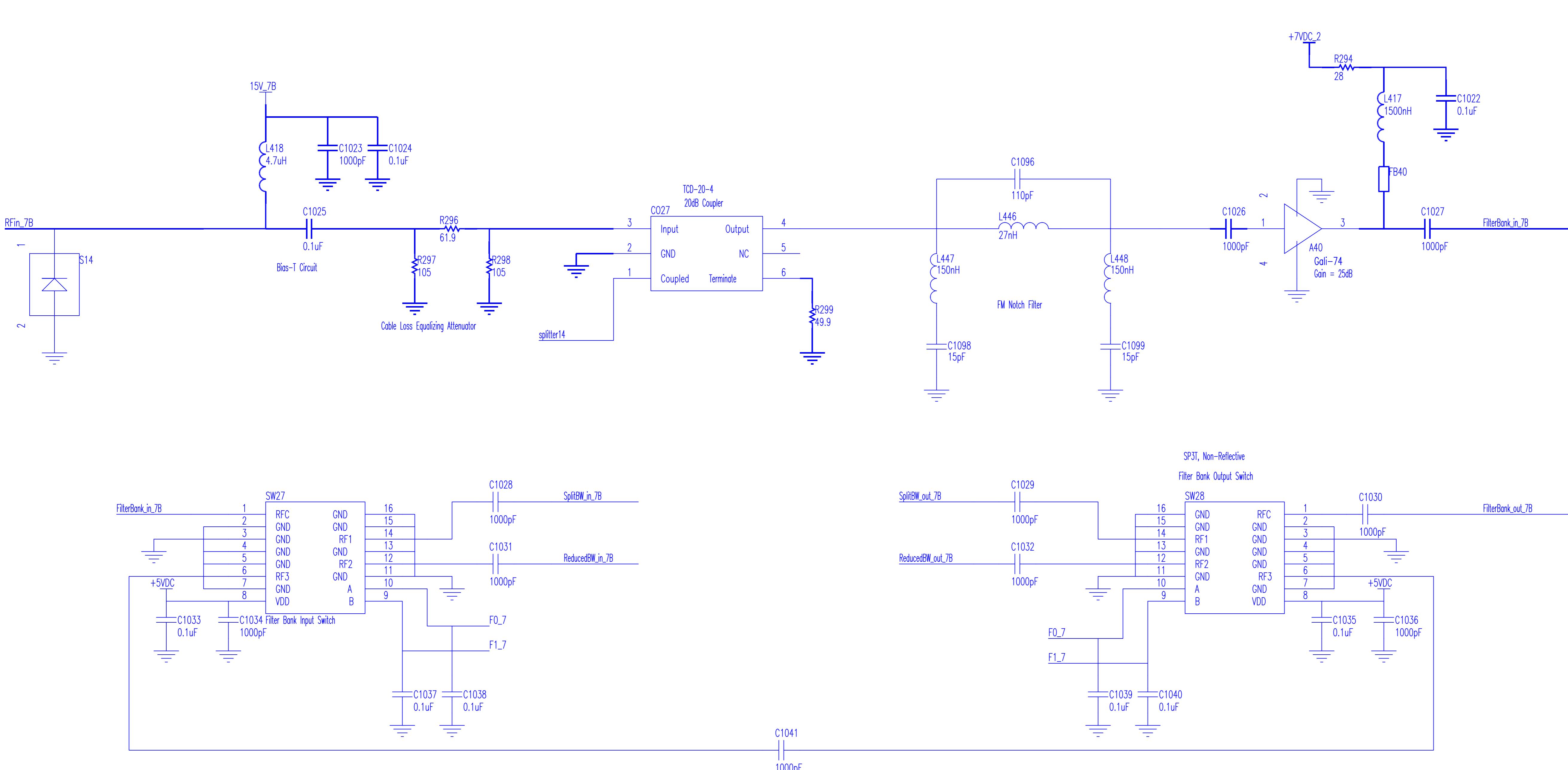
D

Reduced Bandwidth Filter
28MHz - 56MHz

Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\VARXch_13.pq2.SchDoc		Drawn By:

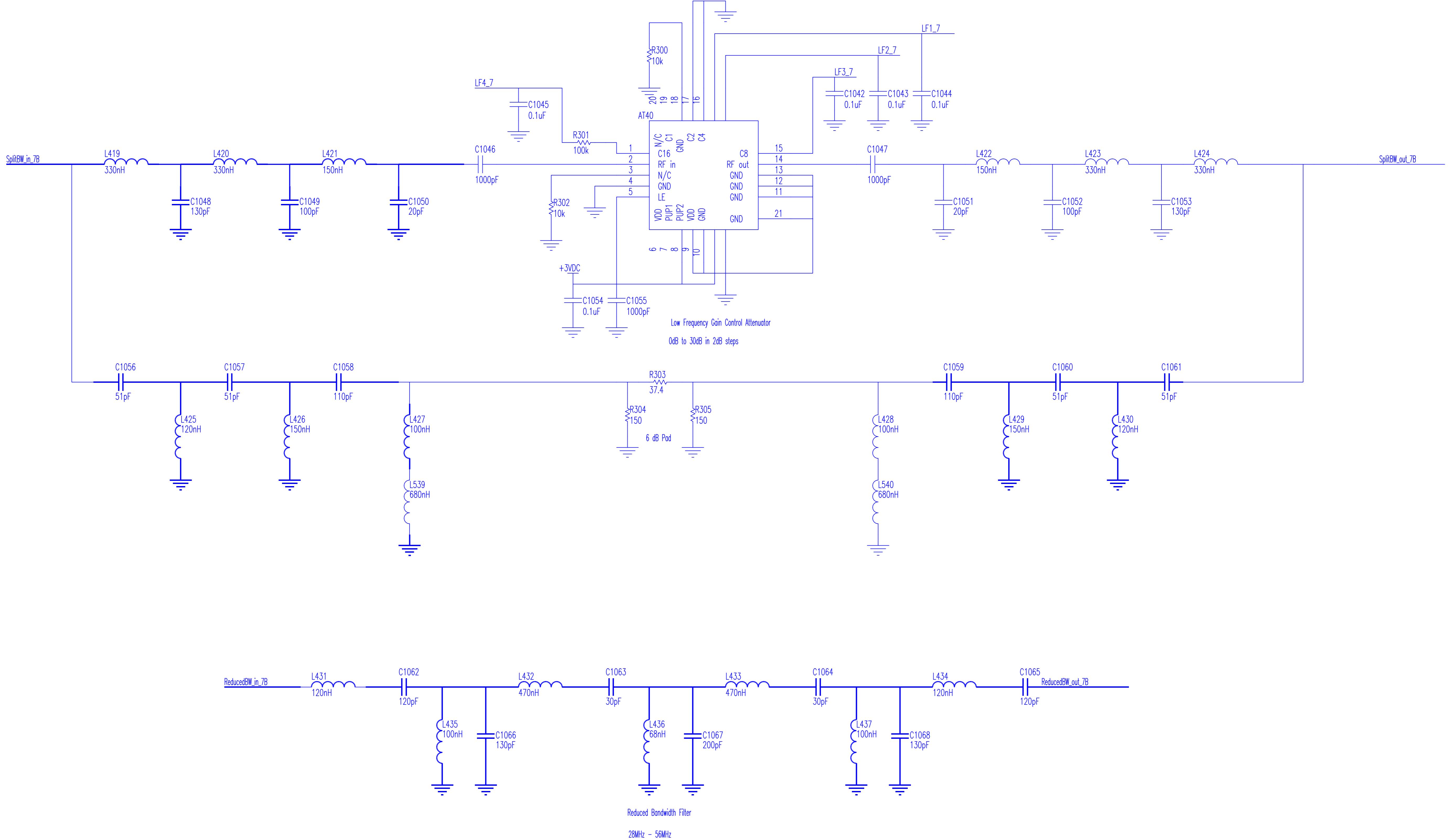


1 2 3 4 5 6

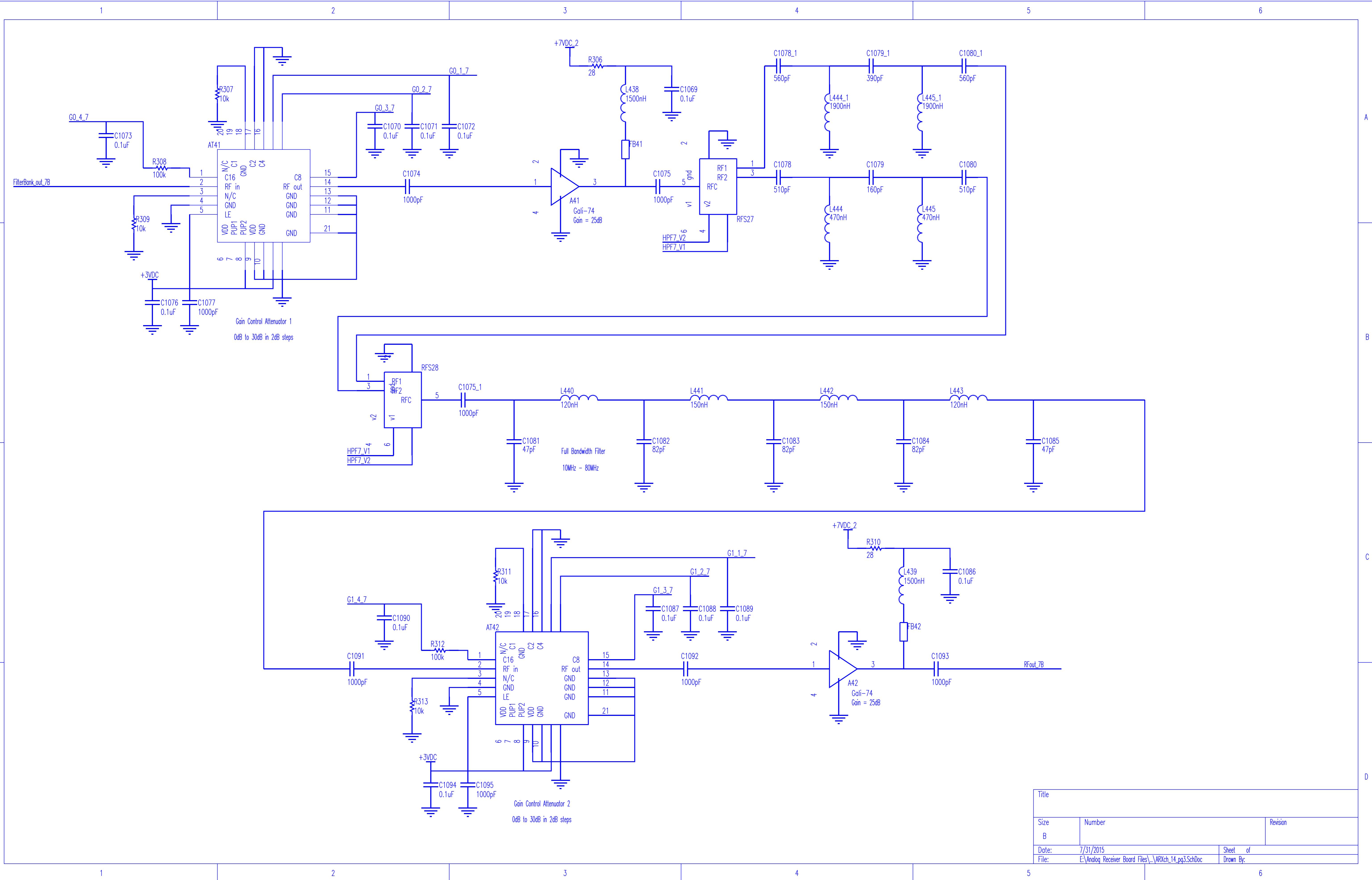


Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\ARXch_14_pg1.SchDoc		Drawn By:

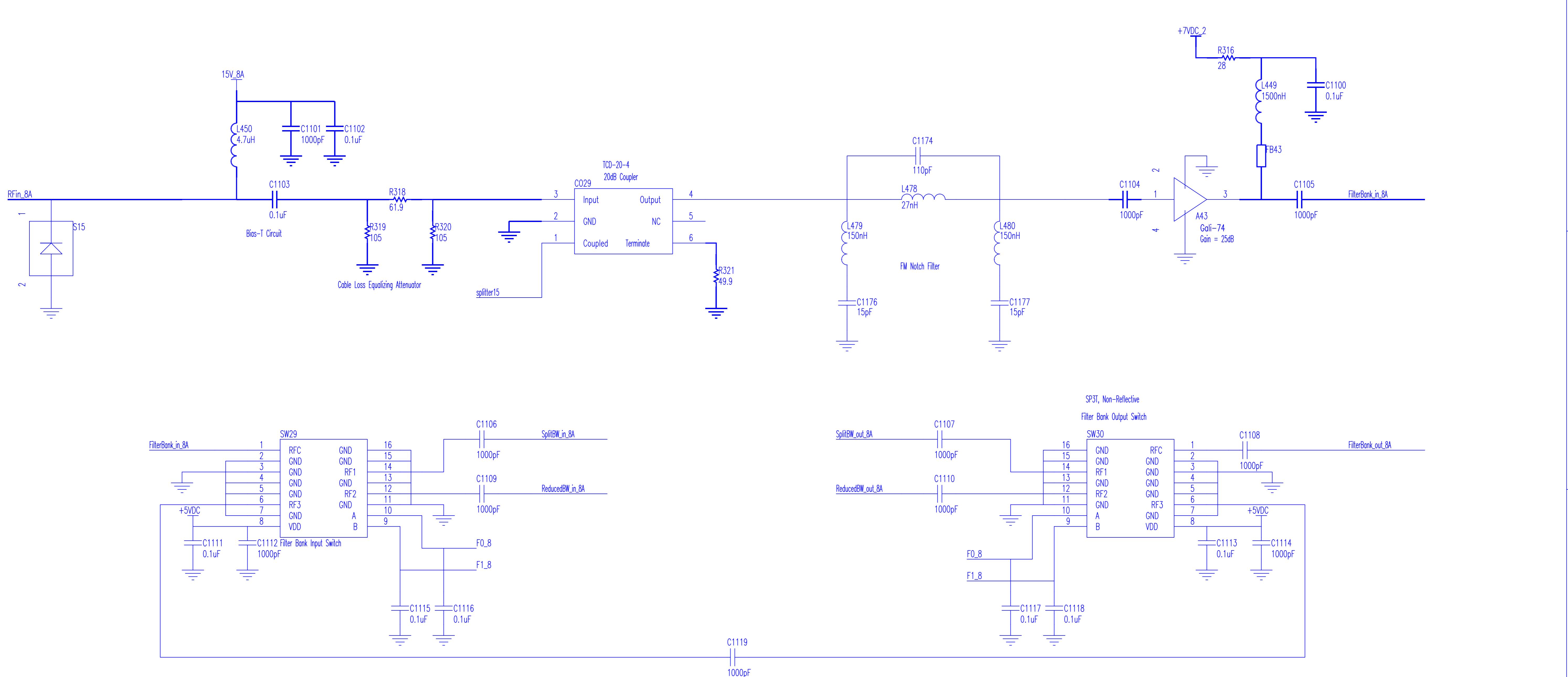
1 2 3 4 5 6



Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\VARXch_14_pg2.SchDoc		Drawn By:



1 2 3 4 5 6



Title		
Size	Number	Revision
B		
Date: 7/31/2015		Sheet of
File: E:\Analog Receiver Board Files\VARXch_15.ppt.SchDoc		Drawn By:

1 2 3 4 5 6

