

Implementing Synchronization Objects

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- ▶ Method: use hardware primitives
 - ▶ Disable interrupt (uniprocessor)
 - ▶ Atomic read-modify-write instructions (multiprocessor)

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 - ▶ CV: queue of waiting threads
- ▶ Method: use hardware primitives
 - ▶ Disable interrupt (uniprocessor)
 - ▶ Atomic read-modify-write instructions (multiprocessor)
- ▶ Kernel mode vs. user mode

Uniprocessor Locks: Disabling Interrupts

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- ▶ Problem
 - ▶ Disable interrupt for a long time (starvation, not real-time)
 - ▶ Cannot allow user-level code to disable interrupts

Implementing Uniprocessor Queueing Locks

- ▶ Briefly disable interrupt to protect lock structure
- ▶ When lock is locked, context switch to another ready thread

Implementing Uniprocessor Queueing Locks

```
1 Lock::acquire() {
2     TCB *chosenTCB;
3
4     disableInterrupts();
5     if (value == BUSY) {
6         waiting.add(runningThread);
7         runningThread->state = WAITING;
8         chosenTCB = readyList.remove();
9         thread_switch(runningThread,
10                      chosenTCB);
11         runningThread->state = RUNNING;
12     } else {
13         value = BUSY;
14     }
15     enableInterrupts();
16 }
```

```
1 Lock::release() {
2     // next thread to hold lock
3     TCB *next;
4
5     disableInterrupts();
6     if (waiting.notEmpty()) {
7         // move one TCB from waiting
8         // to ready
9         next = waiting.remove();
10        next->state = READY;
11        readyList.add(next);
12    } else {
13        value = FREE;
14    }
15    enableInterrupts();
16 }
```

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- Do not set value = FREE in release() to prevent starvation

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12    } else {
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- ▶ Do not set value = FREE in release() to prevent starvation
- ▶ During call to thread_switch, interrupts are turned off

Implementing Multiprocessor Spinlocks

- ▶ Cannot turn off interrupt
- ▶ Use atomic read-modify-write instructions
 - ▶ Implementation: related to cache
 - ▶ Computer Architecture courses (ECS 154B / probably ECS 201)
 - ▶ "What Every Programmer Should Know About Memory" Figure 3.18

Implementing Multiprocessor Spinlocks

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- ▶ Use atomic read-modify-write instructions
 - ▶ Implementation: related to cache
 - ▶ Computer Architecture courses (ECS 154B / probably ECS 201)
 - ▶ "What Every Programmer Should Know About Memory" Figure 3.18
- ▶ Atomic test-and-set instruction

```
1  int test_and_set(int* lockPtr, int newValue) {  
2      int oldValue;  
3      oldValue = *lockPtr;  
4      *lockPtr = newValue;  
5      return oldValue;  
6  }
```

Implementing Multiprocessor Spinlocks

```
1  class SpinLock {
2      private:
3          int value = 0; // 0 = FREE; 1 = BUSY
4
5      public:
6          void acquire() {
7              while (test_and_set(&value)) // while BUSY
8                  ; // spin
9          }
10
11         void release() {
12             value = 0;
13             memory_barrier();
14         }
15     }
```

Implementing Multiprocessor Spinlocks

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1  class SpinLock {
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11         void release() {
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- Busy wait (assume locks are only held shortly)

Implementing Multiprocessor Queueing Locks

- ▶ Critical section length can be long
- ▶ Minimize busy waiting

Implementing Multiprocessor Queueing Locks

- ▶ Critical section length can be long
- ▶ Minimize busy waiting
- ▶ Class definitions

```
1  class Lock {
2      private:
3          int value = FREE;
4          SpinLock spinLock;
5          Queue waiting;
6      public:
7          void acquire();
8          void release();
9  }
10 class Scheduler {
11     private:
12         Queue readyList;
13         SpinLock schedulerSpinLock;
14     public:
15         void suspend(SpinLock *lock);
16         void makeReady(Thread *thread);
17 }
```

Implementing Uniprocessor Queueing Locks

```
1 Lock::acquire() {
2     spinLock.acquire();
3     if (value != FREE) {
4         waiting.add(runningThread);
5         scheduler.suspend(&spinLock);
6         // scheduler releases spinLock
7     } else {
8         value = BUSY;
9         spinLock.release();
10    }
11 }
12 Lock::release() {
13     TCB *next;
14     spinLock.acquire();
15     if (waiting.notEmpty()) {
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1. Call scheduler.suspend without releasing Lock's spinLock

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► Suspending a thread

1. Call scheduler.suspend without releasing Lock's spinLock
2. disableInterrupts() to prevent thread being preempted

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► Suspending a thread

1. Call scheduler.suspend without releasing Lock's spinLock
2. disableInterrupts() to prevent thread being preempted
3. Acquire Scheduler's spinLock to protect readyList

Implementing Uniprocessor Queueing Locks

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1. After release spinLock

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► What if we release Lock's spinLock before calling scheduler.suspend?

1. After release spinLock
2. Another thread release lock, make this thread READY

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```

► What if we release Lock's spinLock before calling scheduler.suspend?

1. After release spinLock
2. Another thread release lock, make this thread READY
3. Current thread calls suspend(), state = WAITING forever

Linux 2.6 Kernel Mutex Lock

- ▶ Optimized for the common case
- ▶ Assumption: most locks are **FREE** most of the time

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- ▶ Optimized for the common case
- ▶ Assumption: most locks are FREE most of the time
- ▶ Acquire: fast path when lock is not already acquired
- ▶ Release: fast path when no waiters on the lock

Implementing Condition Variables

- Similar to implementing locks

```
1  class CV {
2      private:
3          Queue waiting;
4      public:
5          void wait(Lock *lock);
6          void signal();
7          void broadcast();
8  }
9
10 void CV::wait(Lock *lock) {
11     assert(lock.isHeld());
12     waiting.add(myTCB);
13     scheduler.suspend(&lock);
14     lock->acquire();
15 }
```

```
1  void CV::signal() {
2      if (waiting.notEmpty()) {
3          thread = waiting.remove();
4          scheduler.makeReady(thread);
5      }
6  }
7
8  void CV::broadcast() {
9      while (waiting.notEmpty()) {
10         thread = waiting.remove();
11         scheduler.makeReady(thread);
12     }
13 }
```

Implementing Condition Variables

- ▶ Similar to implementing locks
- ▶ Still, pass spinLock to scheduler.suspend

```
1  class CV {
2      private:
3          Queue waiting;
4      public:
5          void wait(Lock *lock);
6          void signal();
7          void broadcast();
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Implementing Application-level Synchronization

- ▶ Kernel-Managed Threads
 - ▶ Simple case: place Lock and CV in kernel space, app use syscalls

Implementing Application-level Synchronization

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 - ▶ Simple case: place Lock and CV in kernel space, app use syscalls
 - ▶ Sophisticated case: fast path in user space, slow path in kernel

Implementing Application-level Synchronization

- ▶ **Kernel-Managed Threads**
 - ▶ Simple case: place Lock and CV in kernel space, app use syscalls
 - ▶ Sophisticated case: fast path in user space, slow path in kernel
- ▶ **User-Managed Threads**
 - ▶ Implement most things at user level
 - ▶ Disabling interrupts → temporarily disable upcalls (usually supported by modern OS)

Thank you

Ref: Anderson & Dahlin, Operating Systems - Principles and Practice

Ref: <https://en.wikipedia.org/wiki/Test-and-set>

Thanks: L^AT_EX, Beamer, OBS