

September 2014

LM78XX / LM78XXA 3-Terminal 1 A Positive Voltage Regulator

Features

- Output Current up to 1 A
- Output Voltages: 5, 6, 8, 9, 10, 12, 15, 18, 24 V
- Thermal Overload Protection
- · Short-Circuit Protection
- Output Transistor Safe Operating Area Protection

Description

The LM78XX series of three-terminal positive regulators is available in the TO-220 package and with several fixed output voltages, making them useful in a wide range of applications. Each type employs internal current limiting, thermal shut-down, and safe operating area protection. If adequate heat sinking is provided, they can deliver over 1 A output current. Although designed primarily as fixed-voltage regulators, these devices can be used with external components for adjustable voltages and currents.



Ordering Information(1)

Product Number	Output Voltage Tolerance	Package	Operating Temperature	Packing Method
LM7805CT				
LM7806CT				
LM7808CT				
LM7809CT				
LM7810CT	±4%		-40°C to +125°C	
LM7812CT				
LM7815CT		TO-220		Rail
LM7818CT		(Single Gauge)		Naii
LM7824CT				
LM7805ACT				
LM7809ACT				
LM7810ACT	±2%		0°C to +125°C	
LM7812ACT				
LM7815ACT				

Note:

1. Above output voltage tolerance is available at 25°C.

Block Diagram

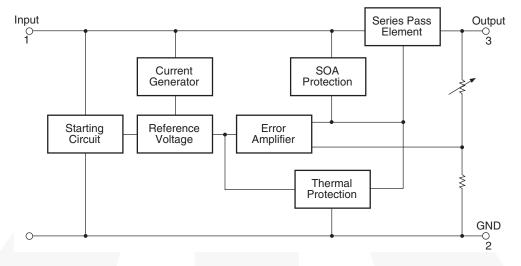


Figure 1. Block Diagram

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parame	ter	Value	Unit
	lanut Valtana	V _O = 5 V to 18 V	35	V
V _I		V _O = 24 V	40	7 V
$R_{\theta JC}$	Thermal Resistance, Junction-Case	nermal Resistance, Junction-Case (TO-220)		
$R_{\theta JA}$	Thermal Resistance, Junction-Air (T	O-220)	65	°C/W
т	Operating Temperature Bange	LM78xx	-40 to +125	- °C
T _{OPR}	Operating Temperature Range	LM78xxA	0 to +125] [
T _{STG}	Storage Temperature Range	<u> </u>	- 65 to +150	°C

Electrical Characteristics (LM7805)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 10 V, C_I = 0.1 μF , unless otherwise specified.

Symbol	Parameter	(Conditions		Тур.	Max.	Unit
		T _J = +25°C		4.80	5.00	5.20	
V _O	Output Voltage	$I_{O} = 5 \text{ mA to}$ $V_{I} = 7 \text{ V to } 20$	1 A, P _O ≤ 15 W,	4.75	5.00	5.25	V
Regline	Line Regulation ⁽²⁾	T _{.J} = +25°C	V _I = 7 V to 25 V		4.0	100.0	mV
Regilile	Line Regulation 7	1j = +25 C	V _I = 8 V to 12 V		1.6	50.0	IIIV
Poglood	Load Regulation ⁽²⁾	T _{.J} = +25°C	$I_{O} = 5 \text{ mA to } 1.5 \text{ A}$		9.0	100.0	m\/
Regload	Load Regulation 7	1j = +25 C	$I_{O} = 250 \text{ mA to } 750 \text{ mA}$		4.0	50.0	mV
IQ	Quiescent Current	T _J = +25°C	T _J = +25°C		5	8	mA
Al	Quiescent Current	$I_O = 5 \text{ mA to}$	1 A		0.03	0.50	mA
ΔI_{Q}	Change	$V_1 = 7 \text{ V to } 25$	V		0.30	1.30	ША
$\Delta V_{O}/\Delta T$	Output Voltage Drift ⁽³⁾	$I_O = 5 \text{ mA}$			-0.8		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, $T_A = +25^{\circ}C$		42		μV
RR	Ripple Rejection ⁽³⁾	f = 120 Hz, V	= 8 V to 18 V	62	73		dB
V_{DROP}	Dropout Voltage	$T_J = +25^{\circ}C, I_0$	_O = 1 A		2		V
R _O	Output Resistance ⁽³⁾	f = 1 kHz			15		mΩ
I _{SC}	Short-Circuit Current	$T_J = +25^{\circ}C$, V	/ _I = 35 V		230		mA
I _{PK}	Peak Current ⁽³⁾	$T_J = +25^{\circ}C$		•	2.2		Α

- 2. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
- 3. These parameters, although guaranteed, are not 100% tested in production.

Electrical Characteristics (LM7806)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 11 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	C	Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C	$T_J = +25^{\circ}C$		6.00	6.25	
V _O	Output Voltage	$I_O = 5 \text{ mA to } 2000 \text{ M}$ $V_I = 8.0 \text{ V to } 2000 \text{ M}$	1 A, P _O ≤ 15 W, 21 V	5.70	6.00	6.30	V
Regline	Line Regulation ⁽⁴⁾	T _J = +25°C	V _I = 8 V to 25 V		5.0	120.0	mV
Regilile	Line Regulation	11 = +23 C	V _I = 9 V to 13 V		1.5	60.0	IIIV
Regload	Load Regulation ⁽⁴⁾	T _{.J} = +25°C	$I_{O} = 5 \text{ mA to } 1.5 \text{ A}$		9.0	120.0	mV
Regioad	Load Regulation 7	1j = +25 C	$I_{O} = 250 \text{ mA to } 750 \text{ mA}$		3.0	60.0	IIIV
ΙQ	Quiescent Current	$T_J = +25^{\circ}C$	$T_J = +25^{\circ}C$		5	8	mA
Al	Quiescent Current	$I_O = 5 \text{ mA to } 1$	1 A			0.5	mA
ΔI_{Q}	Change	V _I = 8 V to 25	V			1.3	IIIA
$\Delta V_{O}/\Delta T$	Output Voltage Drift ⁽⁵⁾	$I_O = 5 \text{ mA}$			-0.8		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		45		μV
RR	Ripple Rejection ⁽⁵⁾	f = 120 Hz, V _I	= 8 V to 18 V	62	73		dB
V _{DROP}	Dropout Voltage	$T_J = +25^{\circ}C, I_C$	_O = 1 A		2		V
R _O	Output Resistance ⁽⁵⁾	f = 1 kHz			19		mΩ
I _{SC}	Short-Circuit Current	$T_J = +25^{\circ}C, V$	_I = 35 V		250		mA
I _{PK}	Peak Current ⁽⁵⁾	$T_J = +25^{\circ}C$			2.2		Α

- 4. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
- 5. These parameters, although guaranteed, are not 100% tested in production.

Electrical Characteristics (LM7808)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 14 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	C	Conditions		Тур.	Max.	Unit
		T _J = +25°C		7.7	8.0	8.3	
Vo	Output Voltage	$I_{O} = 5 \text{ mA to}$ $V_{I} = 10.5 \text{ V to}$	1 A, P _O ≤ 15 W, 23 V	7.6	8.0	8.4	V
Regline	Line Regulation ⁽⁶⁾	T _J = +25°C	V _I = 10.5 V to 25 V		5	160	mV
Regilile	Line Regulation	11 = +23 C	V _I = 11.5 V to 17 V		2	80	1111
Doglood	Load Regulation ⁽⁶⁾	T _{.J} = +25°C	$I_{O} = 5 \text{ mA to } 1.5 \text{ A}$		10	160	mV
Regload	Load Regulation 7	1j = +25 C	$I_{O} = 250 \text{ mA to } 750 \text{ mA}$		5	80	IIIV
IQ	Quiescent Current	T _J = +25°C	T _J = +25°C		5	8	mA
Al	Quiescent Current I _O = 5 mA	$I_O = 5 \text{ mA to}$	1 A		0.05	0.50	m ^
ΔI_{Q}	Change	V _I = 10.5 V to	25 V		0.5	1.0 mA	
$\Delta V_O/\Delta T$	Output Voltage Drift ⁽⁷⁾	$I_O = 5 \text{ mA}$			-0.8		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		52		μV
RR	Ripple Rejection ⁽⁷⁾	f = 120 Hz, V	= 11.5 V to 21.5 V	56	73		dB
V _{DROP}	Dropout Voltage	$I_{O} = 1 A, T_{J} =$	+25°C		2		V
R _O	Output Resistance ⁽⁷⁾	f = 1 kHz			17		mΩ
I _{SC}	Short-Circuit Current	$V_{I} = 35 \text{ V}, T_{J}$	= +25°C		230		mA
I _{PK}	Peak Current ⁽⁷⁾	$T_J = +25^{\circ}C$			2.2		Α

- 6. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
- 7. These parameters, although guaranteed, are not 100% tested in production.

Electrical Characteristics (LM7809)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 15 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	C	Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C	$T_J = +25^{\circ}C$		9.00	9.35	
V _O	Output Voltage	$I_O = 5 \text{ mA to}$ $V_I = 11.5 \text{ V to}$	1 A, P _O ≤ 15 W, 24 V	8.60	9.00	9.40	V
Regline	Line Regulation ⁽⁸⁾	T _J = +25°C	V _I = 11.5 V to 25 V		6	180	mV
Regilile	Line Regulation	11 = +23 C	V _I = 12 V to 17 V		2	90	IIIV
Regload	Load Regulation ⁽⁸⁾	T _{.J} = +25°C	$I_{O} = 5 \text{ mA to } 1.5 \text{ A}$		12	180	mV
Regioad	Load Regulation 7	1j = +25 C	$I_{O} = 250 \text{ mA to } 750 \text{ mA}$		4	90	IIIV
ΙQ	Quiescent Current	T _J = +25°C	T _J = +25°C		5	8	mA
Al	Quiescent Current	$I_O = 5 \text{ mA to}$	1 A			0.5	mA
ΔI_{Q}	Change	V _I = 11.5 V to	26 V			1.3	IIIA
$\Delta V_{O}/\Delta T$	Output Voltage Drift ⁽⁹⁾	$I_O = 5 \text{ mA}$			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		58		μV
RR	Ripple Rejection ⁽⁹⁾	f = 120 Hz, V	= 13 V to 23 V	56	71		dB
V _{DROP}	Dropout Voltage	$I_{O} = 1 A, T_{J} =$	+25°C		2		V
R _O	Output Resistance ⁽⁹⁾	f = 1 kHz			17		mΩ
I _{SC}	Short-Circuit Current	$V_{I} = 35 \text{ V}, T_{J}$	= +25°C		250		mA
I _{PK}	Peak Current ⁽⁹⁾	$T_J = +25^{\circ}C$			2.2		Α

- 8. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
- 9. These parameters, although guaranteed, are not 100% tested in production.

Electrical Characteristics (LM7810)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 16 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	C	Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C	T _J = +25°C		10.0	10.4	
V _O	Output Voltage	$I_O = 5 \text{ mA to } 2000 \text{ M}$ $V_I = 12.5 \text{ V to } 2000 \text{ M}$	1 A, P _O ≤ 15 W, 25 V	9.5	10.0	10.5	V
Regline	Line Regulation ⁽¹⁰⁾	T _{.1} = +25°C	V _I = 12.5 V to 25 V		10	200	mV
Regilile	Line Regulation	11 - +25 C	V _I = 13 V to 25 V		3	100	IIIV
Regload	Load Regulation ⁽¹⁰⁾	T _{.J} = +25°C	$I_{O} = 5 \text{ mA to } 1.5 \text{ A}$		12	200	mV
Regioad	Load Regulation 7	1j = +25 C	$I_{O} = 250 \text{ mA to } 750 \text{ mA}$		4	400	IIIV
ΙQ	Quiescent Current	$T_J = +25^{\circ}C$	$T_J = +25^{\circ}C$		5.1	8.0	mA
Al	Quiescent Current	$I_O = 5 \text{ mA to } 1$	1 A			0.5	mA
ΔI_{Q}	Change	$V_{I} = 12.5 \text{ V to}$	29 V			1.0	ША
$\Delta V_{O}/\Delta T$	Output Voltage Drift ⁽¹¹⁾	$I_O = 5 \text{ mA}$			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		58		μV
RR	Ripple Rejection ⁽¹¹⁾	f = 120 Hz, V _I	= 13 V to 23 V	56	71		dB
V _{DROP}	Dropout Voltage	$I_{O} = 1 A, T_{J} =$	+25°C		2		V
R _O	Output Resistance ⁽¹¹⁾	f = 1 kHz			17		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _J	= +25°C		250		mA
I _{PK}	Peak Current ⁽¹¹⁾	$T_J = +25^{\circ}C$			2.2		Α

- 10. Load and line regulation are specified at constant junction temperature. Changes in V_0 due to heating effects must be taken into account separately. Pulse testing with low duty is used.
- 11. These parameters, although guaranteed, are not 100% tested in production.

Electrical Characteristics (LM7812)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 19 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	C	Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C	$T_J = +25^{\circ}C$		12.0	12.5	V
V _O	Output Voltage		$_{O}$ = 5 mA to 1 A, $P_{O} \le 15$ W, I_{I} = 14.5 V to 27 V	12.0	12.6		
Regline	Line Regulation ⁽¹²⁾	T _{.1} = +25°C	V _I = 14.5 V to 30 V		10	240	mV
Regime	Line Regulation	1j = +25 C	V _I = 16 V to 22 V		3	120	1111
Regload	Load Regulation ⁽¹²⁾	T _{.1} = +25°C	$I_{O} = 5 \text{ mA to } 1.5 \text{ A}$		11	240 mV	m\/
Regioau	Load Regulation 7	1 _J = +25 C	$I_{O} = 250 \text{ mA to } 750 \text{ mA}$		5	120	IIIV
IQ	Quiescent Current	$T_J = +25^{\circ}C$	$T_{J} = +25^{\circ}C$		5.1	8.0	mA
AI	Quiescent Current	$I_O = 5 \text{ mA to } 1$	1 A		0.1	0.5	mA
ΔI_{Q}	Change	V _I = 14.5 V to	30 V		0.5	1.0	IIIA
$\Delta V_{O}/\Delta T$	Output Voltage Drift ⁽¹³⁾	I _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		76		μV
RR	Ripple Rejection ⁽¹³⁾	f = 120 Hz, V _I	= 15 V to 25 V	55	71		dB
V_{DROP}	Dropout Voltage	$I_{O} = 1 A, T_{J} =$	+25°C		2		V
R _O	Output Resistance ⁽¹³⁾	f = 1 kHz			18		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _J	= +25°C		230		mA
I _{PK}	Peak Current ⁽¹³⁾	$T_J = +25^{\circ}C$			2.2		Α

- 12. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
- 13. These parameters, although guaranteed, are not 100% tested in production.

Electrical Characteristics (LM7815)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 23 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	C	Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		14.40	15.00	15.60	
V _O	Output Voltage	$I_O = 5 \text{ mA to } 2000 \text{ M}$ $V_I = 17.5 \text{ V to } 2000 \text{ M}$	1 A, P _O ≤ 15 W, 30 V	14.25	15.00	00 15.75	V
Regline	Line Regulation ⁽¹⁴⁾	T _{.J} = +25°C	V _I = 17.5 V to 30 V		11	300	mV
Regilile	Line Regulation	11 - +25 C	V _I = 20 V to 26 V		3	150	IIIV
Regload	Load Regulation ⁽¹⁴⁾	T _{.J} = +25°C	$I_{O} = 5 \text{ mA to } 1.5 \text{ A}$		12	300	mV
Regioad	Load Regulation 7	1j = +25 C	$I_{O} = 250 \text{ mA to } 750 \text{ mA}$		4	150	IIIV
ΙQ	Quiescent Current	$T_J = +25^{\circ}C$	$T_J = +25^{\circ}C$		5.2	8.0	mA
Al	Quiescent Current	$I_O = 5 \text{ mA to } ^{\circ}$	1 A			0.5	mA
ΔI_{Q}	Change	V _I = 17.5 V to	30 V			1.0	IIIA
$\Delta V_{O}/\Delta T$	Output Voltage Drift ⁽¹⁵⁾	I _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		90		μV
RR	Ripple Rejection ⁽¹⁵⁾	f = 120 Hz, V _I	= 18.5 V to 28.5 V	54	70		dB
V _{DROP}	Dropout Voltage	$I_{O} = 1 A, T_{J} =$	+25°C		2		V
R _O	Output Resistance ⁽¹⁵⁾	f = 1 kHz			19		mΩ
I _{SC}	Short-Circuit Current	$V_{I} = 35 \text{ V}, T_{J} = 35 \text{ V}$	= +25°C		250		mA
I _{PK}	Peak Current ⁽¹⁵⁾	$T_J = +25^{\circ}C$			2.2		Α

- 14. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
- 15. These parameters, although guaranteed, are not 100% tested in production.

Electrical Characteristics (LM7818)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 27 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	C	Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C	T _J = +25°C		18.0	18.7	
V _O	Output Voltage	$I_0 = 5 \text{ mA to } 1000$ $V_1 = 21 \text{ V to } 300$	1 A, P _O ≤ 15 W, 3 V	17.1	18.0	18.9	V
Regline	Line Regulation ⁽¹⁶⁾	T _{.1} = +25°C	V _I = 21 V to 33 V		15	360	mV
Regilile	Line Regulation	11 - +25 C	V _I = 24 V to 30 V		5	180	IIIV
Regload	Load Regulation ⁽¹⁶⁾	T _{.J} = +25°C	$I_{O} = 5 \text{ mA to } 1.5 \text{ A}$		15	360 mV	m\/
Regioad	Load Regulation 7	1j = +25 C	$I_{O} = 250 \text{ mA to } 750 \text{ mA}$		5	180	IIIV
ΙQ	Quiescent Current	$T_J = +25^{\circ}C$	$T_J = +25^{\circ}C$		5.2	8.0	mA
Al	Quiescent Current	$I_O = 5 \text{ mA to } 1$	1 A			0.5	mA
ΔI_{Q}	Change	V _I = 21 V to 3	3 V			1.0	IIIA
$\Delta V_{O}/\Delta T$	Output Voltage Drift ⁽¹⁷⁾	$I_O = 5 \text{ mA}$			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		110		μV
RR	Ripple Rejection ⁽¹⁷⁾	f = 120 Hz, V _I	= 22 V to 32 V	53	69		dB
V _{DROP}	Dropout Voltage	$I_{O} = 1 A, T_{J} =$	+25°C		2		V
R _O	Output Resistance ⁽¹⁷⁾	f = 1 kHz			22		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _J	= +25°C		250		mA
I _{PK}	Peak Current ⁽¹⁷⁾	$T_J = +25^{\circ}C$			2.2		Α

- 16. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
- 17. These parameters, although guaranteed, are not 100% tested in production.

Electrical Characteristics (LM7824)

Refer to the test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 33 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	C	Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		23.00	24.00	25.00	
V _O	Output Voltage	$I_O = 5 \text{ mA to 1 A, P}_O \le 15 \text{ W},$ $V_I = 27 \text{ V to 38 V}$ 22.80 24.00	24.00	25.25	V		
Regline	Line Regulation ⁽¹⁸⁾	T _{.1} = +25°C	$V_1 = 27 \text{ V to } 38 \text{ V}$		17	480	mV
Regilile	Line Regulation	11 - +25 C	$V_1 = 30 \text{ V to } 36 \text{ V}$		6	240	IIIV
Regload	Load Regulation ⁽¹⁸⁾	T _{.J} = +25°C	$I_{O} = 5 \text{ mA to } 1.5 \text{ A}$		15	480	mV
Regioad	Load Regulation	1j = +25 C	$I_{O} = 250 \text{ mA to } 750 \text{ mA}$		5	240	1117
ΙQ	Quiescent Current	$T_J = +25^{\circ}C$	T _J = +25°C		5.2	8.0	mA
Al	Quiescent Current	$I_O = 5 \text{ mA to } 1$	1 A		0.1	0.5	mA
ΔI_{Q}	Change	$V_{I} = 27 \text{ V to } 3$	8 V		0.5	1.0	ША
$\Delta V_{O}/\Delta T$	Output Voltage Drift ⁽¹⁹⁾	$I_O = 5 \text{ mA}$			-1.5		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		120		μV
RR	Ripple Rejection ⁽¹⁹⁾	f = 120 Hz, V _I	= 28 V to 38 V	50	67		dB
V _{DROP}	Dropout Voltage	$I_{O} = 1 A, T_{J} =$	+25°C		2		V
R _O	Output Resistance ⁽¹⁹⁾	f = 1 kHz			28		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _J	= +25°C		230		mA
I _{PK}	Peak Current ⁽¹⁹⁾	$T_J = +25^{\circ}C$			2.2		Α

- 18. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
- 19. These parameters, although guaranteed, are not 100% tested in production.

Electrical Characteristics (LM7805A)

Refer to the test circuit, 0° C < T_J < 125° C, I_O = 1 A, V_I = 10 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	C	Conditions	Min.	Тур.	Max.	Unit
		$T_J = +25^{\circ}C$		4.9	5.0	5.1	
V _O	Output Voltage	$I_O = 5 \text{ mA to } 1$ $V_I = 7.5 \text{ V to } 2$	I A, P _O ≤ 15 W, 20 V	4.8	5.0	5.2	V
		$V_{I} = 7.5 \text{ V to } 25 \text{ V}, I_{O} = 500 \text{ mA}$			5.0	50.0	
Dogling	Line Regulation ⁽²⁰⁾	V _I = 8 V to 12	V		3.0	50.0	mV
Regline	Line Regulation	T _J = +25°C	V _I = 7.3 V to 20 V		5.0	50.0	IIIV
		1 _J = +25°C	$V_1 = 7.3 \text{ V to } 20 \text{ V}$ $V_1 = 8 \text{ V to } 12 \text{ V}$		1.5	25.0	
		$T_J = +25^{\circ}C, I_C$	o = 5 mA to 1.5 A		9	100	
Regload	Load Regulation ⁽²⁰⁾	$I_O = 5 \text{ mA to } 1$	I _O = 5 mA to 1 A		9	100	mV
		I _O = 250 mA t	$I_{O} = 250 \text{ mA to } 750 \text{ mA}$		4	50	
IQ	Quiescent Current	$T_J = +25^{\circ}C$			5	6	mA
		I _O = 5 mA to 1 A				0.5	
ΔI_Q	Quiescent Current Change	V _I = 8 V to 25			0.8	mA	
	Onlange	$V_{\rm I} = 7.5 \text{ V to } 20 \text{ V}, T_{\rm J} = +25^{\circ}\text{C}$				0.8	
$\Delta V_{O}/\Delta T$	Output Voltage Drift ⁽²¹⁾	$I_O = 5 \text{ mA}$			-0.8		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		42		μV
RR	Ripple Rejection ⁽²¹⁾	f = 120 Hz, V _O = 500 mA, V _I =8 V to 18 V			68		dB
V _{DROP}	Dropout Voltage	$I_{O} = 1 A, T_{J} =$		2		V	
R _O	Output Resistance ⁽²¹⁾	f = 1 kHz		17		mΩ	
I _{SC}	Short-Circuit Current	V _I = 35 V, T _J =	= +25°C		250		mA
I _{PK}	Peak Current ⁽²¹⁾	$T_J = +25^{\circ}C$			2.2		Α

- 20. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
- 21. These parameters, although guaranteed, are not 100% tested in production.

Electrical Characteristics (LM7809A)

Refer to the test circuit, 0°C < T_J < 125°C, I_O = 1 A, V_I = 15 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit
		T _J = +25°C		8.82	9.00	9.16	V
V _O	Output Voltage	$I_O = 5 \text{ mA to 1 A}, P_O \le 15 \text{ W},$ $V_I = 11.2 \text{ V to 24 V}$		8.65	9.00	9.35	
	Line Regulation ⁽²²⁾	$V_{I} = 11.7 \text{ V to } 25 \text{ V}, I_{O} = 500 \text{ mA}$			6	90	m)/
Regline		V _I = 12.5 V to 19 V			4	45	
Regilile		T .0500	V _I = 11.5 V to 24 V		6	90	- mV
		1 _J = +25 C	$V_I = 11.5 \text{ V to } 24 \text{ V}$ $V_I = 12.5 \text{ V to } 19 \text{ V}$		2	45	
		$T_J = +25^{\circ}\text{C}, I_O = 5 \text{ mA to } 1.5 \text{ A}$			12	100	mV
Regload	Load Regulation ⁽²²⁾	I _O = 5 mA to 1 A			12	100	
		I _O = 250 mA to 750 mA			5	50	
IQ	Quiescent Current	$T_J = +25^{\circ}C$			5	6	mA
	Quiescent Current Change	$I_O = 5 \text{ mA to } ^{\prime}$	1 A			0.5	
ΔI_Q		V _I = 12 V to 25 V, I _O = 500 mA				0.8	mA
		V _I = 11.7 V to 25 V, T _J = +25°C				0.8	
$\Delta V_{O}/\Delta T$	Output Voltage Drift ⁽²³⁾	I _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 100 kHz, T _A = +25°C			58		μV
RR	Ripple Rejection ⁽²³⁾	$f = 120 \text{ Hz}, V_0 = 500 \text{ mA}, V_1 = 12 \text{ V to } 22 \text{ V}$			62		dB
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J = +25°C			2		V
R _O	Output Resistance ⁽²³⁾	f = 1 kHz			17		mΩ
I _{SC}	Short-Circuit Current	$V_I = 35 \text{ V}, T_J = +25^{\circ}\text{C}$			250		mA
I _{PK}	Peak Current ⁽²³⁾	T _J = +25°C			2.2		Α

- 22. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
- 23. These parameters, although guaranteed, are not 100% tested in production.

Electrical Characteristics (LM7810A)

Refer to the test circuit, 0° C < T_J < 125° C, I_O = 1 A, V_I = 16 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit
Vo		T _J = +25°C		9.8	10.0	10.2	V
	Output Voltage	$I_O = 5$ mA to 1 A, $P_O \le 15$ W, $V_I = 12.8$ V to 25 V		9.6	10.0	10.4	
	Line Regulation ⁽²⁴⁾	V _I = 12.8 V to 26 V, I _O = 500 mA			8	100	\/
D !!		V _I = 13 V to 20 V			4	50	
Regline		T = 135°C	V _I = 12.5 V to 25 V		8	100	- mV
		1 _J = +25°C	$V_I = 12.5 \text{ V to } 25 \text{ V}$ $V_I = 13 \text{ V to } 20 \text{ V}$		3	50	
	Load Regulation ⁽²⁴⁾	$T_{J} = +25^{\circ}C, I_{O}$		12	100	mV	
Regload		$I_O = 5 \text{ mA to } 1$		12	100		
		I _O = 250 mA to 750 mA			5		50
IQ	Quiescent Current	$T_J = +25^{\circ}C$			5	6	mA
	Quiescent Current Change	$I_O = 5 \text{ mA to } 1$	A			0.5	
ΔI_{Q}		V _I = 12.8 V to 25 V, I _O = 500 mA				0.8	mA
		V _I = 13 V to 26	V, T _J = +25°C			0.5	
$\Delta V_{O}/\Delta T$	Output Voltage Drift ⁽²⁵⁾	I _O = 5 mA			-1		mV/°C
V_N	Output Noise Voltage	f = 10 Hz to 100 kHz, T _A = +25°C			58		μV
RR	Ripple Rejection ⁽²⁵⁾	$f = 120 \text{ Hz}, V_O = 500 \text{ mA}, V_I = 14 \text{ V to } 24 \text{ V}$			62		dB
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J = +25°C			2		V
R _O	Output Resistance ⁽²⁵⁾	f = 1 kHz			17		mΩ
I _{SC}	Short-Circuit Current	$V_{I} = 35 \text{ V}, T_{J} = +25^{\circ}\text{C}$			250		mA
I _{PK}	Peak Current ⁽²⁵⁾	T _J = +25°C			2.2		Α

- 24. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
- 25. These parameters, although guaranteed, are not 100% tested in production.

Electrical Characteristics (LM7812A)

Refer to the test circuit, 0°C < T_J < 125°C, I_O = 1 A, V_I = 19 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit
		$T_J = +25$ °C		11.75	12.00	12.25	V
Vo	Output Voltage	$I_O = 5$ mA to 1 A, $P_O \le 15$ W, $V_I = 14.8$ V to 27 V		11.50	12.00	12.50	
	Line Regulation ⁽²⁶⁾	$V_I = 14.8 \text{ V to } 30 \text{ V}, I_O = 500 \text{ mA}$			10	120	\/
Doglino		V _I = 16 V to 22 V			4	120	
Regline		T .0500	$V_1 = 14.5 \text{ V to } 27 \text{ V}$ $V_1 = 16 \text{ V to } 22 \text{ V}$		10	120	- mV
		$T_J = +25^{\circ}C$	V _I = 16 V to 22 V		3	60	
		$T_J = +25^{\circ}\text{C}, I_O = 5 \text{ mA to } 1.5 \text{ A}$			12	100	mV
Regload	Load Regulation ⁽²⁶⁾	$I_O = 5 \text{ mA to } 1$		12	100		
		I _O = 250 mA to 750 mA			5	50	
IQ	Quiescent Current	$T_J = +25^{\circ}C$		5	6	mA	
	Quiescent Current Change	$I_O = 5 \text{ mA to } ^{\circ}$	1 A			0.5	
ΔI_Q		V _I = 14 V to 27 V, I _O = 500 mA				0.8	mA
		V _I = 15 V to 3	0 V, T _J = +25°C			0.8	
$\Delta V_{O}/\Delta T$	Output Voltage Drift ⁽²⁷⁾	I _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 100 kHz, T _A = +25°C			76		μV
RR	Ripple Rejection ⁽²⁷⁾	$f = 120 \text{ Hz}, V_0 = 500 \text{ mA}, V_1 = 14 \text{ V to } 24 \text{ V}$			60		dB
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J = +25°C			2		V
R _O	Output Resistance ⁽²⁷⁾	f = 1 kHz			18		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _J = +25°C			250		mA
I _{PK}	Peak Current ⁽²⁷⁾	T _J = +25°C			2.2		Α

- 26. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
- 27. These parameters, although guaranteed, are not 100% tested in production.

Electrical Characteristics (LM7815A)

Refer to the test circuit, 0° C < T_J < 125° C, I_O = 1 A, V_I = 23 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit
		$T_J = +25^{\circ}C$		14.75	15.00	15.30	V
V _O	Output Voltage	$I_O = 5 \text{ mA to 1 A, P}_O \le 15 \text{ W,}$ $V_I = 17.7 \text{ V to 30 V}$		14.40	15.00	15.60	
	Line Regulation ⁽²⁸⁾	$V_{I} = 17.4 \text{ V to } 30 \text{ V}, I_{O} = 500 \text{ mA}$			10	150	m)/
Poglino		V _I = 20 V to 26 V			5	150	
Regline		T 0500	V _I = 17.5 V to 30 V		11	150	- mV
		1j = +25°C	$V_1 = 17.5 \text{ V to } 30 \text{ V}$ $V_1 = 20 \text{ V to } 26 \text{ V}$		3	75	
		$T_J = +25$ °C, $I_O = 5$ mA to 1.5 A			12	100	mV
Regload	Load Regulation ⁽²⁸⁾	I _O = 5 mA to 1 A			12	100	
		I _O = 250 mA to 750 mA			5	50	
IQ	Quiescent Current	T _J = +25°C			5.2	6.0	mA
	Quiescent Current Change	$I_O = 5 \text{ mA to}$	1 A			0.5	
ΔI_Q		V _I = 17.5 V to 30 V, I _O = 500 mA				0.8	mA
		$V_{I} = 17.5 \text{ V to}$	30 V, T _J = +25°C			0.8	
$\Delta V_{O}/\Delta T$	Output Voltage Drift ⁽²⁹⁾	I _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 100 kHz, T _A = +25°C			90		μV
RR	Ripple Rejection ⁽²⁹⁾	f = 120 Hz, V _O = 500 mA, V _I =18.5 V to 28.5 V			58		dB
V _{DROP}	Dropout Voltage	I _O = 1 A, T _J = +25°C			2		V
R _O	Output Resistance ⁽²⁹⁾	f = 1 kHz			19		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _J = +25°C			250		mA
I _{PK}	Peak Current ⁽²⁹⁾	$T_J = +25^{\circ}C$			2.2		Α

- 28. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
- 29. These parameters, although guaranteed, are not 100% tested in production.

Typical Performance Characteristics

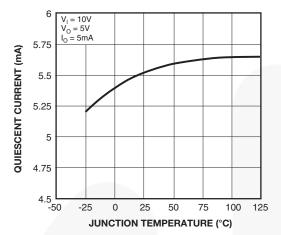


Figure 2. Quiescent Current

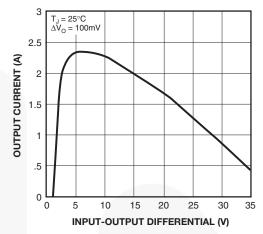


Figure 3. Peak Output Current

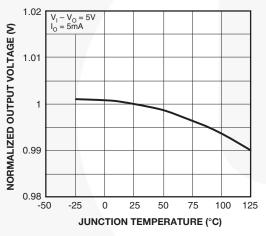


Figure 4. Output Voltage

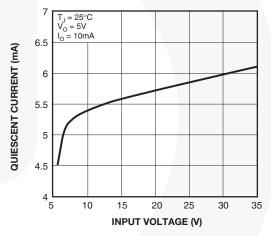


Figure 5. Quiescent Current

Typical Applications

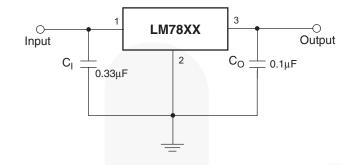


Figure 6. DC Parameters

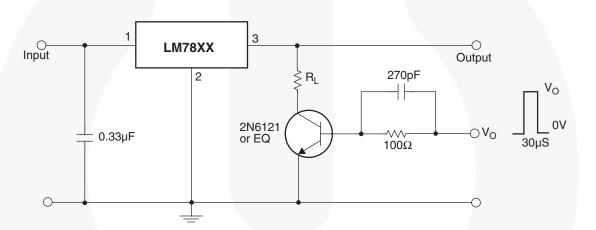


Figure 7. Load Regulation

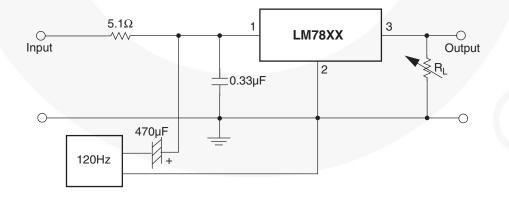


Figure 8. Ripple Rejection

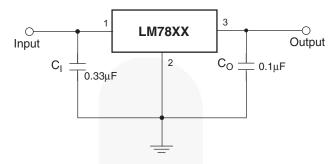


Figure 9. Fixed-Output Regulator

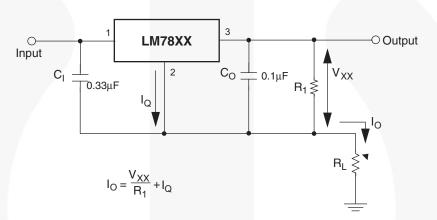


Figure 10. Constant Current Regulator

- 29. To specify an output voltage, substitute voltage value for "XX". A common ground is required between the input and the output voltage. The input voltage must remain typically 2.0 V above the output voltage even during the low point on the input ripple voltage.
- 30. C_I is required if regulator is located an appreciable distance from power supply filter.
- 31. C_{O} improves stability and transient response.

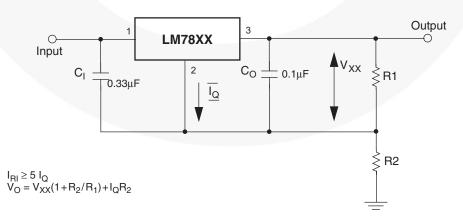


Figure 11. Circuit for Increasing Output Voltage

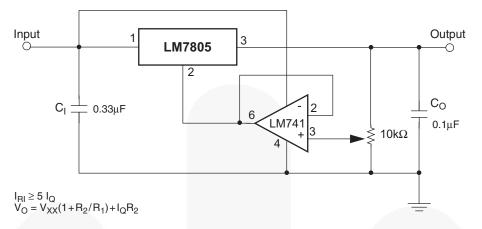


Figure 12. Adjustable Output Regulator (7 V to 30 V)

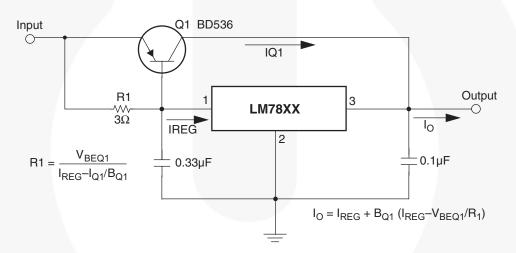


Figure 13. High-Current Voltage Regulator

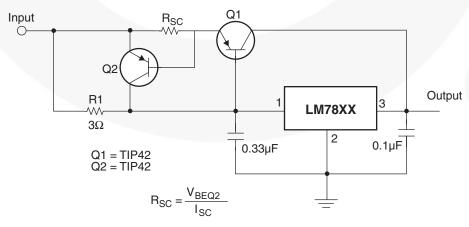


Figure 14. High Output Current with Short-Circuit Protection

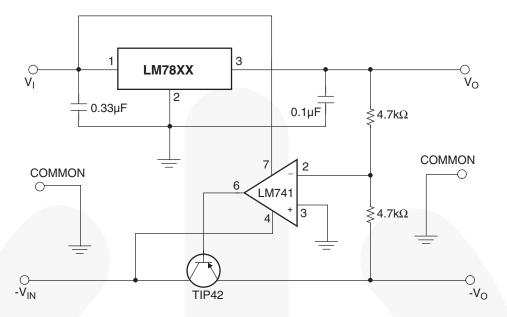


Figure 15. Tracking Voltage Regulator

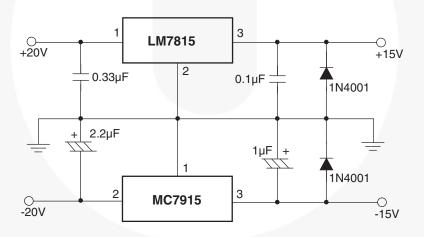


Figure 16. Split Power Supply (±15 V - 1 A)

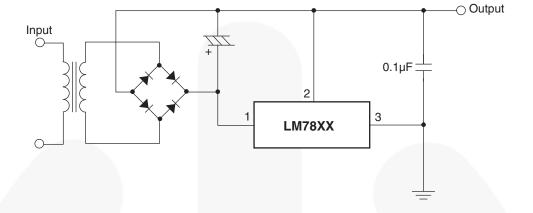


Figure 17. Negative Output Voltage Circuit

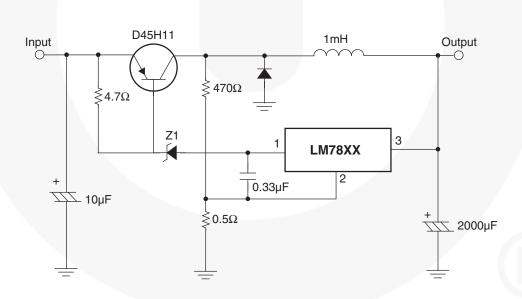
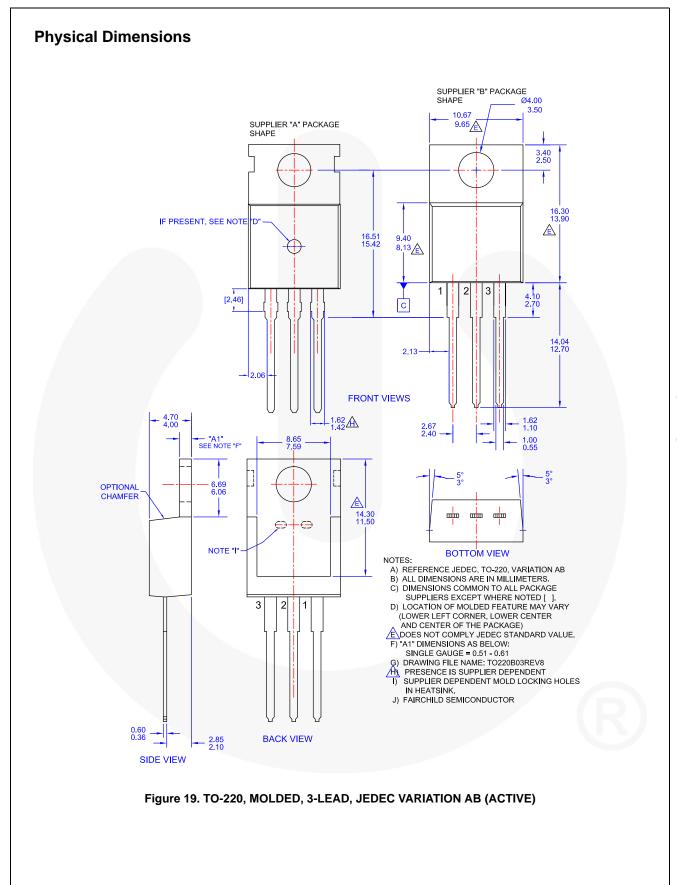


Figure 18. Switching Regulator







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