

Marco Micera

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Cloud Engineer based in Berlin, Germany, into cloud architecture, microservices, CI/CD, and open source.

Technologies

Most experienced with Kubernetes, Docker (Compose), GitHub Actions, Java, Scala, Bash, Linux, RESTful API (OpenAPI/Swagger), git
Some experience with GCP, Terraform, Jenkins, Golang, Prometheus, C/C++, Python, HTML/CSS, relational and graph DBs
Dabbled in Azure, AWS, Grafana, Spring Boot, JavaScript, jQuery, Node.js, Java Android, J2EE, PHP

Work Experience

Cloud Infrastructure Engineer

ENDOCODE AG

Berlin, Germany

Jul 2020 – Present

- Consolidating and maintaining multi-provider (GCP, AWS, & Azure) Terraform code for a global health company
- Developed an ad-hoc license detector in Java for the EU-funded project Fasten, integrated into their pipeline (Kafka on Kubernetes)
- Partially migrated Linux Foundation-hosted FOSS compliance tool Quartermaster to microservices, moved to message queuing

Research Assistant

UNIVERSITY OF LUGANO

Darmstadt, Germany

Apr 2019 – Jul 2019

- Proposed and implemented a custom flow-based cluster scheduler aware of network resources and composites
- Collaborated in (i) extending a simulator built around the scheduler in Scala, and (ii) refining the solver's cost model

Blog Posts

2021 **Java containerization for modular PF4j applications**, Endocode AG

Berlin, Germany

Personal Projects

kubemarks – github.com/marcomicera/kubemarks (+ [PerfKitBenchmarker](#) submod.)

POLYTECHNIC UNIVERSITY OF TURIN

Turin, Italy

Sep 2019 – Feb 2020

- Benchmarking tool based on Google Cloud Platform's PerfKit Benchmarker that can periodically run benchmarks as CronJobs on Kubernetes
- Results can be exposed to a Prometheus Pushgateway thanks to its Python client, allowing visual monitoring with Grafana

followifier – github.com/marcomicera/followifier

POLYTECHNIC UNIVERSITY OF TURIN

Turin, Italy

Apr 2018 – Jan 2020

- Tracking system that exploits Wi-Fi Probe Request frames sent by ESP32 boards placed in the corners of a room to detect and monitor devices
- Implemented ESP32 boards' firmware in C and main server's logic in Boost C++

Education

M.Sc. in Computer Engineering

POLYTECHNIC UNIVERSITY OF TURIN

Turin, Italy

Mar 2017 – Mar 2020

- Relevant courses: cloud computing, computer network technologies and services, distributed programming, software engineering ([list](#))
- Thesis at the Technical University of Darmstadt, Germany, titled "Data center resource management for in-network processing"

B.Sc. in Computer Engineering

UNIVERSITY OF PISA

Pisa, Italy

Oct 2013 – Feb 2017

- Part-time web developer for the School of Engineering ([credits](#))

Skills

- 🔑 Strong advocate of version control, issue tracking systems, detailed documentation, code modularity, and testing
- 🐧 Knowledge of UNIX/Linux, familiarity with TCP/IP, network programming, and multi-threading programming
- 👥 Good relational and organizational skills thanks to the time spent with the Erasmus Student Network in Pisa, Turin, and Darmstadt

Languages

Italian, Native (C2)

English, Fluent (C1, IELTS 7.5)

Spanish, Intermediate (B1)

German, Basic (A2)