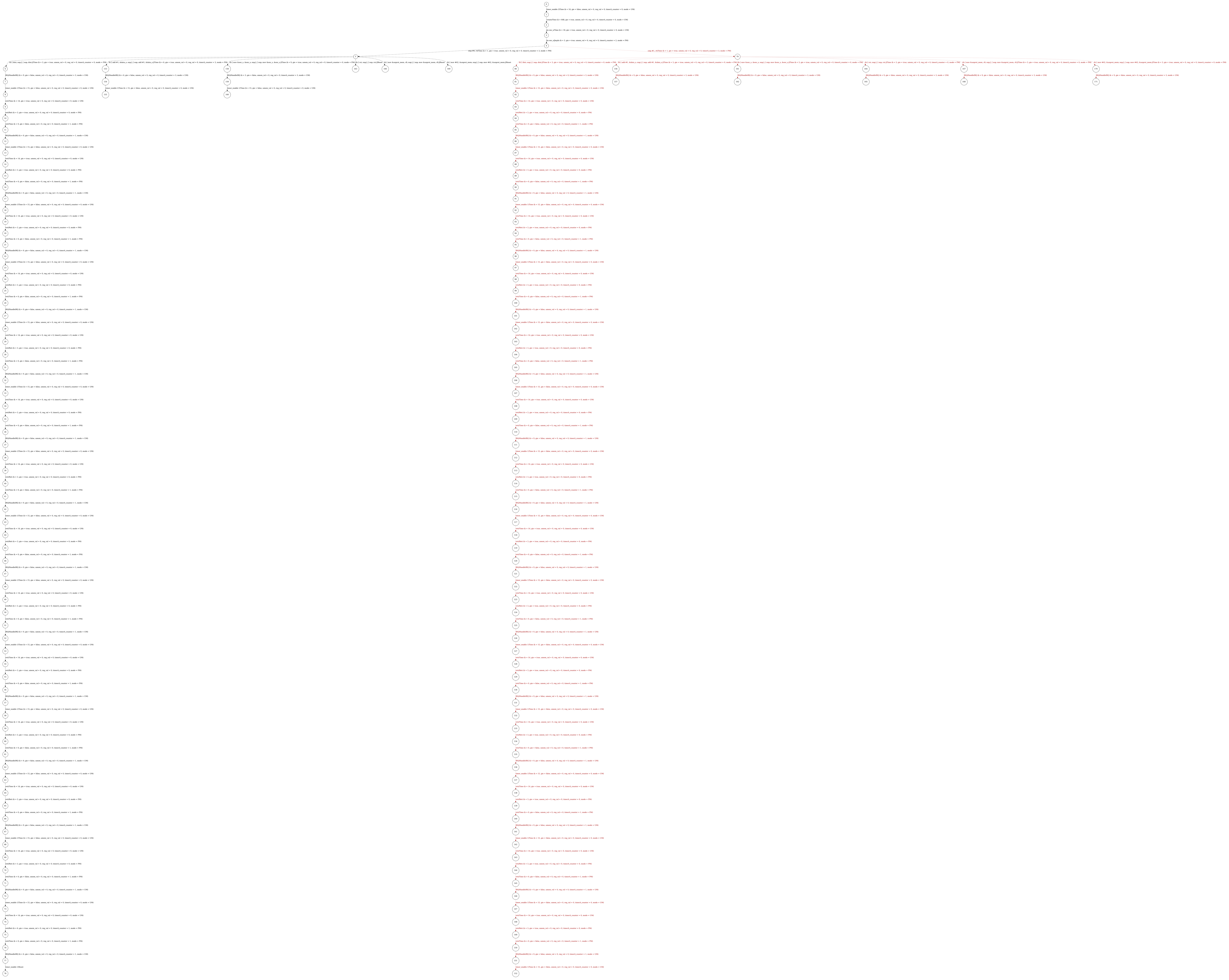
0
timer_enable 3/Time (k = 16, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
1
create/Time (k = 648, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
2
jin enc_s/Time (k = 18, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
3
jin enc_s/JmpIn (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 2, mode = PM)
4
cmp #0, r4/Time (k = 1, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 3, mode = PM)
5
cmp #1, r4/Time (k = 1, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 3, mode = PM)
79
ifz [dint; nop;], [nop; dint;]/Time (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
ifz [add #1, &data_s; nop;], [nop; add #1, &data_s;]/Time (k = 6, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 3, mode = PM)
ifz [mov &enc_s, &enc_s; nop;], [nop; mov &enc_s, &enc_s;]/Time (k = 9, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
ifz [rst; nop;], [nop; rst;]/Reset
ifz [mov &unprot_mem, r8; nop;], [nop; mov &unprot_mem, r8;]/Reset
ifz [mov #42, &unprot_mem; nop;], [nop; mov #42, &unprot_mem;]/Reset
6
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 3, mode = UM)
7
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
8
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
9
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
10
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
11
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
12
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
13
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
14
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
15
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
16
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
17
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
18
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
19
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
20
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
21
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
22
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
23
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
24
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
25
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
26
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
27
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
28
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
29
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
30
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
31
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
32
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
33
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
34
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
35
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
36
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
37
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
38
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
39
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
40
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
41
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
42
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
43
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
44
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
45
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
46
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
47
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
48
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
49
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
50
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
51
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
52
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
53
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
54
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
55
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
56
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
57
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
58
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
59
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
60
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
61
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
62
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
63
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
64
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
65
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
66
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
67
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
68
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
69
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
70
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
71
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
72
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
73
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
74
reti/Reti (k = 6, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
75
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
76
IRQ/HandleIRQ (k = 6, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
77
timer_enable 1/Reset
78
153
IRQ/HandleIRQ (k = 6, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 3, mode = UM)
154
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
155
158
IRQ/HandleIRQ (k = 3, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 3, mode = UM)
159
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
160
163
166
169
ifz [dint; nop;], [nop; dint;]/Time (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
ifz [add #1, &data_s; nop;], [nop; add #1, &data_s;]/Time (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
ifz [mov &enc_s, &enc_s; nop;], [nop; mov &enc_s, &enc_s;]/Time (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
ifz [rst; nop;], [nop; rst;]/Time (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
ifz [mov &unprot_mem, r8; nop;], [nop; mov &unprot_mem, r8;]/Time (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
ifz [mov #42, &unprot_mem; nop;], [nop; mov #42, &unprot_mem;]/Time (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
80
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 3, mode = UM)
81
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
82
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
83
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
84
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
85
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
86
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
87
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
88
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
89
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
90
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
91
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
92
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
93
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
94
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
95
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
96
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
97
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
98
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
99
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
100
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
101
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
102
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
103
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
104
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
105
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
106
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
107
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
108
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
109
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
110
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
111
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
112
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
113
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
114
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
115
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
116
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
117
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
118
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
119
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
120
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
121
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
122
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
123
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
124
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
125
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
126
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
127
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
128
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
129
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
130
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
131
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
132
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
133
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
134
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
135
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
136
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
137
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
138
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
139
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
140
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
141
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
142
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
143
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
144
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
145
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
146
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
147
reti/Time (k = 14, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
148
reti/Reti (k = 3, gie = true, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = PM)
149
reti/Time (k = 0, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = PM)
150
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 1, mode = UM)
151
timer_enable 1/Time (k = 15, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 0, mode = UM)
152
156
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 3, mode = UM)
157
161
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 3, mode = UM)
162
164
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 3, mode = UM)
165
167
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 3, mode = UM)
168
170
IRQ/HandleIRQ (k = 9, gie = false, umem_val = 0, reg_val = 0, timerA_counter = 3, mode = UM)
171