TOLL - GATE

(A TOLL PLAZA SIMULATION)

A PROJECT REPORT

Major Project – 8 CS 5

Submitted To-:   Submitted By-:

Mr. Mudit Khetan   Mayank Mishra
Associate Professor   (8CS - A22)
(Computer Sc. Dept.)

IN

B.Tech. (Computer Science & Engineering) VIII Semester

Session –: 2016 - 2017

SCHOOL OF ENGINEERING AND TECHNOLOGY (SADTM Campus)
JAIPUR NATIONAL UNIVERSITY