

LAB 08

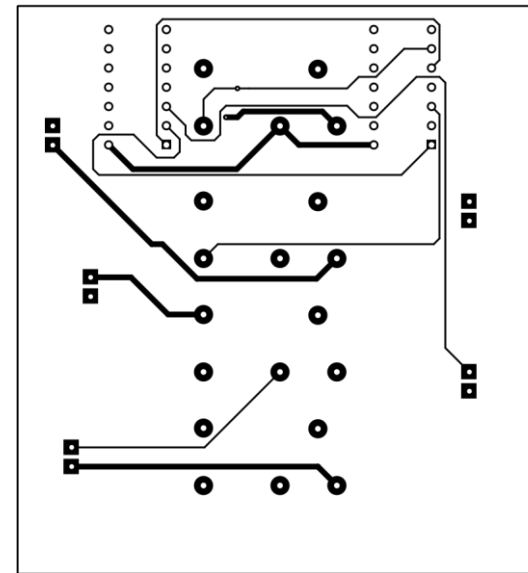
LFR PCB with Logical Protection

<u>SR</u>	<u>Components Used</u>	<u>Amt</u>
1	2-Pin (CONN-SIL2	x5
2	Relay (G5S-1-DC5)	x4
3	Battery	x1
4	AND GATE IC 74LS09	x1
5	OR GATE IC 4069	x1

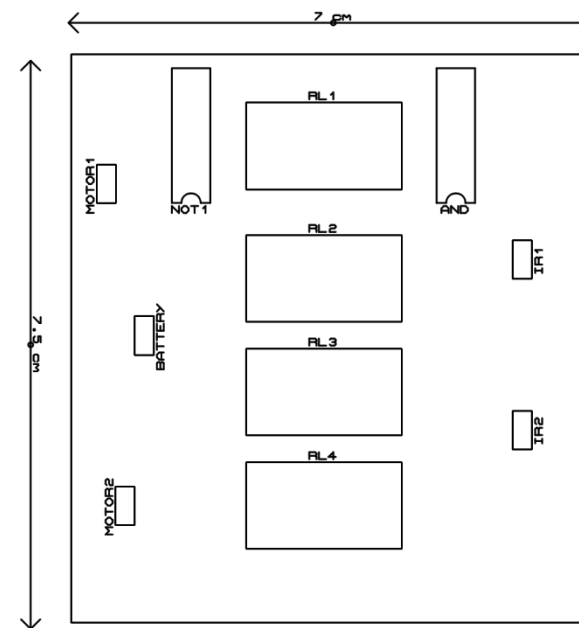
Submitted by Muhammad Haris Irfan

Submitted to Muhammad Imran Saeed Lodhi

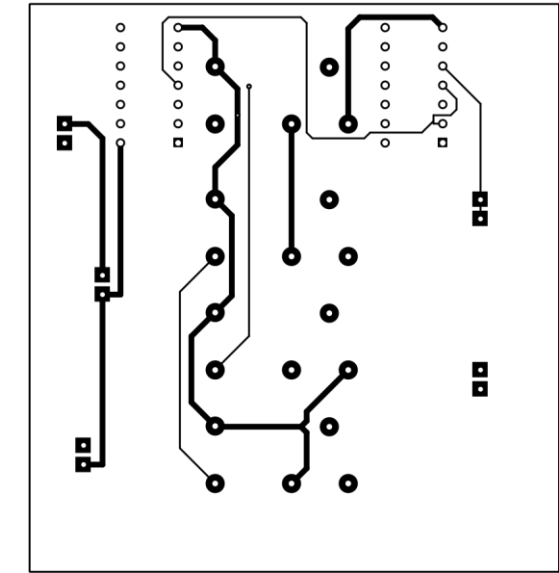
Date: 28-09-20



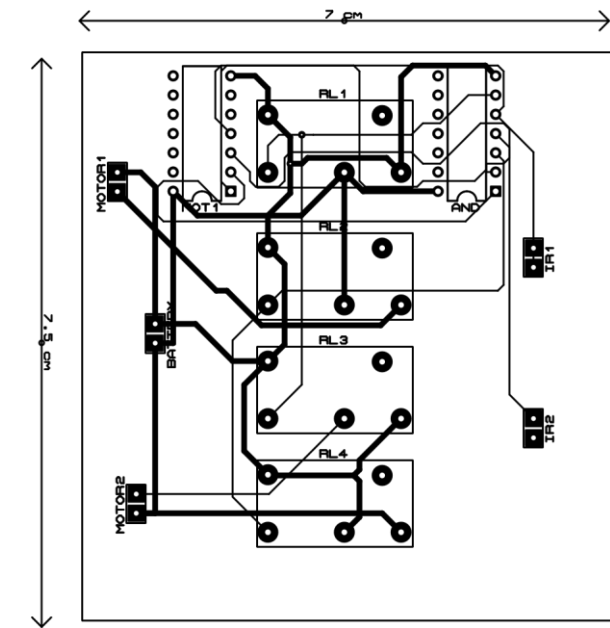
Top Copper Layer



Top Silk Layer



Bottom Copper Layer



All Four layer