TR-76D-433

RF Transceiver Module

Data Sheet

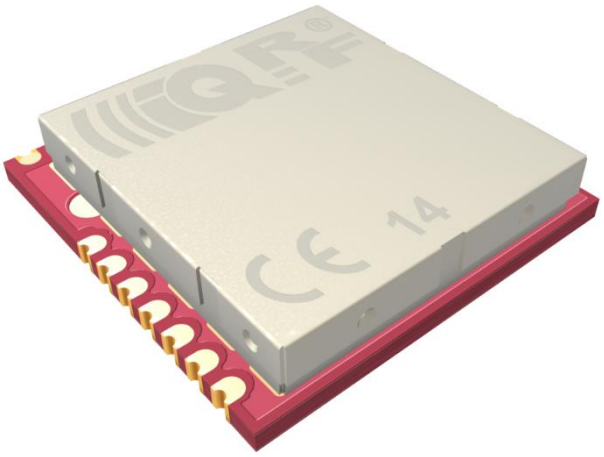
Preliminary

Smarter Wireless. Simply.

Description

TR-76D-433 is a family of IQRF transceiver modules operating in the 433 MHz license free ISM (Industry, Scientific and Medical) frequency band. Its highly integrated ready-to-use design containing MCU, RF circuitry and serial EEPROM requires no external components. SMT mounting and very small dimensions allow space saving. Extended RF power results in higher RF range. Ultra low power consumption fits for battery powered applications. MCU with built-in operating system significantly reduces application development time. Optional DPA framework supports applications even without programming.

There is no difference between TR and DCTR transceiver versions from IQRF OS v4.02D. All TRs support both OS as well as DPA approaches.



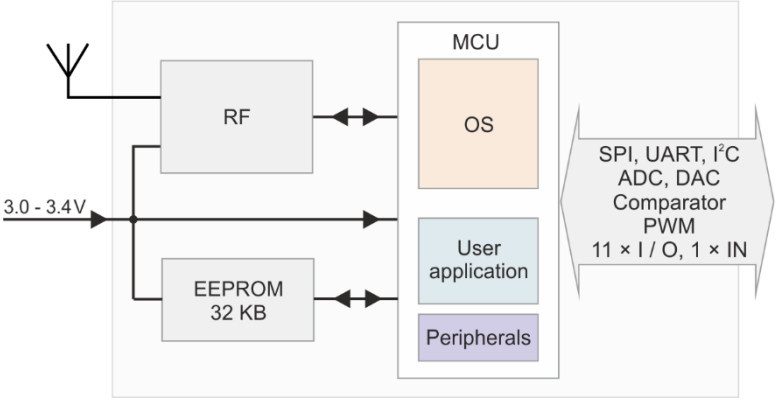
Key features

- Operating system (upgradeable at the user), easy to use
- DPA framework for Data controlled approach (formerly DCTR)
- GFSK modulation
- RF band 433 MHz, multiple channel
- RF output power 8 mW
- MCU with extended resources, user interrupt capability
- Extra low power consumption, power management modes
- SPI interface supported by OS in background
- Serial EEPROM 256 Kb
- PWM output
- Programmable HW timer
- Battery monitoring
- 18 pins, 11 I/O pins, 1 input only pin
- A/D converter (4 channels), D/A converter, analog comparator
- Soldering antenna pad-hole
- Stamp-hole pads, SMT mounting, compatible with SIM card connector without metallic holder (KON-SIM-02)
- Shielding can

Applications

- Bidirectional RF communication
- Point-to-point or network wireless connectivity
- Telemetry, AMR (automatic meter reading)
- WSN (wireless sensor network)
- Building automation
- Street lighting control
- Wireless monitoring, control and regulation
- Remote data acquisition
- RF connectivity in many other fields
- Also for municipal and indoor areas
- Internet of Things

Block diagram



The information contained in this publication regarding device applications and the like is provided only for your convenience and may be superseded by updates. It is your responsibility to ensure that your application meets your specifications.

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Electrical specifications

***Typical values** unless otherwise stated*

Parameters specified in this datasheet are typical values. They are at power supply V_{CC} = 3 V only. V_{CC} voltage different from 3 V can impact on RF range and other parameters.

Supply voltage (V_{CC}) [1]	3.0 V min., 3.4 V max., stabilized
Operating temperature [2]	-40 °C to +85 °C
Supply current	
Deep sleep mode (OS v4.00 or higher only)	< 300 nA (all peripherals disabled [3], RF IC in Standby mode)
Sleep mode	< 1 µA (all peripherals disabled [3], RF IC in Sleep mode)
Run mode	
RF sleep	1.6 mA
RF ready	3.0 mA
RX mode	
STD	12 mA
LP [4]	240 µA
XLP [4]	18 µA
TX mode (TR-76DC-433 with no antenna)	8.2 mA – 22.8 mA (according to RF output power)
RF band	433 MHz
RF channels	See IQRF OS User's guide, Appendix *Channel maps*
RF data modulation	GFSK (Gaussian Frequency Shift Keying)
RF data transmission bit rate [5]	19.8 kb/s
RF sensitivity	-104 dBm (STD RX mode, `checkRF(0)`)
RF output power (TR-76DC-433 with no antenna)	12.6 dBm (for 50 Ω load), programmable in 8 levels (0 – 7). See *Diagram 1*.
RF interface	Single-ended, output impedance 50 Ω
Input voltage on Q4 to Q15 pins	0 V to VCC
A/D converter	10 bit, 4 inputs. Refer to MCU datasheet.
Size (L x W x H)	15.2 mm x 14.9 mm x 3.3 mm

Note 1: RF power and other parameters depend on the supply voltage. Refer to datasheets of MCU and RF IC used. Test your application with respect to required supply voltage range.
Note 2: RF range may change with lower temperature. Frost, condensation or humidity over 85% may disable module functionality. Module suitability should be tested in the final application at real conditions before volume use.
Note 3: Additional current is consumed when a peripheral (e.g. watchdog, Brown-out detection etc.) is enabled.
Note 4: Depends on interferences.
Note 5: Several RF bit rates different from 19.8 kb/s will be available in future IQRF OS versions.

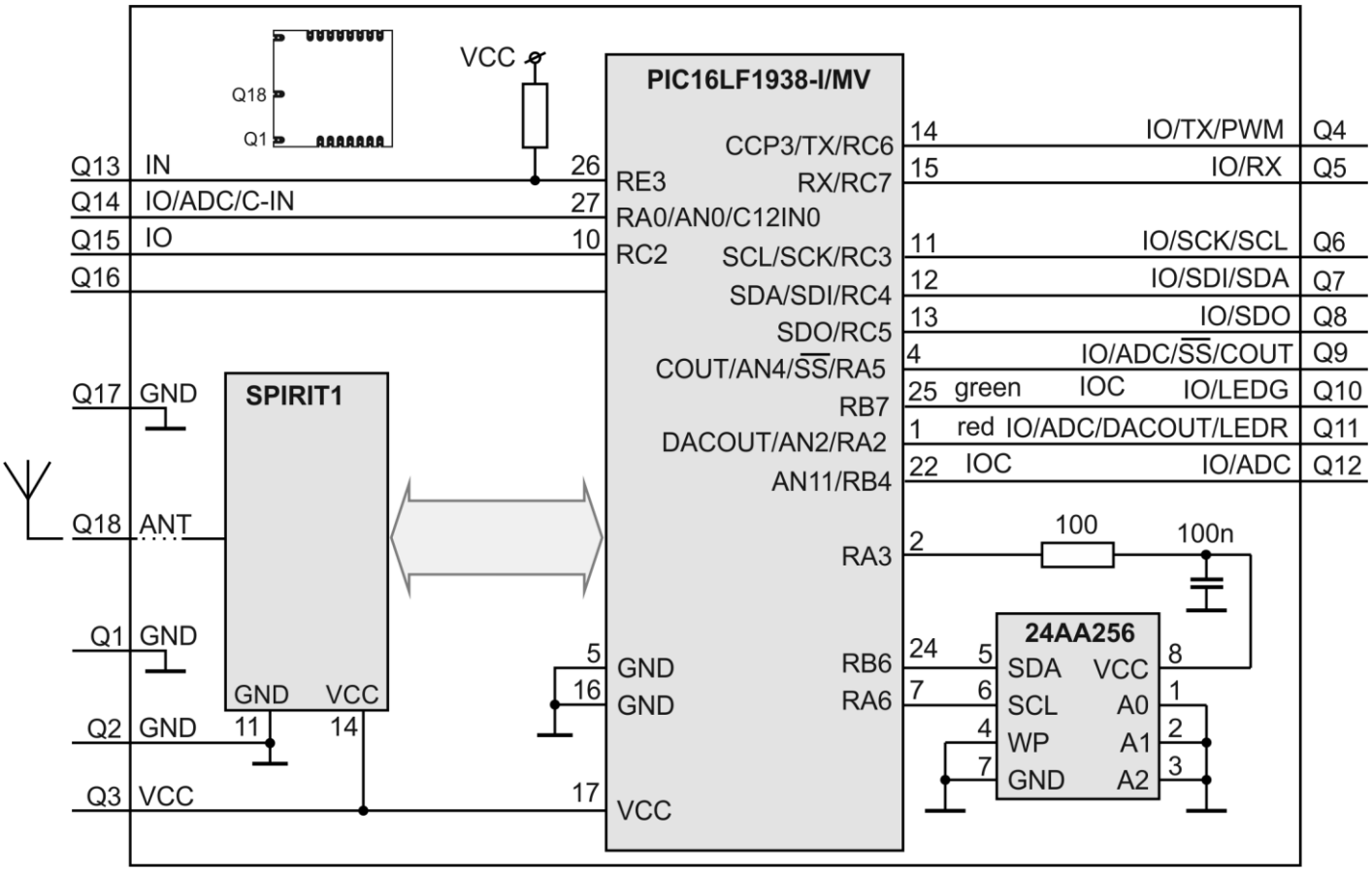
Absolute maximum ratings

Stresses above listed maximum values may cause permanent damage to the device and affect device reliability. Functional operation under these or any other conditions beyond those specified is not supported.

Supply voltage (V_{CC})	3.9 V
Voltage on Q4 to Q15 pins (configured as inputs) vs. GND	-0.3 V to (V_{CC} + 0.3 V)
Storage temperature	-40 °C to +85 °C
Ambient temperature under bias	-40 °C to +85 °C

***Caution:** Electrostatic sensitive device. Observe appropriate precautions for handling.*

Simplified schematic



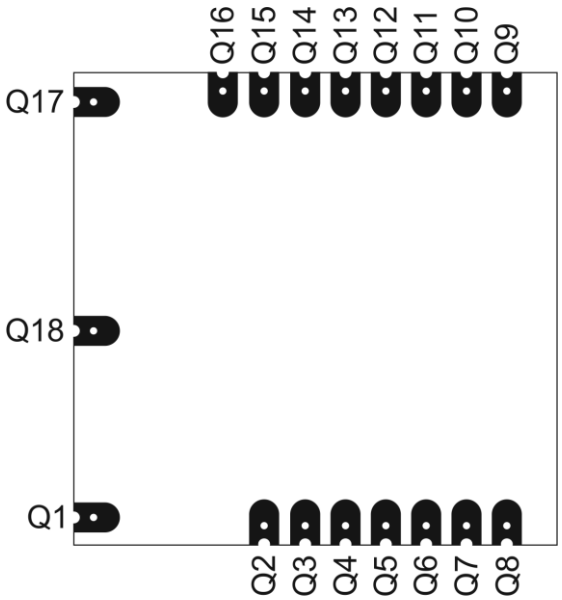
Basic components

IC	Type	Manufacturer	Note
MCU	PIC16LF1938–I/MV	Microchip	
RF IC	SPIRIT1	STMicroelectronics	
RF balun	BALF-SPI-02D3	STMicroelectronics	
EEPROM	24AA256-I/CS16K	Microchip	256 Kb

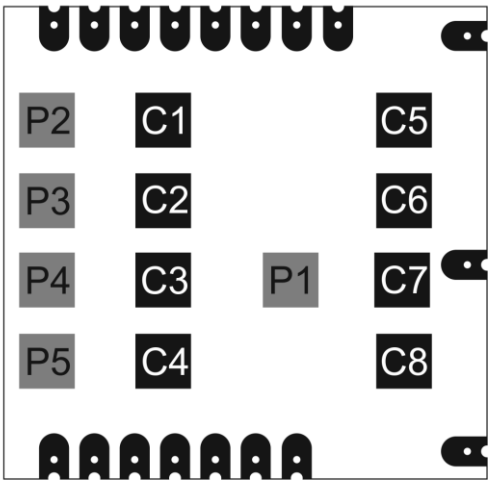
For more information refer to datasheets of ICs used.

Pin	Name	Description
Q1	**GND**	Ground
Q2, C4	**GND**	Ground
Q3, C3	**V_{CC}**	Power supply voltage
Q4	**IO / TX / PWM**	
	RC6	General I/O pin
	TX	UART TX
	CCP3	PWM output
Q5	**IO / RX**	
	RC7	General I/O pin
	RX	UART RX
Q6, C6	**IO / SCK / SCL**	
	RC3	General I/O pin
	SCK	SPI clock input
	SCL	I^2C clock
Q7 [1], C7	**IO / SDI / SDA**	
	RC4	General I/O pin
	SDI	SPI data
	SDA	I^2C data
Q8 [1], C8	**IO / SDO**	
	RC5	General I/O pin
	SDO	SPI data out
Q9, C5	**IO / ADC / -SS / COUT**	
	RA5	General I/O pin
	AN4	Analog A/D input
	-SS	SPI Slave select
	C2OUT	Comparator output
Q10 [2]	**IO / LEDG**	
	RB7	General I/O pin, programmable pull-up Interrupt / Wake-up on change (IOC)
	LED1	LEDG supported by OS
Q11 [2]	**IO / ADC / LEDR**	
	RA2	General I/O pin
	AN2	Analog A/D input
	LED2	LEDR supported by OS
	DACOUT	D/A converter output
Q12	**IO / ADC**	
	RB4	General I/O pin, with programmable pull-up Interrupt / Wake-up on change (IOC) RFPGM / (X)LP mode termination
	AN11	Analog A/D input
Q13	**IN**	
	RE3	General input only pin
Q14, C1	**IO / ADC / C-IN**	
	RA0	General I/O pin
	AN0	Analog A/D input
	C12IN0	Comparator –input
Q15, C2	**IO**	
	RC2	General I/O pin
Q16	–	Do not use, leave unconnected
Q17	**GND**	Ground
Q18	**ANT**	Antenna
P1–P5	For manufacturer only	

Top view



Bottom view

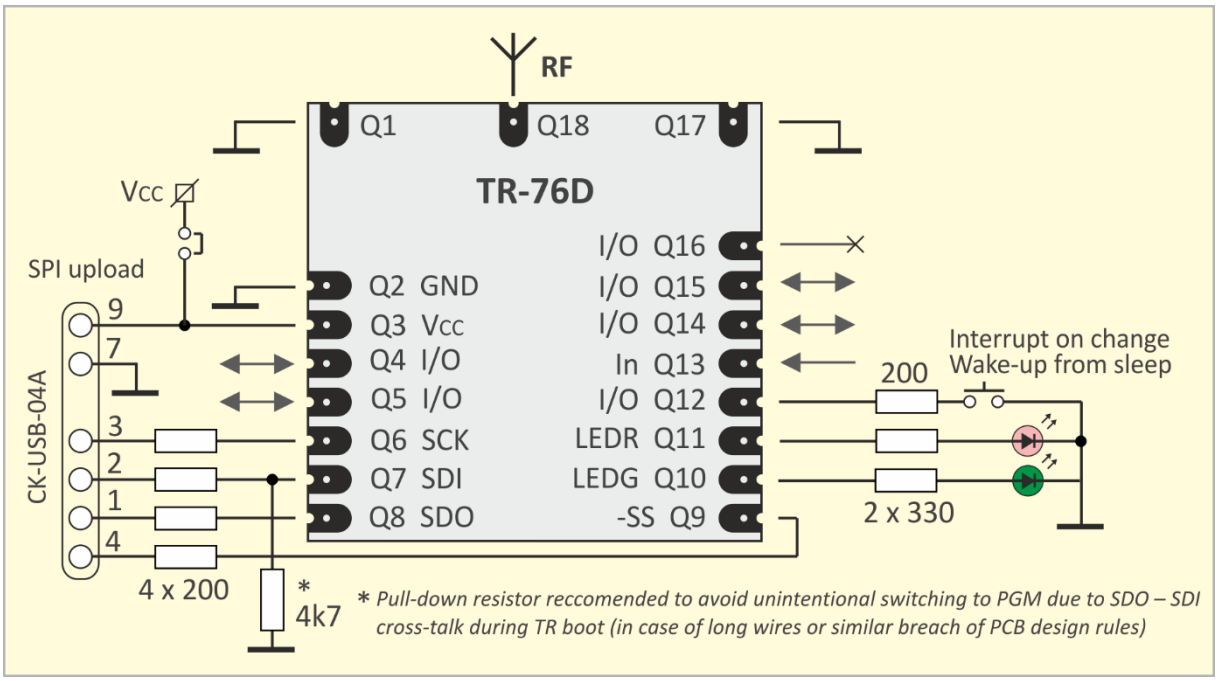


Note 1: Pin Q8 is used as output and pin Q7 as input during initial ~200 ms boot-up (after power supply rising-up) to detect programming mode. That is why these two pins should not be interconnected to each other.

Note 2: This pin is affected by IQRF OS (and possibly DPA) LED functions and system LED indication.

There are no on-board protection series resistors on I/O pins. It is recommended to use 200 Ω series resistors on each pin.

Recommended circuit for development



For development, it is recommended to implement the following arrangement:

- Serial protective resistors on each I/O pin used.
- Both system LEDs (LEDR and LEDG) for IQRF OS and DPA status indication and for possible user indication. When the Q10 and Q11 pins are used as user I/Os, it must be taken into account that these pins can be affected by IQRF OS or DPA.
- Pin Q12 configured as an input with the internal pull-up resistor and equipped with a pushbutton connected to the ground. Then pressing the button can generate an interrupt on pin change, wake-up the transceiver from sleep, terminate RFPGM mode, initiate bonding etc.
- Pull-down resistor on pin Q7 recommended to avoid unintentional switching to PGM mode due to SDO - SDI cross-talk during TR boot (in case of long wires or similar breach of PCB design rules only).
- SPI interface for wired upload of application code into the transceiver using an IQRF programmer, e.g CK-USB-04A.

Depending on actual user application and power supply range, it may be required to isolate interface pins and/or power supply from user circuitry during uploading. For details refer to the CK-USB-04A User's guide, chapter *Application/In-circuit upload*.

RF range

RF range strongly depends on the following design aspects:

- Hardware:
  - Construction of the devices (especially TR location within the device, PCB layout, ground planes, conductive areas and bulk objects such as metallic parts and batteries in the nearest surroundings, with respect to possible reflections and counterpoise effect)
  - Physical arrangement of devices (especially mutual orientations of antennas with respect to polarizations and radiation patterns)
- Application software:
  - RF output power is selectable from 8 levels
  - To increase immunity to RF noise, incoming RF signal can be filtered according to signal strength.

Refer to IQRF OS Reference guide, function `checkRF` and Application note *AN014 RF range optimizing at TR-7xDx transceivers*.

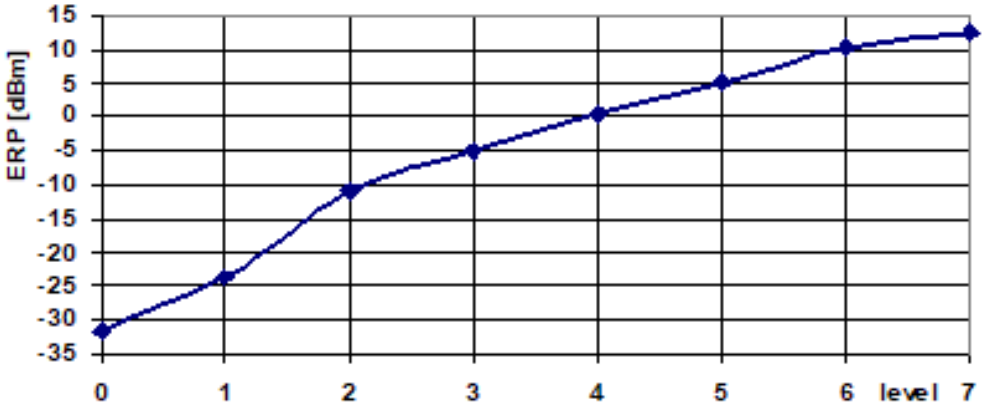


Diagram 1: *Effective radiated power (ERP) vs. level in the `setRFpower(level)` function. Refer to IQRF OS Reference guide. TR-76DC-433 without an antenna and antenna cable.*

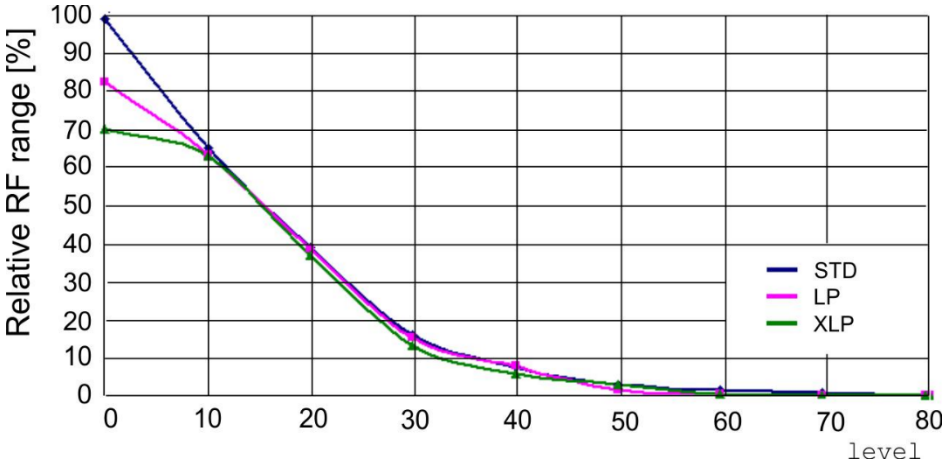
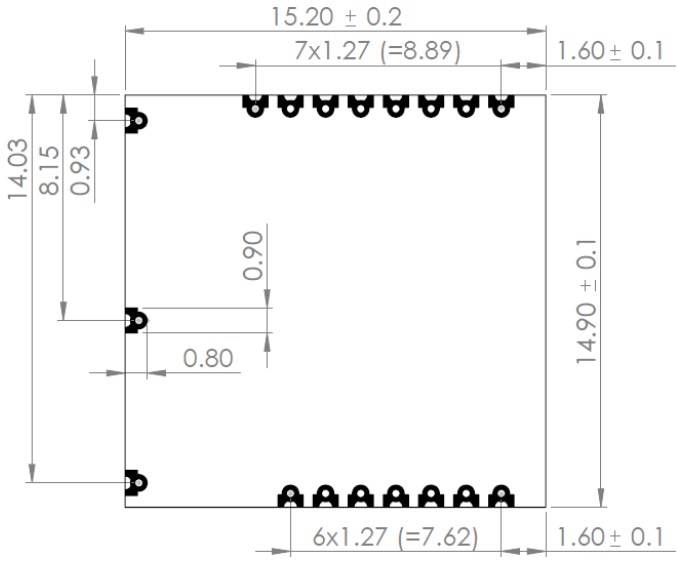


Diagram 2: *Relative RF range vs. `level` in the `checkRF(level)` function in STD, LP and XLP RX modes. Refer to IQRF OS Reference guide.*

Mechanical drawings



Top view. Units: mm.

Hardware revision

v1.02	First release.

Application

*Users have to ensure observing local provisions and restrictions relating to the use of short-range devices **by software**, e.g. the CEPT ERC/REC 70-03 Recommendation and subsequent amendments in EU.*

See IQRF video tutorial set on www.iqrf.org/videos.

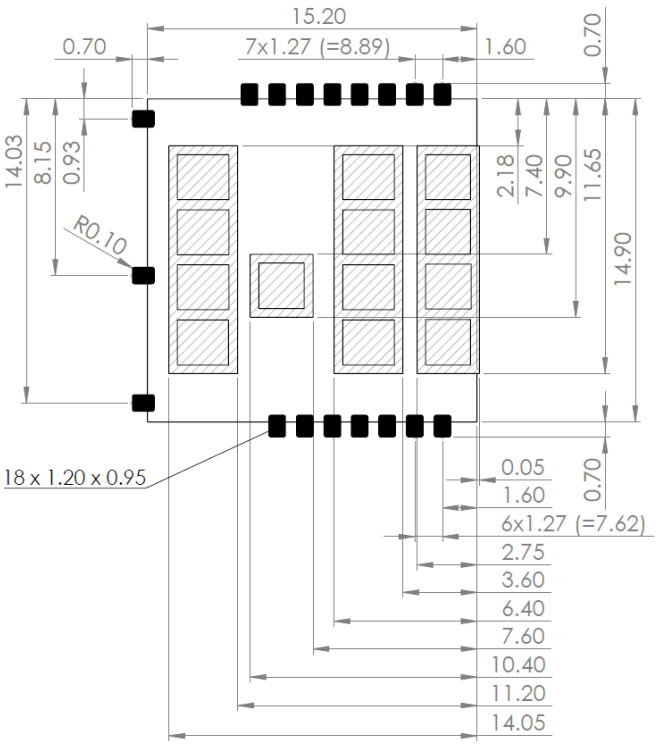
Assembly

For proper mounting of surface mount TR-76Dx-433 modules and avoiding damage during solder reflow assembly, the IPC/JEDEC J-STD-020C standard must be observed. The parts must be baked dry according to IPC/JEDEC J-STD-033C, MSL 4 before reflow soldering. For reflow profile and details refer to the AN010 Application note – *SMT mounting of IQRF TR modules*.

It is not allowed to connect wires to pads C1 to C8 and P1 to P5 by soldering.

***Caution:** TR-76Dx must not be plugged in a SIM connector with metallic holder.*

Recommended PCB layout



Pads

Restricted areas for wires on top side

Top view. Units: mm.

Sealing

In case of sealing or protecting TR modules against a harsh environment by coating, encapsulating or potting using a lacquer, gel or other filling matter, the ion cleanness of the TR modules must be less than 1 µg/cm^2 of NaCl equivalent otherwise there is a risk of corrosion.

Such a surface treatment always impacts the RF range. Thus, sealing material should have the relative permeability (μ_r) as close to 1 within given frequency band. E.g. μ_r = 4 **at 433 MHz** decreases relative range to approx. 70%.

Protecting materials, methods, accomplishments and handling must comply with general requirements and rules for proper use with electronic devices. Damaging, either chemical or mechanical (even due to the thermal expansivity of the material used) must be avoided. Testing is necessary to ensure that the application meets the specifications.

Operating system

See IQRF OS User's guide and IQRF OS Reference guide.

DPA framework

See DPA Framework technical guide.

Application software

See IQRF Quick start guide and IQRF application examples.

Programming (upload)

There are the following possibilities to upload an application program in TR-76Dx modules:

- Wired upload with TR-76Dx-433 plugged via the SIM connector in the CK-USB-04A programmer.
- For TR-76Dx-433 modules populated in an application:
  - Wired upload
    - Using the CK-USB-04A programmer. See the CK-USB-04A User's guide.
    - Using the CK-USB-04 programmer and the KON-TR-01P adapter. See the KON-TR-01P User's guide.
  - Wireless upload: See the IQRF OS User's guide, Appendix *RFPGM – RF programming*^{TM}.

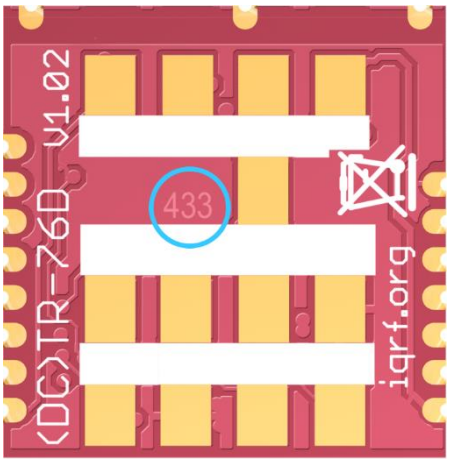
Product information

Ordering codes

T R-76D-433

- RF band 433 MHz
- Transceiver series. TR / DCTR are not differentiated from IQRF OS v4.02D.

Type	Antenna connection	Data controlled
TR-76D-433	Soldering pad-hole	Yes



433 MHz band identification, bottom view

Document history

- 171108 Cosmetic improvements only.
- 170908 First release. Preliminary.

Sales and Service

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Complies with directives 2011/65/EU (RoHS) and 2012/19/EU (WEEE).

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