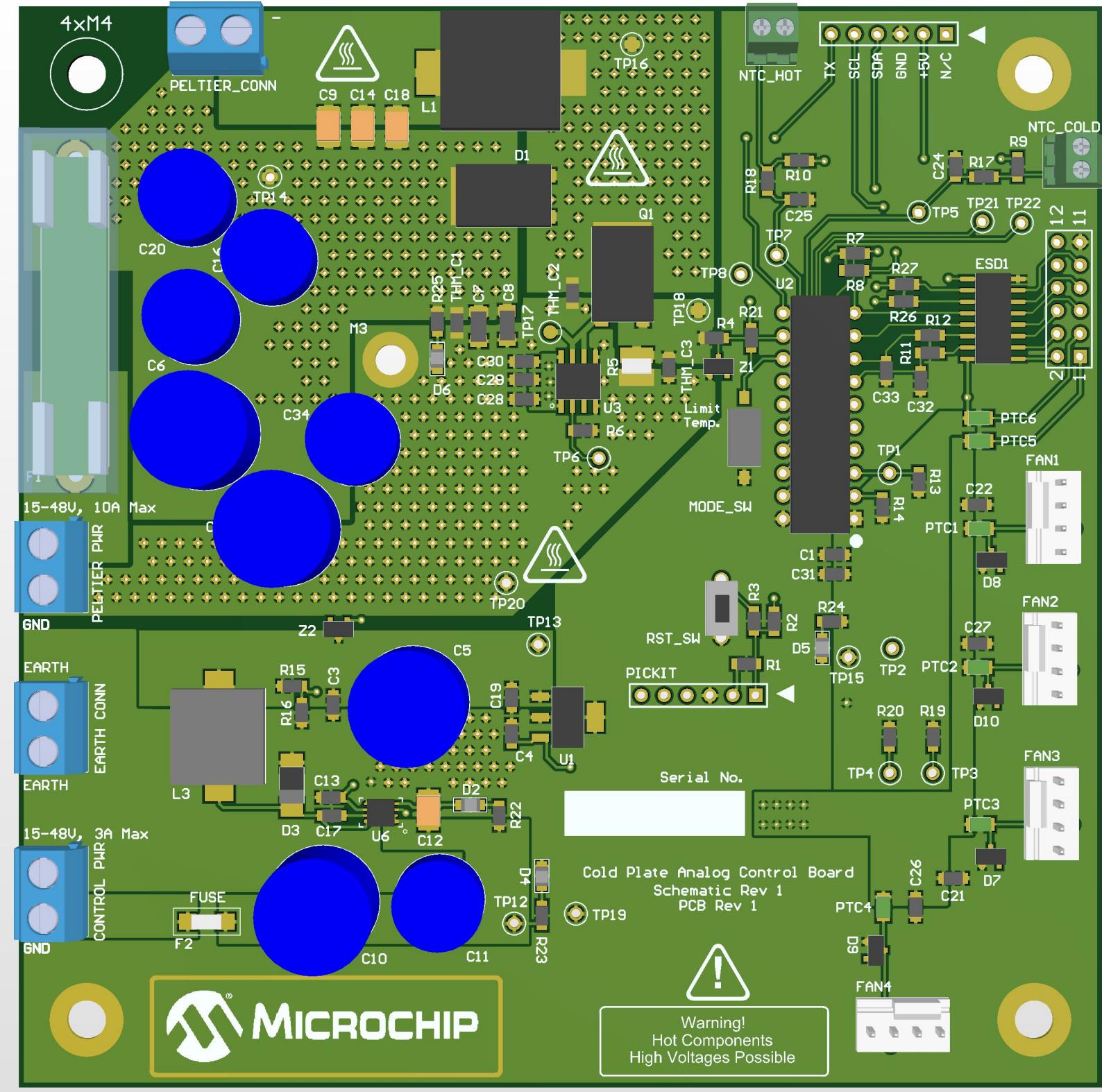


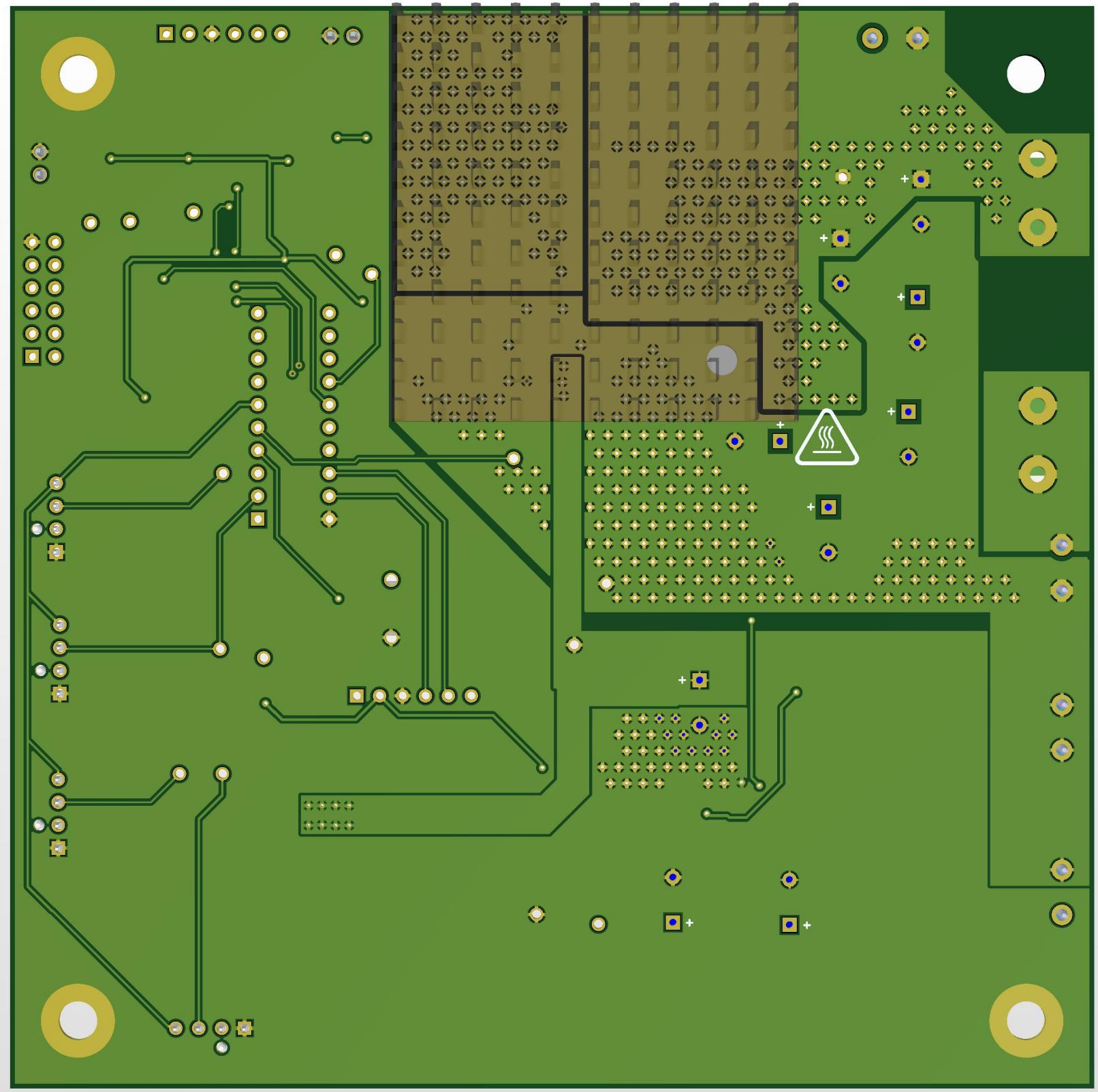
Project Owner: Robert Perkel
PCB Layout Contact: Robert Perkel



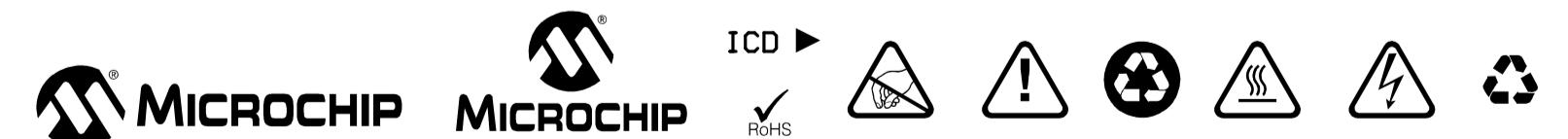
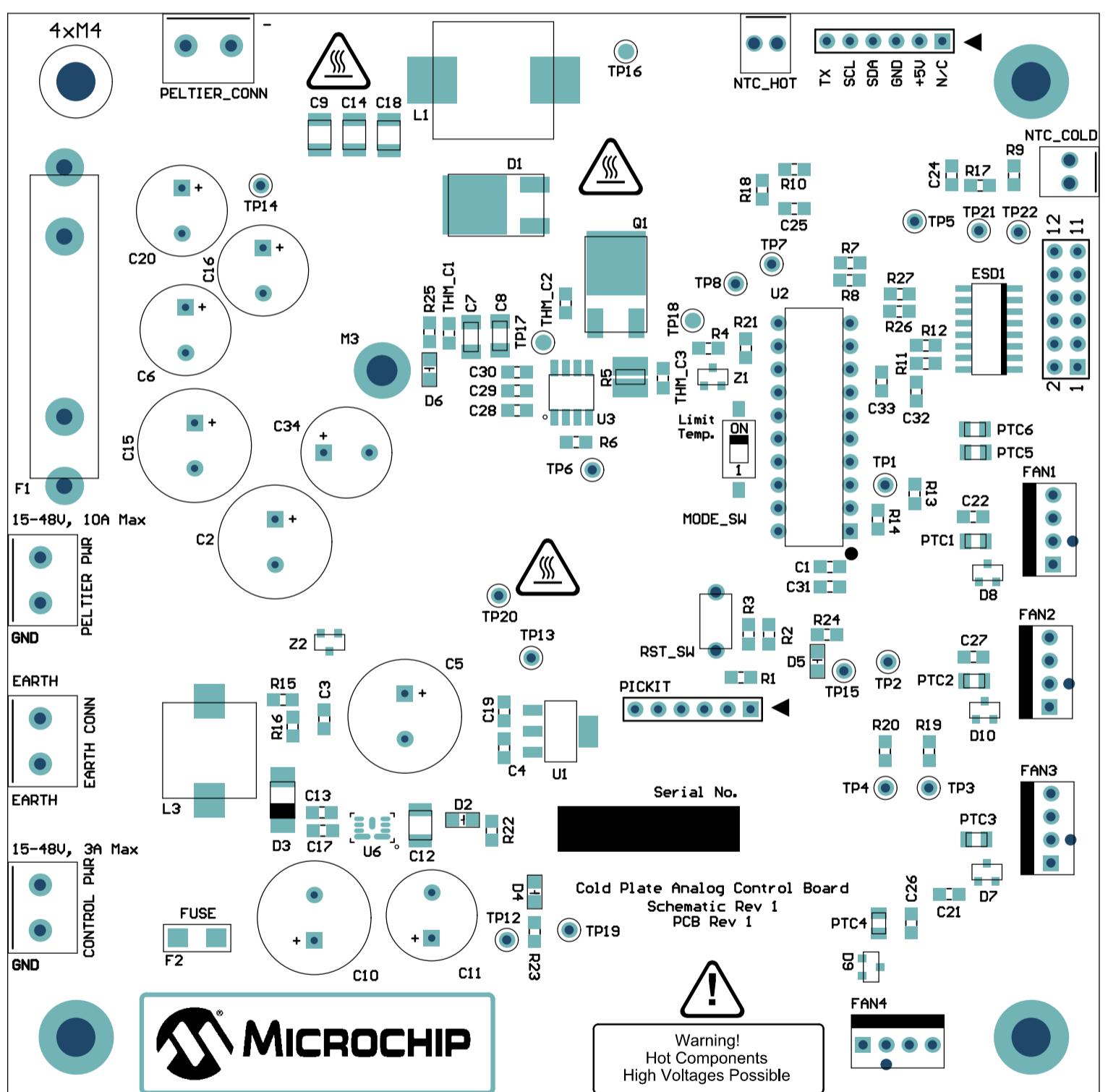
Variant: [No Variations]

Designed with
Altium
Altium.com





REV	ECO	Comments	Date

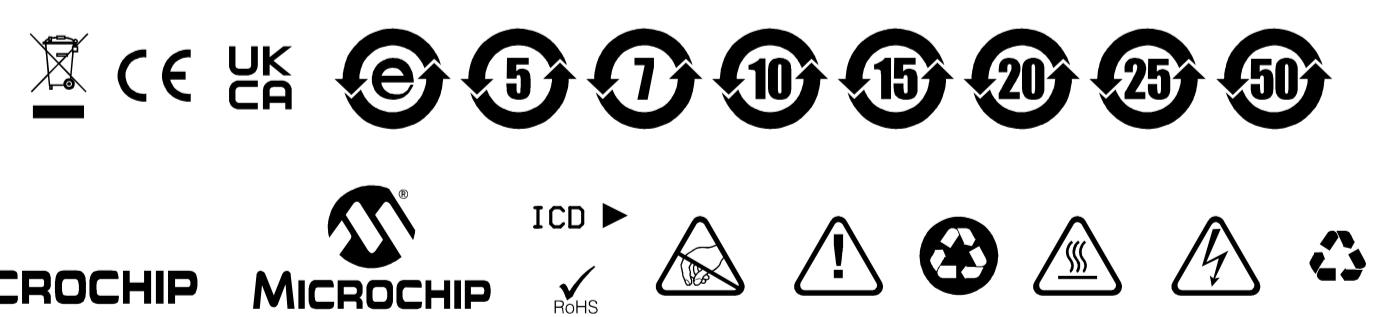
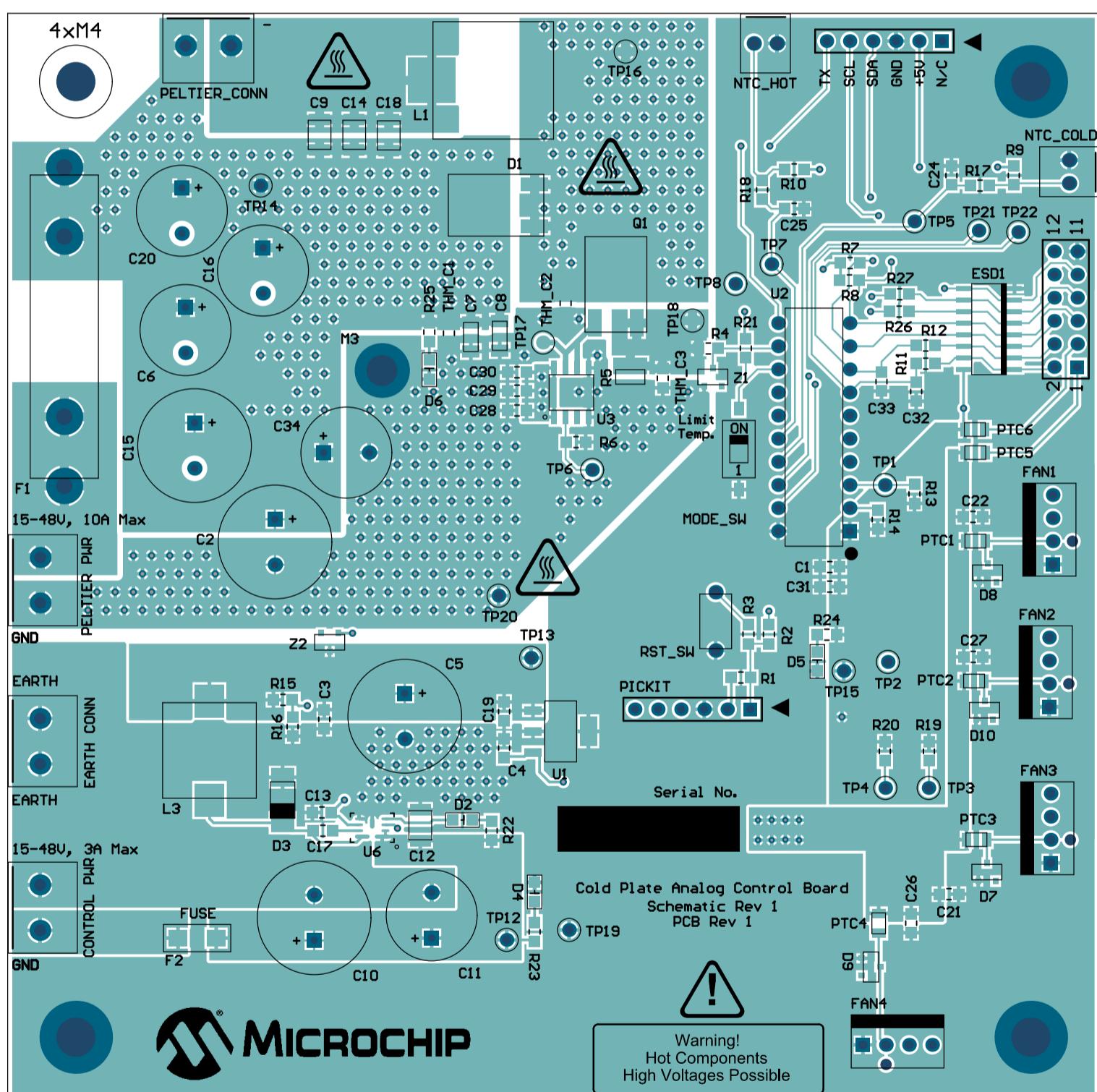


TITLE: Cold Plate Analog Control Board		PART NUMBER: **
PCB Layout Contact: Robert Perkel	GERBER FILE: 'TOP SILK'	
Project Owner: Robert Perkel	BOARD NUMBER: 04-**	DATE: 7/10/2022
PCB FILE NAME: 171xx_Cold_Plate.PcbDoc	LAYER NAME: BOARD (M3)	REV: 1 10mm 400mI

dsPICDEM.net™ mTouch® chipKIT™
dsPICDEM™ MPLAB® dsPIC®
PICDEM.net™ PICkit™ PIC®
PICDEM™ PICtail™

MICROCHIP
Microchip Technology Inc.
2355 W. Chandler Blvd.
Chandler, AZ 85224

REV	ECO	Comments	Date



TITLE: Cold Plate Analog Control Board		PART NUMBER: **
PCB Layout Contact: Robert Perkel	GERBER FILE: 'TOP Copper & Silk'	
Project Owner: Robert Perkel	BOARD NUMBER: 04-**	DATE: 7/10/2022
PCB FILE NAME: 171xx Cold Plate.PcbDoc	LAYER NAME: BOARD (M3)	REV: 1 10mm 400mI

REV	ECO	Comments	Date

A

A

B

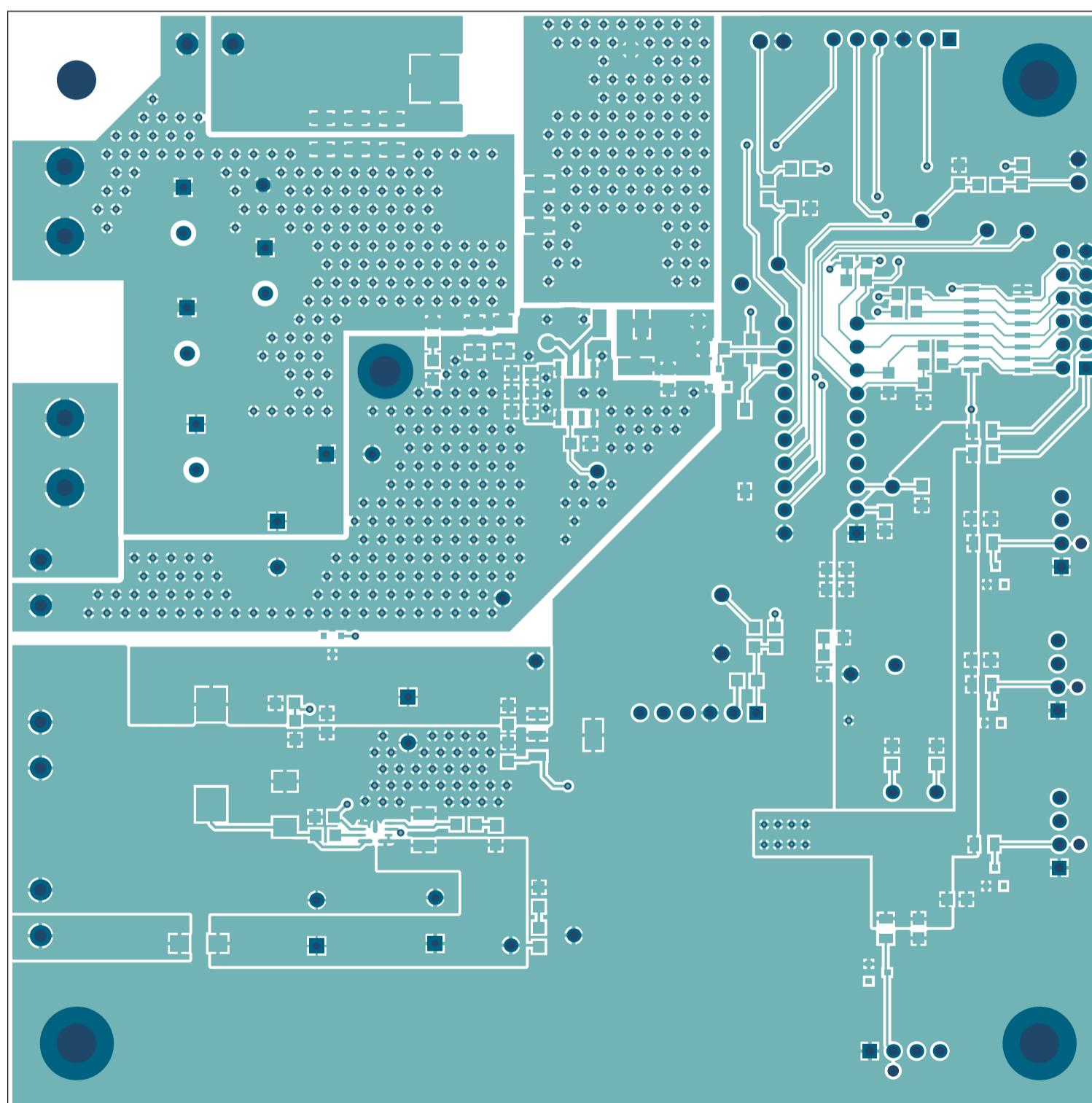
B

C

C

D

D



 MICROCHIP Microchip Technology Inc. 2355 W. Chandler Blvd. Chandler, AZ 85224	TITLE: Cold Plate Analog Control Board			PART NUMBER: **
	PCB Layout Contact: Robert Perkel	GERBER FILE: 'TOP Copper'		
	Project Owner: Robert Perkel	BOARD NUMBER: 04-**		DATE: 7/10/2022
	PCB FILE NAME: 171xx_Cold_Plate.PcbDoc	LAYER NAME: BOARD\{M2}	REV: 1	10mm 400mI

A

A

B

B

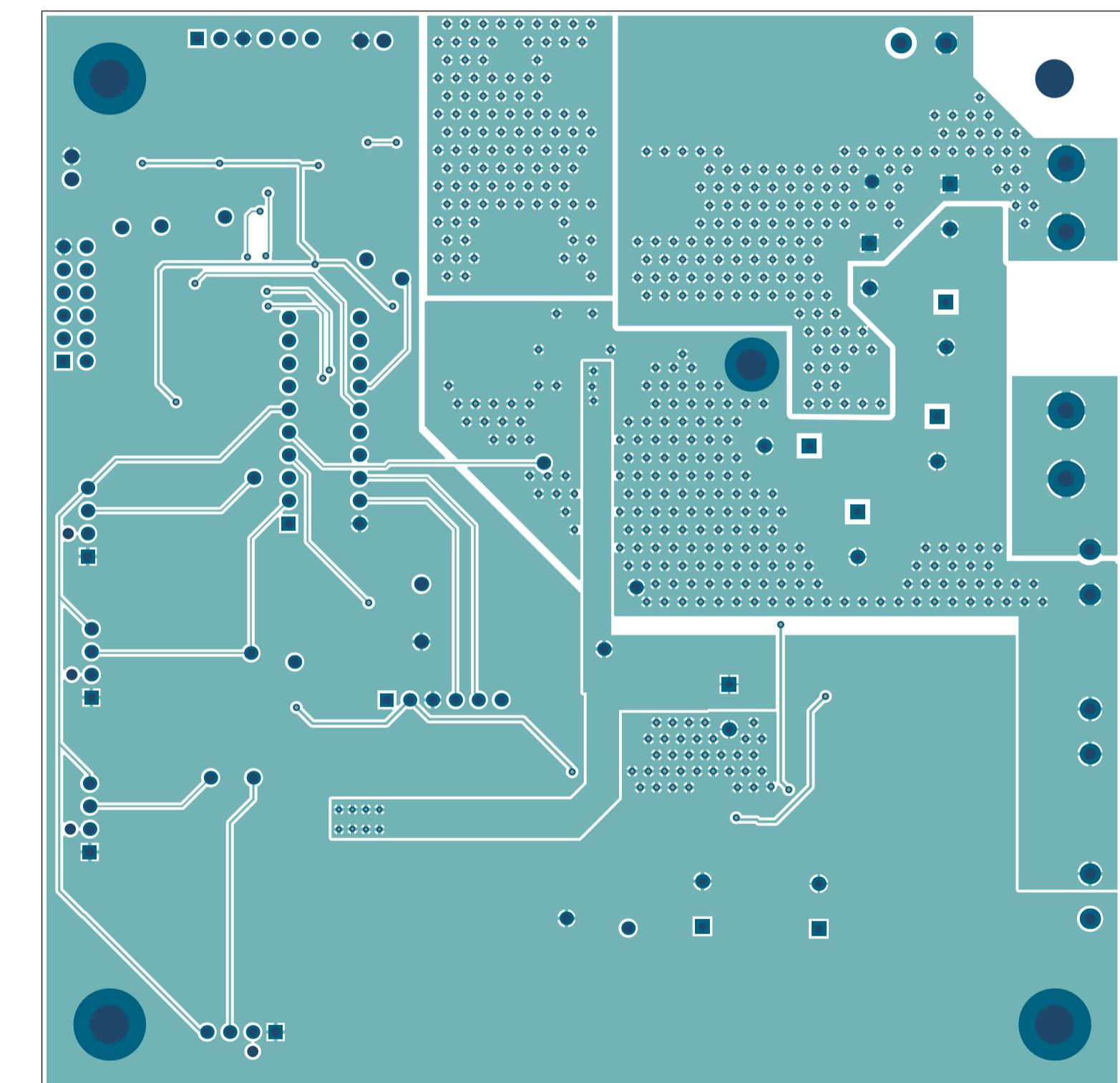
C

C

D

D

04-**-R1



TITLE:		PART NUMBER: **	
PCB DESIGNER:		GERBER FILE:	
Robert Perkel		'BOTTOM Copper'	
ENGINEER:		BOARD NUMBER:	DATE:
Robert Perkel		04-**	7/10/2022
PCB FILE NAME:		LAYER NAME:	REV:
171xx_Cold_Plate.PcbDoc		10mm	400mI

BOARD (MS)¹

A

A

B

B

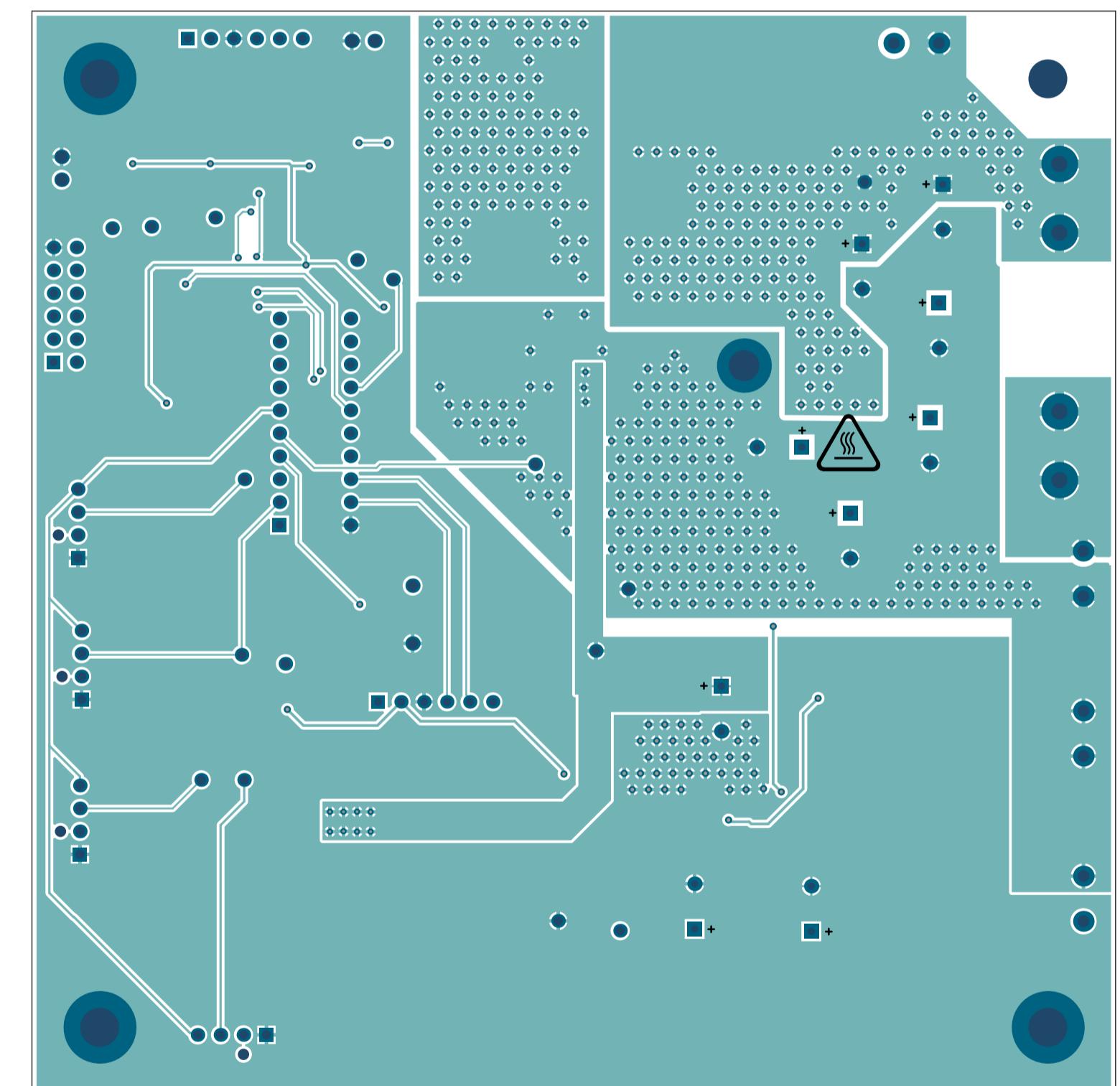
C

C

D

D

04-***-R1



TITLE:		PART NUMBER: **	
PCB DESIGNER:		GERBER FILE:	
Robert Perkel		'Bottom Copper & Silk'	
ENGINEER:		BOARD NUMBER:	DATE:
Robert Perkel		04-**	7/10/2022
PCB FILE NAME:		LAYER NAME:	REV:
171xx_Cold_Plate.PcbDoc		10mm	400mI

1

2

3

4

5

6

A

A

B

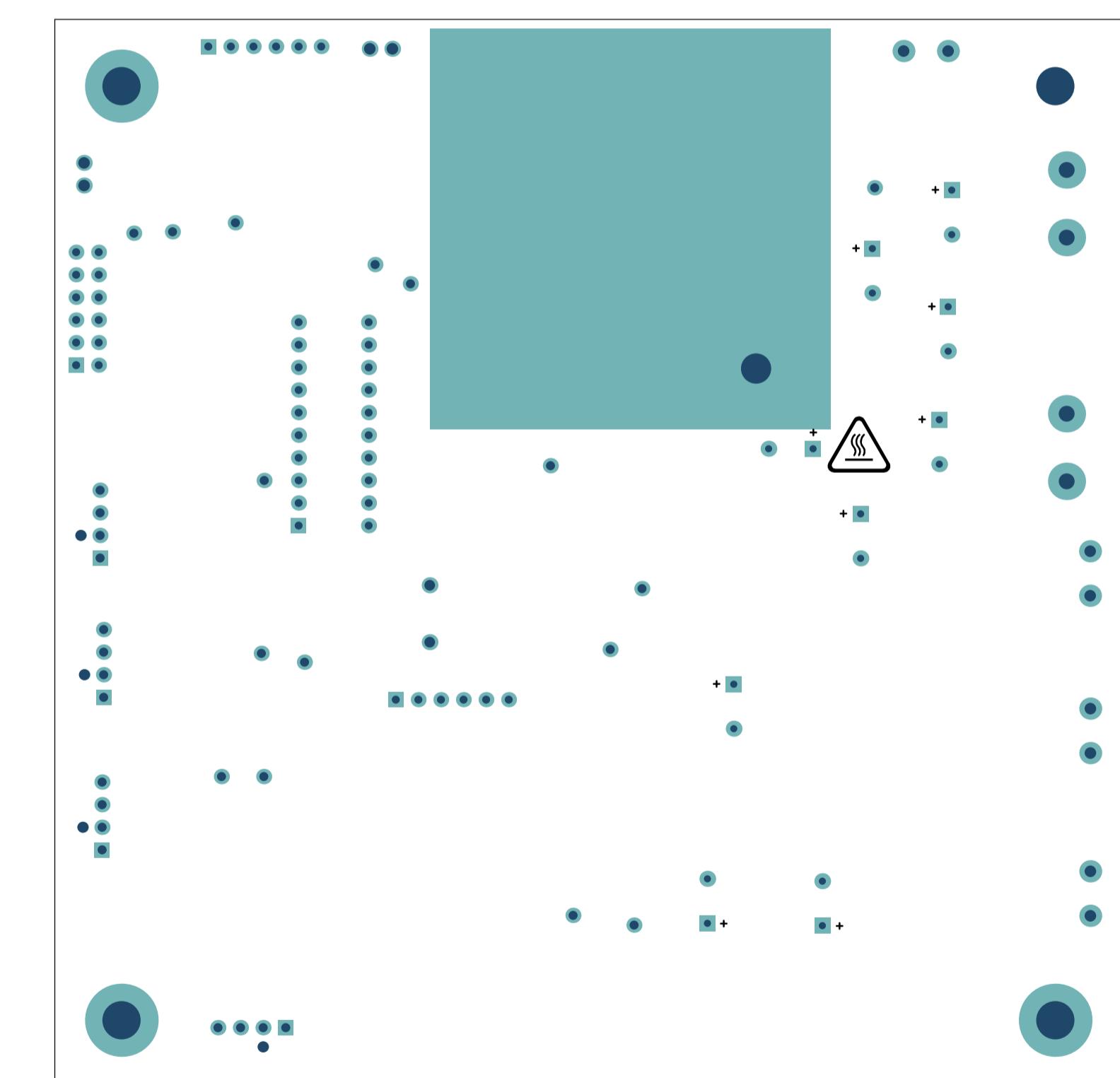
B

C

C

D

D



TITLE:		PART NUMBER: **	
Cold Plate Analog Control Board			
PCB DESIGNER:		GERBER FILE:	
Robert Perkel		'BOTTOM SILK'	
ENGINEER:		BOARD NUMBER:	DATE:
Robert Perkel		04-**	7/10/2022
PCB FILE NAME:		LAYER NAME:	REV:
171xx_Cold_Plate.PcbDoc			10mm 400mI