

Solutions to *The Art of Electronics 3rd Edition*

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# Solutions for Chapter 2

## Exercise 2.1

In order to solve this problem, many assumptions must be made. Different people may assume slightly different values for parameters. This is OK. What is important is making good assumptions and checking our conclusions to make sure they are reasonable.

To solve for the current in the LED, let us assume we know the LED is red, so it follows the red LED curve from Figure 2.8 in the book. Let us also assume the transistor is acting like a closed switch, so the collector voltage of Q1 is close to 0 V. Let us also assume the LED is ON, so its voltage is approximately  $V_{\text{LED}} = 2 \text{ V}$ . From the preceding assumptions, we can calculate that the LED current is

$$I_{\text{LED}} = \frac{3.3 \text{ V} - 2 \text{ V}}{330 \Omega} = \frac{1.3 \text{ V}}{330 \Omega} \approx 3.94 \text{ mA}$$

If we use Figure 2.8 (from the textbook) to check our numbers, we see that a current of 3.94 mA roughly correlates to an LED voltage of  $V_{\text{LED}} = 1.7 \text{ V}$ . We will run the same calculation again to reduce our error.

$$I_{\text{LED}}^* = \frac{3.3 \text{ V} - 1.7 \text{ V}}{330 \Omega} = \frac{1.6 \text{ V}}{330 \Omega} \approx \boxed{4.85 \text{ mA}}$$

In order to determine the minimum current gain required from our transistor, we must calculate the base current. Let us assume we know the base-emitter voltage  $V_{\text{BE}} = 0.6 \text{ V}$ . Therefore

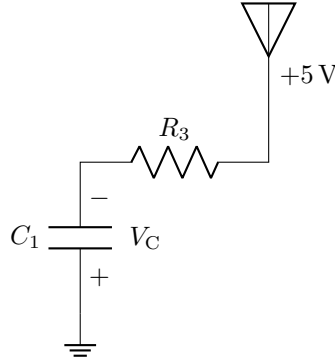
$$I_{\text{B}} = \frac{3.3 \text{ V} - 0.6 \text{ V}}{10 \text{ k}\Omega} = 270 \mu\text{A}$$

So the minimum current gain must be

$$\beta_{\text{min}} = \frac{I_{\text{LED}}^*}{I_{\text{B}}} \approx \frac{4.85 \text{ mA}}{270 \mu\text{A}} \approx \boxed{18.0}$$

## Exercise 2.2

When  $Q_1$  goes is brought in saturation, the base voltage of  $Q_2$  equals the opposite of the voltage on the capacitor  $C_1$  at  $t = 0 \text{ s}$ ,  $V_0 = 4.4 \text{ V}$  and  $Q_2$  is then cutoff.  $V_{\text{out}}$  will be equal to 5 V until  $Q_2$  is brought in saturation again. This happens when its base voltage gets higher or equal to the  $Q_2$  threshold voltage (0.6 V). As soon as  $Q_1$  is brought in saturation,  $C_1$  starts to discharge into the resistor  $R_3$  and the equivalent circuit, valid until  $Q_2$  is cutoff, is then:

Figure 1.1: Equivalent  $C_1$  discharging circuit.

The time evolution of the voltage across the capacitor  $C_1$  is given by:

$$V_C(t) = (V_0 - V_\infty) e^{-\frac{t}{R_3 C_1}} + V_\infty$$

where  $V_\infty$  is the steady-state voltage on the capacitor  $C_1$  and equals  $-5\text{ V}$ . Given the considerations above, we have that  $V_C(t = T_{\text{pulse}}) = -0.6\text{ V}$ . Solving for  $t$  gives:

$$T_{\text{pulse}} = -R_3 C_1 \ln \left( \frac{-0.6\text{ V} - V_\infty}{V_0 - V_\infty} \right) = \boxed{0.76 R_3 C_1 = 76\text{ }\mu\text{s}}$$

## Exercise 2.3

The output voltage is now influenced by  $R_5$  that goes in series with  $R_4$ , and by the  $V_{\text{BE}}$  of  $Q_3$  which is equal to  $0.6\text{ V}$  when the transistor is in saturation. Therefore:

$$V_{\text{out}} = \frac{R_5}{R_5 + R_4} (5\text{ V} - 0.6\text{ V}) + 0.6\text{ V} = \boxed{4.79\text{ V}}$$

The minimum value of  $\beta$  of  $Q_3$  can be obtained looking at the maximum value of the current flowing through the collector of  $Q_3$ ,  $I_c^{Q_3}$ . As soon as  $Q_1$  goes in saturation, the capacitor  $C_1$  starts to discharge and its current is given by  $C_1 dV_C/dt$ . With reference to the variables introduced in the previous exercise (2.2):

$$I_c^{Q_3}(t) = \frac{5\text{ V}}{R_2} - I_{C_1}(t) = \frac{5\text{ V}}{R_2} + C_1 \frac{1}{R_3 C_1} (V_0 - V_\infty) e^{-\frac{t}{R_3 C_1}}$$

Therefore:

$$\beta_{\min} = \frac{I_c^{Q_3}(t)|_{\max}}{I_b^{Q_3}} = \frac{I_c^{Q_3}(t=0\text{ s})}{I_b^{Q_3}} = \boxed{27}$$

Exercise 2.4 **TODO: write solution**

Exercise 2.5 **TODO: write solution**

Exercise 2.6 **TODO: write solution**

Exercise 2.7 **TODO: write solution**

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